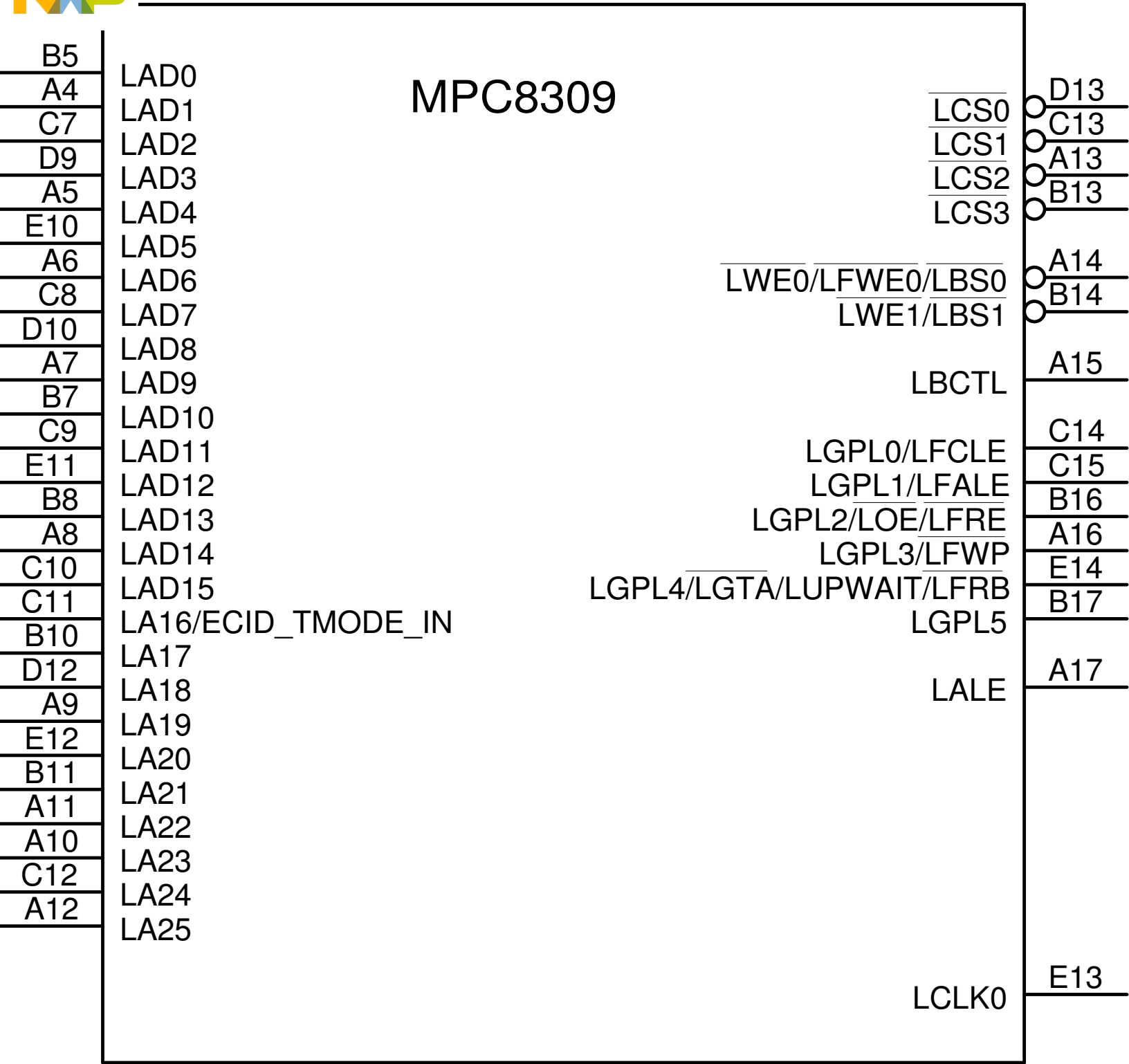


|     |            |         |            |     |
|-----|------------|---------|------------|-----|
| L3  | MEMC_MA0   | MPC8309 | MEMC_MDQ0  | U5  |
| L5  | MEMC_MA1   |         | MEMC_MDQ1  | AA1 |
| L2  | MEMC_MA2   |         | MEMC_MDQ2  | W3  |
| L1  | MEMC_MA3   |         | MEMC_MDQ3  | R5  |
| M3  | MEMC_MA4   |         | MEMC_MDQ4  | W2  |
| M4  | MEMC_MA5   |         | MEMC_MDQ5  | U3  |
| M1  | MEMC_MA6   |         | MEMC_MDQ6  | U2  |
| N1  | MEMC_MA7   |         | MEMC_MDQ7  | T3  |
| N2  | MEMC_MA8   |         | MEMC_MDQ8  | H3  |
| N3  | MEMC_MA9   |         | MEMC_MDQ9  | H4  |
| L4  | MEMC_MA10  |         | MEMC_MDQ10 | G3  |
| P2  | MEMC_MA11  |         | MEMC_MDQ11 | F3  |
| N4  | MEMC_MA12  |         | MEMC_MDQ12 | G5  |
| P1  | MEMC_MA13  |         | MEMC_MDQ13 | F4  |
| K2  |            |         | MEMC_MDQ14 | F5  |
| K3  | MEMC_MBA0  |         | MEMC_MDQ15 | E3  |
| N5  | MEMC_MBA1  |         | MEMC_MDQ16 | V4  |
|     | MEMC_MBA2  |         | MEMC_MDQ17 | Y2  |
| K1  |            |         | MEMC_MDQ18 | Y1  |
| J3  | MEMC_MRAS  |         | MEMC_MDQ19 | U4  |
| J1  | MEMC_MCAS  |         | MEMC_MDQ20 | V1  |
|     | MEMC_MWE   |         | MEMC_MDQ21 | R4  |
| J4  |            |         | MEMC_MDQ22 | U1  |
| K5  | MEMC_MCS0  |         | MEMC_MDQ23 | T2  |
|     | MEMC_MCS1  |         | MEMC_MDQ24 | J5  |
| P4  |            |         | MEMC_MDQ25 | G2  |
|     | MEMC_MCKE  |         | MEMC_MDQ26 | G1  |
| R1  |            |         | MEMC_MDQ27 | F1  |
| T1  | MEMC_MCK0  |         | MEMC_MDQ28 | E2  |
|     | MEMC_MCK0  |         | MEMC_MDQ29 | D2  |
| R3  |            |         | MEMC_MDQ30 | C2  |
| P3  | MEMC_MCK1  |         | MEMC_MDQ31 | C1  |
|     | MEMC_MCK1  |         |            |     |
| H1  |            |         |            | W1  |
| H2  | MEMC_MODT0 |         | MEMC_MDM0  | E1  |
|     | MEMC_MODT1 |         | MEMC_MDM1  | V3  |
| M6  |            |         | MEMC_MDM2  | D1  |
|     | MEMC_MVREF |         | MEMC_MDM3  | W5  |
| Y5  |            |         | MEMC_MDM8  |     |
| AA4 | MEMC_MECC0 |         |            | T5  |
| Y4  | MEMC_MECC1 |         | MEMC_MDQS0 | H5  |
| AA3 | MEMC_MECC2 |         | MEMC_MDQS1 | P5  |
| AC2 | MEMC_MECC3 |         | MEMC_MDQS2 | E5  |
| AB2 | MEMC_MECC4 |         | MEMC_MDQS3 | V5  |
| Y3  | MEMC_MECC5 |         | MEMC_MDQS8 |     |
| AB1 | MEMC_MECC6 |         |            |     |
|     | MEMC_MECC7 |         |            |     |



<Value>

|     |                                     |                                      |                               |     |
|-----|-------------------------------------|--------------------------------------|-------------------------------|-----|
| B23 | PCI_AD0/BOOT_ROM_ADDR[2]            | MPC8309                              | PCI_C_BE0/BOOT_ROM_RDATA[21]  | L20 |
| C21 | PCI_AD1/BOOT_ROM_ADDR[3]            |                                      | PCI_C_BE1/BOOT_ROM_RDATA[22]  | L23 |
| E20 | PCI_AD2/BOOT_ROM_ADDR[4]            |                                      | PCI_C_BE2/BOOT_ROM_RDATA[23]  | L22 |
| G19 | PCI_AD3/BOOT_ROM_ADDR[5]            |                                      | PCI_C_BE3/BOOT_ROM_RDATA[24]  | L21 |
| C23 | PCI_AD4/BOOT_ROM_ADDR[6]            |                                      |                               |     |
| H19 | PCI_AD5/BOOT_ROM_ADDR[7]            |                                      | PCI_PAR/BOOT_ROM_RDATA[25]    | M19 |
| D21 | PCI_AD6/CE_PIO_0/BOOT_ROM_ADDR[8]   |                                      | PCI_FRAME/BOOT_ROM_RDATA[26]  | M20 |
| F20 | PCI_AD7/BOOT_ROM_ADDR[9]            |                                      | PCI_TRDY/BOOT_ROM_RDATA[27]   | M23 |
| E21 | PCI_AD8/BOOT_ROM_ADDR[10]           |                                      | PCI_IRDY/BOOT_ROM_RDATA[28]   | M21 |
| H20 | PCI_AD9/BOOT_ROM_ADDR[11]           |                                      | PCI_STOP/BOOT_ROM_RDATA[29]   | N23 |
| D22 | PCI_AD10/BOOT_ROM_ADDR[12]          |                                      | PCI_DEVSEL/BOOT_ROM_RDATA[30] | N22 |
| D23 | PCI_AD11/BOOT_ROM_RDATA[0]          |                                      | PCI_IDSEL/BOOT_ROM_RDATA[31]  | N21 |
| J19 | PCI_AD12/BOOT_ROM_RDATA[1]          |                                      | PCI_SERR/BOOT_ROM_MOD_EN      | N19 |
| F21 | PCI_AD13/BOOT_ROM_RDATA[2]          |                                      | PCI_PERR/BOOT_ROM_RWB         | P20 |
| G21 | PCI_AD14/BOOT_ROM_RDATA[3]          |                                      |                               |     |
| E22 | PCI_AD15/BOOT_ROM_RDATA[4]          |                                      | PCI_REQ0/BOOT_ROM_XFR_WAIT    | P21 |
| E23 | PCI_AD16/BOOT_ROM_RDATA[5]          | PCI_REQ1/CPCI_HS_ES/BOOT_ROM_XFR_ERR |                               | P22 |
| J20 | PCI_AD17/BOOT_ROM_RDATA[6]          |                                      | PCI_REQ2                      | T22 |
| F23 | PCI_AD18/BOOT_ROM_RDATA[7]          |                                      |                               |     |
| G23 | PCI_AD19/BOOT_ROM_RDATA[8]          |                                      | PCI_GNT0                      | T21 |
| K19 | PCI_AD20/BOOT_ROM_RDATA[9]          |                                      | PCI_GNT1/CPCI_HS_LED          | U22 |
| H21 | PCI_AD21/BOOT_ROM_RDATA[10]         |                                      | PCI_GNT2/CPCI_HS_ENUM         | U21 |
| L19 | PCI_AD22/BOOT_ROM_RDATA[11]         |                                      |                               |     |
| G22 | PCI_AD23/BOOT_ROM_RDATA[12]         |                                      |                               | V21 |
| H23 | PCI_AD24/BOOT_ROM_RDATA[13]         |                                      |                               |     |
| J21 | PCI_AD25/BOOT_ROM_RDATA[14]         |                                      |                               | T19 |
| H22 | PCI_AD26/UC5_URM/BOOT_ROM_RDATA[15] |                                      | PCI_CLK0                      | U19 |
| J23 | PCI_AD27/BOOT_ROM_RDATA[16]         |                                      | PCI_CLK1                      | R19 |
| K18 | PCI_AD28/UC7_URM/BOOT_ROM_RDATA[17] |                                      | PCI_CLK2                      |     |
| K21 | PCI_AD29/BOOT_ROM_RDATA[18]         |                                      |                               | B22 |
| K22 | PCI_AD30/BOOT_ROM_RDATA[19]         |                                      | PCI_INTA                      | F19 |
| K23 | PCI_AD31/BOOT_ROM_RDATA[20]         |                                      | PCI_RESET_OUT                 |     |
|     |                                     |                                      |                               |     |
|     |                                     |                                      | PCI_SYNC_IN                   | T23 |
|     |                                     |                                      | PCI_SYNC_OUT                  | R20 |

<Value>



|                              |                                                                                                                                                                               |         |                                                                                                                                                  |                             |
|------------------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|---------|--------------------------------------------------------------------------------------------------------------------------------------------------|-----------------------------|
| W16<br>AC22<br>AA18          | FEC1_COL/GTM1_TIN1/GPIO_16<br>FEC1_CRS/GTM1_TGATE1/GPIO_17                                                                                                                    | MPC8309 | FEC3_COL/GPIO_48<br>FEC3_CRS/GPIO_49                                                                                                             | AC12<br>W11                 |
| W16<br>AC22<br>AA18          | FEC1_RX_CLK[CLK9]/GPIO_18<br>FEC1_RX_DV/GTM1_TIN2/GPIO_19/PLLCZ_CORE_CLKIN<br>FEC1_RX_ER/GTM1_TGATE2/GPIO_20/JTAG_BISE                                                        |         | FEC3_RX_CLK[CLK11]/GPIO_50<br>FEC3_RX_DV/FEC1_TMR_TX_ESFD/GPIO_51<br>FEC3_RX_ER/FEC1_TMR_RX_ESFD/GPIO_52                                         | W12<br>AA12<br>AB11         |
| AB20<br>Y17<br>AB19<br>AC21  | FEC1_RXD0/GPIO_21/JTAG_PRPGPS<br>FEC1_RXD1/GTM1_TIN3/GPIO_22/JTAG_BISR_TDO_EN<br>FEC1_RXD2/GTM1_TGATE3/GPIO_23/TPR_SYS_AAD[0]<br>FEC1_RXD3/GPIO_24/TPR_SYS_AAD[1]             |         | FEC3_RXD0/FEC3_TMR_TX_ESFD/GPIO_53<br>FEC3_RXD1/FEC3_TMR_RX_ESFD/GPIO_54<br>FEC3_RXD2/TSEC_TMR_TRIG1/GPIO_55<br>FEC3_RXD3/TSEC_TMR_TRIG2/GPIO_56 | AA11<br>AC11<br>Y11<br>AB10 |
| W15<br>AC19<br>AC20          | FEC1_TX_CLK[CLK10]/GTM1_TIN4/GPIO_25/TPR_SYS_AAD[2]<br>FEC1_TX_EN/GTM1_TGATE4/GPIO_26/TPR_SYS_AAD[3]<br>FEC1_TX_ER/GTM1_TOUT4/GPIO_27/TPR_SYS_AAD[4]                          |         | FEC3_TX_CLK[CLK12]/TSEC_TMR_CLK/GPIO_57<br>FEC3_TX_EN/TSEC_TMR_GCLK/GPIO_58<br>FEC3_TX_ER/TSEC_TMR_PP1/GPIO_59                                   | AC10<br>AA10<br>AC8         |
| AA17<br>AC18<br>AA16<br>AB17 | FEC1_TXD0/GTM1_TOUT1/GPIO_28/TPR_SYS_AAD[5]<br>FEC1_TXD1/GTM1_TOUT2/GPIO_29/TPR_SYS_AAD[6]<br>FEC1_TXD2/GTM1_TOUT3/GPIO_30/TPR_SYS_AAD[7]<br>FEC1_TXD3/GPIO_31/TPR_SYS_AAD[8] |         | FEC3_TXD0/TSEC_TMR_PP2/GPIO_60<br>FEC3_TXD1/TSEC_TMR_PP3/GPIO_61<br>FEC3_TXD2/TSEC_TMR_ALARM1/GPIO_62<br>FEC3_TXD3/TSEC_TMR_ALARM2/GPIO_63       | AB8<br>AA9<br>AA8<br>AC7    |
| Y15<br>AC17                  | FEC2_COL/GTM2_TIN1/GPIO_32/TPR_SYS_AAD[9]<br>FEC2_CRS/GTM2_TGATE1/GPIO_33/TPR_SYS_AAD[10]                                                                                     |         | FEC_MDC<br>FEC_MDIO                                                                                                                              | W18<br>W17                  |
| W14<br>AB16<br>Y14           | FEC2_RX_CLK[CLK7]/GPIO_34/TPR_SYS_AAD[11]<br>FEC2_RX_DV/GTM2_TIN2/GPIO_35/TPR_SYS_AAD[12]<br>FEC2_RX_ER/GTM2_TGATE2/GPIO_36/TPR_SYS_AAD[13]                                   |         |                                                                                                                                                  |                             |
| AA15<br>AC15<br>AC16<br>AA14 | FEC2_RXD0/GPIO_37/TPR_SYS_AAD[14]<br>FEC2_RXD1/GTM2_TIN3/GPIO_38/TPR_SYS_AAD[15]<br>FEC2_RXD2/GTM2_TGATE3/GPIO_39/TPR_SYS_SYNC<br>FEC2_RXD3/GPIO_40/TPR_SYS_DACK              |         |                                                                                                                                                  |                             |
| W13<br>AB14<br>AC14          | FEC2_TX_CLK[CLK8]/GTM2_TIN4/GPIO_41<br>FEC2_TX_EN/GTM2_TGATE4/GPIO_42/CLOCK_XLB_CLOCK_OUT<br>FEC2_TX_ER/GTM2_TOUT4/GPIO_43                                                    |         |                                                                                                                                                  |                             |
| Y12<br>AA13<br>AB13<br>AC13  | FEC2_TXD0/GTM2_TOUT1/GPIO_44/PD_XLB2MG_DDR_CLOCK<br>FEC2_TXD1/GTM2_TOUT2/GPIO_45<br>FEC2_TXD2/GTM2_TOUT3/GPIO_46<br>FEC2_TXD3/GPIO_47                                         |         |                                                                                                                                                  |                             |
|                              |                                                                                                                                                                               |         |                                                                                                                                                  |                             |
|                              |                                                                                                                                                                               |         |                                                                                                                                                  |                             |
|                              |                                                                                                                                                                               |         |                                                                                                                                                  |                             |
|                              |                                                                                                                                                                               |         |                                                                                                                                                  |                             |
|                              |                                                                                                                                                                               |         |                                                                                                                                                  |                             |

<Value>



| MPC8309 |                        |                                                     |      |
|---------|------------------------|-----------------------------------------------------|------|
| AC6     | UART1_SOUT1            | GPIO_0/SD_CLK/MSRCID0(DDR_ID)                       | E4   |
| W10     | UART1_SIN1             | GPIO_1/SD_CMD/MSRCID1(DDR_ID)                       | E6   |
| Y9      | UART1_SOUT2/UART1_RTS1 | GPIO_2/SD_CD/MSRCID2(DDR_ID)                        | D3   |
|         | UART1_SIN2/UART1_CTS1  | GPIO_3/SD_WP/MSRCID3(DDR_ID)                        | E7   |
| A20     |                        | GPIO_4/SD_DAT0/MSRCID4(DDR_ID)                      | D4   |
| B20     | IIC_SDA1               | GPIO_5/SD_DAT1/MDVAL(DDR_ID)                        | C4   |
| D19     | IIC_SCL1               | GPIO_6/SD_DAT2/QE_EXT_REQ_3                         | B2   |
| C20     | IIC_SDA2/CKSTOP_OUT    | GPIO_7/SD_DAT3/QE_EXT_REQ_1                         | B3   |
|         | IIC_SCL2/CKSTOP_IN     | GPIO_8/RXCAN1/LSRCID0/LCS4                          | C16  |
| A21     |                        | GPIO_9/TXCAN1/LSRCID1/LCS5                          | C17  |
| A22     | IRQ0/MCP_IN            | GPIO_10/RXCAN2/LSRCID2/LCS6                         | E15  |
| E18     | IRQ1/MCP_OUT           | GPIO_11/TXCAN2/LSRCID3/LCS7                         | A18  |
| E19     | IRQ2/CKSTOP_IN         | GPIO_12/RXCAN3/LSRCID4/LCLK1                        | D15  |
|         | IRQ3/CKSTOP_OUT/INTA   | GPIO_13/TXCAN3/LDVAL                                | C18  |
| B19     |                        | GPIO_14/RXCAN4                                      | D16  |
| E16     | SPIMOSI                | GPIO_15/TXCAN4                                      | C19  |
| E17     | SPIMISO                |                                                     |      |
| A19     | SPICLK                 | USBDR_PWRFAULT/CE_PIO_1                             | AA6  |
| D18     | SPISEL                 | USBDR_CLK/UART2_SIN2/UART2_CTS1                     | AC9  |
|         | SPISEL_BOOT            | USBDR_DIR/URM_TRIG                                  | AA7  |
| A2      |                        | USBDR_NXT/UART2_SIN1/UC1_URM/QE_EXT_REQ_4           | AC5  |
| C5      | TCK                    | USBDR_TXDRXD0/GPIO_32/QE_TRB_0                      | Y6   |
| A3      | TDI                    | USBDR_TXDRXD1/GPIO_33/QE_TRB_1                      | W9   |
| D7      | TDO                    | USBDR_TXDRXD2/GPIO_34/QE_BRG_1                      | AB5  |
| E9      | TMS                    | USBDR_TXDRXD3/GPIO_35/QE_BRG_2                      | AA5  |
|         | TRST                   | USBDR_TXDRXD4/GPIO_36/QE_BRG_3                      | Y8   |
| C6      |                        | USBDR_TXDRXD5/GPIO_37/QE_BRG_4                      | AC4  |
|         | TEST_MODE              | USBDR_TXDRXD6/GPIO_38/QE_BRG_9                      | AC3  |
|         |                        | USBDR_TXDRXD7/GPIO_39/QE_BRG_11                     | AB3  |
| W23     | HRESET                 | USBDR_PCTL0/UART2_SOUT1/UC2_URM/LB_POR_CFG_BOOT_ECC | W8   |
| W22     | PORESET                | USBDR_PCTL1/UART2_SOUT2/UART2_RTS1/LB_POR_BOOT_ERR  | W7   |
| D6      |                        | USBDR_STP/UC3_URM/QE_EXT_REQ_2                      | W6   |
|         | QUIESCE                |                                                     |      |
| E8      | THERM0                 | HDLC1_TXCLK[CLK16]/GPIO_0/QE_BRG_5/TDM1_TCK[CLK4]   | AA20 |
| R22     |                        | HDLC1_RXCLK[CLK15]/GPIO_1/TDM1_RCK[CLK3]            | AA21 |
| R23     | QE_CLK_IN              | HDLC1_TXD/GPIO_2/TDM1_TD/CFG_RESET_SOURCE[0]        | AB22 |
|         | SYS_CLK_IN             | HDLC1_RXD/GPIO_3/TDM1_RD                            | AB23 |
|         |                        | HDLC1_CD/GPIO_4/TDM1_TFS                            | W19  |
| P23     |                        | HDLC1_CTS/GPIO_5/TDM1_RFS                           | V19  |
|         | SYS_XTAL_IN            | HDLC1_RTS/GPIO_6/TDM1_STROBE/CFG_RESET_SOURCE[1]    | AA23 |
| P19     | SYS_XTAL_OUT           | HDLC2_TXCLK[CLK13]/GPIO_16/QE_BRG_7/TDM2_TCK[CLK6]  | Y20  |
|         |                        | HDLC2_RXCLK[CLK14]/GPIO_17/TDM2_RCK[CLK5]/QE_BRG_8  | Y22  |
|         |                        | HDLC2_TXD/GPIO_18/TDM2_TD/CFG_RESET_SOURCE[2]       | W20  |
| U23     | CFG_CLKIN_DIV          | HDLC2_RXD/GPIO_19/TDM2_RD                           | W21  |
| V23     | RTC_PIT_CLOCK          | HDLC2_CD/GPIO_20/TDM2_TFS                           | V20  |
|         |                        | HDLC2_CTS/GPIO_21/TDM2_RFS                          | Y23  |
|         |                        | HDLC2_RTS/GPIO_22/TDM2_STROBE/CFG_RESET_SOURCE[3]   | U20  |
|         |                        |                                                     | A23  |
|         |                        |                                                     | NC   |

<Value>



F

|     |        |        |     |
|-----|--------|--------|-----|
| M16 | AVDD1  | NVDD1  | F7  |
| N8  | AVDD2  | NVDD2  | F8  |
|     | AVDD3  | NVDD3  | F9  |
| F6  |        | NVDD4  | F10 |
| G6  | GVDD1  | NVDD5  | F11 |
| H6  | GVDD2  | NVDD6  | F12 |
| J6  | GVDD3  | NVDD7  | F13 |
| K6  | GVDD4  | NVDD8  | F14 |
| L6  | GVDD5  | NVDD9  | F15 |
| N6  | GVDD6  | NVDD10 | F16 |
| P6  | GVDD7  | NVDD11 | F17 |
| R6  | GVDD8  | NVDD12 | F18 |
| T6  | GVDD9  | NVDD13 | G18 |
| U6  | GVDD10 | NVDD14 | H18 |
| V6  | GVDD11 | NVDD15 | J18 |
| V7  | GVDD12 | NVDD16 | L18 |
|     | GVDD13 | NVDD17 | M18 |
| H8  |        | NVDD18 | N18 |
| H9  | VDD1   | NVDD19 | P18 |
| H10 | VDD2   | NVDD20 | R18 |
| H11 | VDD3   | NVDD21 | T18 |
| H12 | VDD4   | NVDD22 | U18 |
| M8  | VDD5   | NVDD23 | V8  |
| H13 | VDD6   | NVDD24 | V9  |
| N16 | VDD7   | NVDD25 | V10 |
| H14 | VDD8   | NVDD26 | V11 |
| H15 | VDD9   | NVDD27 | V12 |
| H16 | VDD10  | NVDD28 | V13 |
| P16 | VDD11  | NVDD29 | V14 |
| P8  | VDD12  | NVDD30 | V15 |
| L8  | VDD13  | NVDD31 | V16 |
| K16 | VDD14  | NVDD32 | V17 |
| J16 | VDD15  | NVDD33 | V18 |
| K8  | VDD16  |        |     |
| J8  | VDD17  |        |     |
| R8  | VDD18  |        |     |
| T16 | VDD19  |        |     |
| R16 | VDD20  |        |     |
| T8  | VDD21  |        |     |
| T9  | VDD22  |        |     |
| T11 | VDD23  |        |     |
| T10 | VDD24  |        |     |
| T12 | VDD25  |        |     |
| T13 | VDD26  |        |     |
| T14 | VDD27  |        |     |
| T15 | VDD28  |        |     |
|     | VDD29  |        |     |

|      | 1     |         |        |      |
|------|-------|---------|--------|------|
| C3   | VSS2  | MPC8309 | VSS52  | L10  |
| F22  | VSS3  |         | VSS53  | M10  |
| J14  | VSS4  |         | VSS54  | N11  |
| K14  | VSS5  |         | VSS55  | P12  |
| M15  | VSS6  |         | VSS56  | R13  |
| L15  | VSS7  |         | VSS57  | Y7   |
| N20  | VSS8  |         | VSS58  | AB6  |
| R9   | VSS9  |         | VSS59  | B12  |
| Y21  | VSS10 |         | VSS60  | D14  |
| T20  | VSS11 |         | VSS61  | J10  |
| AB21 | VSS12 |         | VSS62  | K10  |
| B1   | VSS13 |         | VSS63  | L11  |
| C22  | VSS14 |         | VSS64  | M11  |
| G4   | VSS15 |         | VSS65  | P13  |
| K15  | VSS16 |         | VSS66  | N12  |
| J15  | VSS17 |         | VSS67  | R14  |
| M2   | VSS18 |         | VSS68  | Y10  |
| M22  | VSS19 |         | VSS69  | AB9  |
| P9   | VSS20 |         | VSS70  | B15  |
| R10  | VSS21 |         | VSS71  | D17  |
| V2   | VSS22 |         | VSS72  | J11  |
| AA2  | VSS23 |         | VSS73  | K11  |
| AC1  | VSS24 |         | VSS74  | D20  |
| B4   | VSS25 |         | VSS75  | B18  |
| D5   | VSS26 |         | VSS76  | J12  |
| G20  | VSS27 |         | VSS77  | K12  |
| J22  | VSS28 |         | VSS78  | L13  |
| K20  | VSS29 |         | VSS79  | L12  |
| M5   | VSS30 |         | VSS80  | L14  |
| N9   | VSS31 |         | VSS81  | K13  |
| P10  | VSS32 |         | VSS82  | J13  |
| R11  | VSS33 |         | VSS83  | F2   |
| V22  | VSS34 |         | VSS84  | B21  |
| AA22 | VSS35 |         | VSS85  | M14  |
| AC23 | VSS36 |         | VSS86  | M13  |
| B6   | VSS37 |         | VSS87  | M12  |
| D8   | VSS38 |         | VSS88  | Y19  |
| J2   | VSS39 |         | VSS89  | Y16  |
| K4   | VSS40 |         | VSS90  | AB15 |
| M9   | VSS41 |         | VSS91  | AB12 |
| L9   | VSS42 |         | VSS92  | Y13  |
| N10  | VSS43 |         | VSS93  | N13  |
| P11  | VSS44 |         | VSS94  | N14  |
| R12  | VSS45 |         | VSS95  | N15  |
| W4   | VSS46 |         | VSS96  | P14  |
| AB4  | VSS47 |         | VSS97  | P15  |
| D11  | VSS48 |         | VSS98  | R2   |
| B9   | VSS49 |         | VSS99  | AB18 |
| J9   | VSS50 |         | VSS100 | R15  |
| K9   | VSS51 |         | VSS101 | R21  |
|      |       |         | VSS102 | T4   |