



U?A			
AH3	TRST		
AG5	TCK		
AE6	TDI		B20
AF5	TDO	D0	A18
AJ3	TMS	D1	A16
		D2	A13
AH4	CLKIN1	D3	E12
AE11	CLKIN2	D4	D9
		D5	A6
G1	A0	D6	B5
H5	A1	D7	A20
H2	A2	D8	E17
H1	A3	D9	B15
J5	A4	D10	B13
J4	A5	D11	A11
J3	A6	D12	E9
J2	A7	D13	B7
J1	A8	D14	B4
K4	A9	D15	D19
K3	A10	D16	D17
K2	A11	D17	D15
K1	A12	D18	C13
L5	A13	D19	B11
L4	A14	D20	A8
L3	A15	D21	A5
L2	A16	D22	C5
L1	A17	D23	C19
M5	A18	D24	C17
N5	A19	D25	C15
N4	A20	D26	D13
N3	A21	D27	C11
N2	A22	D28	B8
N1	A23	D29	A4
P4	A24	D30	E6
P3	A25	D31	E18
P2	A26	D32	B17
P1	A27	D33	A15
R1	A28	D34	A12
R3	A29	D35	D11
R5	A30	D36	C8
R4	A31	D37	E7
		D38	A3
E29	CS9	D39	D18
D29	CS8	D40	A17
G25	CS7	D41	A14
F28	CS6	D42	B12
F27	CS5	D43	A10
F26	CS4	D44	D8
E28	CS3	D45	B6
E27	CS2	D46	C4
C29	CS1	D47	C18
F25	CS0	D48	E16
		D49	B14
F2	TT4	D50	C12
G2	TT3	D51	B10
G3	TT2	D52	A7
G4	TT1	D53	C6
F1	TT0	D54	D5
		D55	B18
F5	TSIZ3	D56	B16
D2	TSIZ2	D57	E14
E4	TSIZ1	D58	D12
C1	TSIZ0	D59	C10
		D60	E8
E3	TS	D61	D6
AB4	TRIS	D62	C2
V5	TEA	D63	
D3	TBST		
C22	TA		

<Value>



U?B

		IRQ0/NMI_OUT	T1
		L2_HIT/IRQ4	Y4
		CPU_BG/BADDR31/IRQ5/CINT	U4
H29	LCL_D0/AD0		
J29	LCL_D1/AD1	CPU_DBG	R2
J28	LCL_D2/AD2	CPU_BR	Y3
J27	LCL_D3/AD3		
J26	LCL_D4/AD4	ARTRY	E1
J25	LCL_D5/AD5	AACK	F3
K25	LCL_D6/AD6		
L29	LCL_D7/AD7	BR	W5
L27	LCL_D8/AD8	BCTL0	A27
L26	LCL_D9/AD9		
L25	LCL_D10/AD10	ABB/IRQ2	E2
M29	LCL_D11/AD11	BADDR27	T5
M28	LCL_D12/AD12	BADDR28	U1
M27	LCL_D13/AD13	CI/BADDR29/IRQ2	U2
M26	LCL_D14/AD14		
N29	LCL_D15/AD15	WT/BADDR30/IRQ3	U3
T25	LCL_D16/AD16	DBB/IRQ3	V2
U27	LCL_D17/AD17	DBG	V1
U26	LCL_D18/AD18	ALE	T2
U25	LCL_D19/AD19	BG	F4
V29	LCL_D20/AD20		
V28	LCL_D21/AD21	CS10/BCTL1	F29
V27	LCL_D22/AD22	CS11/AP0	G28
V26	LCL_D23/AD23		
W27	LCL_D24/AD24	LWE0/LSDDQM0/LBS0/PCI_CFG0	H28
W26	LCL_D25/AD25	LWE1/LSDDQM1/LBS1/PCI_CFG1	H27
W25	LCL_D26/AD26	LWE2/LSDDQM2/LBS2/PCI_CFG2	H26
Y29	LCL_D27/AD27	LWE3/LSDDQM3/LBS3/PCI_CFG3	G29
Y28	LCL_D28/AD28		
Y25	LCL_D29/AD29	LCL_DP0/C0/BE0	L28
AA29	LCL_D30/AD30	LCL_DP1/C1/BE1	N28
AA28	LCL_D31/AD31	LCL_DP2/C2/BE2	T28
		LCL_DP3/C3/BE3	W28
		RSTCONF	AJ4
		SRESET	AF6
		QREQ	AA3

<Value>

U?C			
AC29	PA0/RESTART1/DREQ3/FCC2_UTM_TXADDR2		
AC25	PA1/REJECT1/DONE3/FCC2_UTM_TXADDR1		
AE28	PA2/CLK20/DACK3/FCC2_UTM_TXADDR0	GBL/IRQ1	W1
AG29	PA3/CLK19/DACK4/L1RXD1A2/FCC2_UTM_RXADDR0	HRESET	AH5
AG28	PA4/REJECT2/DONE4/FCC2_UTM_RXADDR1		
AG26	PA5/RESTART2/DREQ4/FCC2_UTM_RXADDR2/FCC1_UT_RXPRTY		
AE24	PA6/FCC2_RXADDR3/L1RSYNCA1		
AH25	PA7/SMSYN2/FCC2_TXADDR3/L1TSYNCA1/L1GNTA1	DP0/RSRV/EXT_BR2	B22
AF23	PA8/SMRXD2/FCC2_TXADDR4/L1RXD0A1/L1RXDA1		
AH23	PA9/SMTXD2/L1TXD0A1		
AE22	PA10/MSNUM5/FCC1_UT8_RXD0/FCC1_UT16_RXD8	IRQ1/DP1/EXT_BG2	A22
AH22	PA11/MSNUM4/FCC1_UT8_RXD1/FCC1_UT16_RXD9	IRQ2/DP2/TLBISYNC/EXT_DBG2	E21
AJ21	PA12/MSNUM3/FCC1_UT8_RXD2/FCC1_UT16_RXD10	IRQ3/DP3/CKSTP_OUT/EXT_BR3	D21
AH20	PA13/MSNUM2/FCC1_UT8_RXD3/FCC1_UT16_RXD11	IRQ4/DP4/CORE_SRESET/EXT_BG3	C21
AG19	PA14/FCC1_MII_HDLC_RXD3/FCC1_UT8_RXD4/FCC1_UT16_RXD12	IRQ5/CINT/DP5/TBEN/EXT_DBG3	B21
AF18	PA15/FCC1_MII_HDLC_RXD2/FCC1_UT8_RXD5/FCC1_UT16_RXD13	IRQ6/DP6/CSE0	A21
AF17	PA16/FCC1_MII_HDLC_RXD1/FCC1_RMII_RXD1/FCC1_UT8_RXD6/FCC1_UT16_RXD14	IRQ7/DP7/CSE1	E20
AE16	PA17/FCC1_MII_HDLC_RXD0/FCC1_MII_TRAN_RXD/FCC1_RMII_RXD0/FCC1_UT8_RXD7/FCC1_UT16_RXD15	IRQ7/INT_OUT/APE	D1
AJ16	PA18/FCC1_MII_HDLC_TXD0/FCC1_MII_TRAN_TXD/FCC1_RMII_TXD0/FCC1_UT8_TXD7/FCC1_UT16_TXD15		
AG15	PA19/FCC1_MII_HDLC_TXD1/FCC1_RMII_TXD1/FCC1_UT8_TXD6/FCC1_UT16_TXD14		
AJ13	PA20/FCC1_MII_HDLC_TXD2/FCC1_UT8_TXD5/FCC1_UT16_TXD13	LWR	D28
AE13	PA21/FCC1_MII_HDLC_TXD3/FCC1_UT8_TXD4/FCC1_UT16_TXD12		
AF12	PA22/FCC1_UT8_TXD3/FCC1_UT16_TXD11		
AG11	PA23/FCC1_UT8_TXD2/FCC1_UT16_TXD10		
AH9	PA24/MSNUM1/FCC1_UT8_TXD1/FCC1_UT16_TXD9	PCI_MODE	AF25
AJ8	PA25/MSNUM0/FCC1_UT8_TXD0/FCC1_UT16_TXD8		
AH7	PA26/FCC1_RMII_RX_ER/FCC1_UTM_RXCLAV/FCC1_UTS_RXCLAV	LSDA10/LGPL0/PCI_MODCKH0	D27
AF7	PA27/FCC1_MII_RX_DV/FCC1_RMII_CRS_DV/FCC1_UT_RXSOC	LSDWE/LGPL1/PCI_MODCKH1	C28
AD5	PA28/FCC1_MII_TX_EN/FCC1_RMII_TX_EN/FCC1_UTM_RXENB/FCC1_UTS_RXENB	LOE/LSDRAS/LGPL2/PCI_MODCKH2	E26
AF1	PA29/FCC1_MII_TX_ER/FCC1_UT_TXSOC	LSDCAS/LGPL3/PCI_MODCKH3	D25
AD3	PA30/FCC1_MII_CRS/FCC1_RTS/FCC1_UTM_TXCLAV/FCC1_UTS_TXCLAV	LGTA/LUPMWAIT/LGPL4/LPBS	C26
AB5	PA31/FCC1_MII_COL/FCC1_UTM_TXENB/FCC1_UTS_TXENB	LGPL5/LSDAMUX/PCI_MODCK	B27
AD28	PB4/FCC3_MII_HDLC_TXD3/L1RSYNCA2/FCC3_RTS/FCC2_UT8_RXD0		
AD26	PB5/FCC3_MII_HDLC_TXD2/L1TSYNCA2/L1GNTA2/FCC2_UT8_RXD1	PSDA10/PGPL0	E23
AD25	PB6/FCC3_MII_HDLC_TXD1/FCC3_RMII_TXD1/L1RXDA2/L1RXD0A2/FCC2_UT8_RXD2	PSDWE/PGPL1	B24
AE26	PB7/FCC3_MII_HDLC_TXD0/FCC3_RMII_TXD0/FCC3_TXD/L1TXDA2/L1TXD0A2/FCC2_UT8_RXD3	POE/PSDRAS/PGPL2	A24
AH27	PB8/FCC3_MII_HDLC_RXD0/FCC3_RMII_RXD0/FCC3_RXD/TXD3/FCC2_UT8_TXD3/L1RSYNCD1	PSDCAS/PGPL3	B23
AG24	PB9/FCC3_MII_HDLC_RXD1/FCC3_RMII_RXD1/L1TXD2A2/FCC2_UT8_TXD2/L1TSYNCD1/L1GNTD1	PGTA/PUPMWAIT/PGPL4/PPBS	A23
AH24	PB10/FCC3_MII_HDLC_RXD2/FCC2_UT8_TXD1/L1RXDD1	PSDAMUX/PGPL5	D22
AJ24	PB11/FCC3_MII_HDLC_RXD3/FCC2_UT8_TXD0/L1TXDD1		
AG22	PB12/FCC3_MII_CRS/TXD2/L1CLKOB1/L1RSYNCC1	PSDVAL	V3
AH21	PB13/FCC3_MII_COL/L1TXD1A2/L1RQB1/L1TSYNCC1/L1GNTC1		
AG20	PB14/FCC3_MII_RMII_TX_EN/RXD3/L1RXDC1	PWE0/PSDDQM0/PBS0	C25
AF19	PB15/FCC3_MII_TX_ER/RXD2/L1TXDC1	PWE1/PSDDQM1/PBS1	E24
AJ18	PB16/FCC3_MII_RMII_RX_ER/CLK18/L1CLKOA1	PWE2/PSDDQM2/PBS2	D24
AJ17	PB17/FCC3_MII_RX_DV/CLK17/FCC3_RMII_CRS_DV/L1RQA1	PWE3/PSDDQM3/PBS3	C24
AE14	PB18/FCC2_MII_HDLC_RXD3/L1CLKOD2/L1RXD2A2/FCC2_UT8_RXD4	PWE4/PSDDQM4/PBS4	B26
AF13	PB19/FCC2_MII_HDLC_RXD2/L1RQD2/L1RXD3A2/FCC2_UT8_RXD5	PWE5/PSDDQM5/PBS5	A26
AG12	PB20/FCC2_MII_HDLC_RMII_RXD1/L1RSYNCD2/FCC2_UT8_RXD6/L1TXD1A1	PWE6/PSDDQM6/PBS6	B25
AH11	PB21/FCC2_MII_HDLC_RMII_RXD0/FCC2_TRAN_RXD/L1TSYNCD2/L1GNTD2/FCC2_UT8_RXD7/L1TXD2A1	PWE7/PSDDQM7/PBS7	A25
AH16	PB22/FCC2_MII_HDLC_TXD0/FCC2_TXD/FCC2_RMII_TXD0/L1RXDD2/FCC2_UT8_TXD7/L1RXD1A1		
AE15	PB23/FCC2_MII_HDLC_TXD1/L1RXD2A1/L1TXDD2/FCC2_RMII_TXD1/FCC2_UT8_TXD6/L1RXD2A1		
AJ9	PB24/FCC2_MII_HDLC_TXD2/L1RSYNCC2/FCC2_UT8_TXD5/L1RXD3A1	MODCK1/AP1/TC0/BNKSEL0	W2
AE9	PB25/FCC2_MII_HDLC_TXD3/L1TSYNCC2/L1GNTC2/FCC2_UT8_TXD4/L1TXD3A1	MODCK2/AP2/TC1/BNKSEL1	W3
AJ7	PB26/FCC2_MII_CRS/L1RXDC2/FCC2_UT8_TXD1	MODCK3/AP3/TC2/BNKSEL2	W4
AH6	PB27/FCC2_MII_COL/L1TXDC2/FCC2_UT8_TXD0		
AE3	PB28/FCC2_MII_RX_ER/FCC2_RMII_RX_ER/FCC2_RTS/L1TSYNCB2/L1GNTB2/TXD1		
AE2	PB29/L1RSYNCB2/FCC2_MII_TX_EN/FCC2_RMII_TX_EN/FCC2_UTM_RXCLAV/FCC2_UTS_RXCLAV		
AC5	PB30/FCC2_MII_RX_DV/FCC2_RMII_CRS_DV/L1RXDB2/FCC2_UT_TXSOC		
AC4	PB31/FCC2_MII_TX_ER/L1TXDB2/FCC2_UT_RXSOC		

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U?D		
AB26	PC0/DREQ1/BRGO7/SMSYN2/L1CLKOA2	
AD29	PC1/DREQ2/BRGO6/L1RQA2/SPISEL	
AE29	PC2/FCC3_CD/DONE2/FCC2_UT8_TXD3	
AE27	PC3/FCC3_CTS/DACK2/CTS4/USB_RP/FCC2_UT8_TXD2	PORESET
AF27	PC4/SI2_L1ST4/FCC2_CD/FCC2_UTM_RXENB/FCC2_UTS_RXENB	
AF24	PC5/SI2_L1ST3/FCC2_CTS/FCC2_UTM_TXCLAV/FCC2_UTS_TXCLAV	L_A14/PAR
AJ26	PC6/FCC1_CD/L1CLKOC1/FCC1_UTM_RXADDR2/FCC1_UTS_RXADDR2/FCC1_UTM_RXCLAV1	
AJ25	PC7/FCC1_CTS/L1RQC1/FCC1_UTM_TXADDR2/FCC1_UTS_TXADDR2/FCC1_UTM_TXCLAV1	L_A15/FRAME/SMI
AF22	PC8/CD4/RENA4/SI2_L1ST2/CTS3/USBRN/FCC1_UT16_TXD0	
AE21	PC9/CTS4/CLSN4/SI2_L1ST1/L1TSYNCA2/L1GNTA2/USB_RP/FCC1_UT16_TXD1	L_A16/TRDY
AF20	PC10/CD3/RENA3/FCC1_UT16_TXD2/SI1_L1ST4/FCC2_UT8_RXD3	
AE19	PC11/CTS3/CLSN3/L1TXD3A2/L1CLKOD1/FCC2_UT8_RXD2	L_A17/IRDY/CKSTP_OUT
AE18	PC12/CD2/RENA2/SI1_L1ST3/FCC1_UTM_RXADDR1/FCC1_UTS_RXADDR1	
AH18	PC13/CTS2/CLSN2/L1RQD1/FCC1_UTM_TXADDR1/FCC1_UTS_TXADDR1	L_A18/STOP
AH17	PC14/CD1/RENA1/FCC1_UTM_RXADDR0/FCC1_UTS_RXADDR0	
AG16	PC15/CTS1/CLSN1/SMTXD2/FCC1_UTM_TXADDR0/FCC1_UTS_TXADDR0	L_A19/DEVSEL
AF15	PC16/CLK16/TIN4	
AJ15	PC17/CLK15/TIN3/BRGO8	L_A20>IDSEL
AH14	PC18/CLK14/TGATE2	
AG13	PC19/CLK13/BRGO7/SPICLK	L_A21/PERR
AH12	PC20/CLK12/TGATE1/USB_OE	
AJ11	PC21/CLK11/BRGO6	L_A22/SERR
AG10	PC22/CLK10/DONE1/FCC1_UT_TXPRTY	
AE10	PC23/CLK9/BRGO5/DACK1	L_A23/REQ0
AF9	PC24/CLK8/TOUT4/FCC2_UT8_TXD3	
AE8	PC25/CLK7/BRGO4/FCC2_UT8_TXD2	L_A24/REQ1/HSEJSW
AJ6	PC26/CLK6/TOUT3/TMCLK	
AG2	PC27/FCC3_TXD/FCC3_MII_TXD0/FCC3_RMII_TXD0/CLK5/BRGO3	L_A25/GNT0
AF3	PC28/CLK4/TIN1/TOUT2/CTS2/CLSN2/FCC2_RXADDR4	
AF2	PC29/CLK3/TIN2/BRGO2/CTS1/CLSN1	L_A26/GNT1/HSLED
AE1	PC30/CLK2/TOUT1/FCC2_UT8_TXD3	
AD1	PC31/CLK1/BRGO1	L_A27/GNT2/HSENUM
AC28	PD4/BRGO8/FCC3_RTS/SMRDX2/L1TSYNCD1/L1GNTD1	L_A28/RST/CORE_SRESET
AD27	PD5/DONE1/FCC1_UT16_TXD3	
AF29	PD6/DACK1/FCC1_UT16_TXD4	L_A29/INTA
AF28	PD7/SMSYN1/FCC1_TXCLAV2/FCC1_UTM_TXADDR3/FCC1_UTS_TXADDR3/FCC2_UTM_TXADDR4/FCC2_UTS_TXADDR1	
AG25	PD8/SMRDX1/BRGO5/FCC2_UT_TXPRTY	L_A30/REQ2
AH26	PD9/SMTXD1/BRGO3/FCC2_UT_RXPRTY	
AJ27	PD10/L1CLKOB2/BRGO4/FCC2_UT8_RXD1/L1RSYNCB1	L_A31/DLLOUT
AJ23	PD11/L1RQB2/FCC2_UT8_RXD0/L1TSYNCB1/L1GNTB1	
AG23	PD12/SI1_L1ST2/L1RXDB1	
AJ22	PD13/SI1_L1ST1/L1TXDB1	
AE20	PD14/L1CLKOC2/I2CSCL/FCC1_UT16_RXD0	NC1
AJ20	PD15/L1RQC2/I2CSDA/FCC1_UT16_RXD1	NC2
AG18	PD16/SPIMISO/FCC1_UT_TXPRTY/L1TSYNCC1/L1GNTC1	
AG17	PD17/BRGO2/SPIMOSI/FCC1_UT_RXPRTY	
AF16	PD18/SPICLK/FCC1_UTM_RXADDR4/FCC1_UTS_RXADDR4/FCC1_UTM_RXCLAV3/FCC2_UTM_RXADDR3/FCC2_UTS_RXADDR0	
AH15	PD19/SPISEL/BRGO1/FCC1_UTM_TXADDR4/FCC1_UTS_TXADDR4/FCC1_UTM_TXCLAV3/FCC2_UTM_TXADDR3/FCC2_UTS_TXADDR0	
AJ14	PD20/RTS4/TENA4/L1RSYNCA2/USB_TP/FCC1_UT16_RXD2	
AH13	PD21/TXD4/L1RXD0A2/L1RXDA2/USB_TN/FCC1_UT16_RXD3	
AJ12	PD22/RXD4/L1TXD0A2/L1TXDA2/USB_RXD/FCC1_UT16_TXD5	
AE12	PD23/RTS3/TENA3/FCC1_UT16_RXD4/L1RSYNCD1	
AF10	PD24/TXD3/FCC1_UT16_RXD5/L1RXDD1	
AG9	PD25/RXD3/FCC1_UT16_TXD6/L1TXDD1	SPARE4
AH8	PD26/RTS2/TENA2/FCC1_UT16_RXD6/L1RSYNCC1	SPARE6
AG7	PD27/TXD2/FCC1_UT16_RXD7/L1RXDC1	
AE4	PD28/RXD2/FCC1_UT16_TXD7/L1TXDC1	
AG1	PD29/RTS1/TENA1/FCC1_UTM_RXADDR3/FCC1_UTS_RXADDR3/FCC1_UTM_RXCLAV2/FCC2_UTM_RXADDR4/FCC2_UTS_RXADDR1	
AD4	PD30/TXD1/FCC2_UTM_TXENB/FCC2_UTS_TXENB	
AD2	PD31/RXD1	

<Value>



U?E		
AB3	VCCSYN	VDDH1
		VDDH2
		VDDH3
		VDDH4
		VDDH5
B9	VCCSYN1	VDDH6
		VDDH7
		VDDH8
		VDDH9
		VDDH10
		VDDH11
		VDDH12
		VDDH13
		VDDH14
		VDDH15
		VDDH16
U28	VDD1	VDDH17
U29		VDDH18
K28		VDDH19
K29		VDDH20
A9		VDDH21
A19		VDDH22
B19		VDDH23
M1		VDDH24
M2		VDDH25
Y1		VDDH26
Y2		VDDH27
AC1		VDDH28
AC2		VDDH29
AH19		VDDH30
AJ19		VDDH31
AH10		VDDH32
AJ10		VDDH33
AJ5		VDDH34
	VDD18	VDDH35
		VDDH36
		VDDH37
		VDDH38
		VDDH39
		VDDH40
		VDDH41
		VDDH42
		VDDH43
		VDDH44
		VDDH45
		VDDH46
		VDDH47

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U?F

AA5	GND1	GND23	D16
AF21	GND2	GND24	D20
AF14	GND3	GND25	D23
AF8	GND4	GND26	C9
AE7	GND5	GND27	E11
AF11	GND6	GND28	E13
AE17	GND7	GND29	E15
AE23	GND8	GND30	E19
AC26	GND9	GND31	E22
AB25	GND10	GND32	B3
Y26	GND11	GND33	G5
V25	GND12	GND34	H4
T26	GND13	GND35	K5
R25	GND14	GND36	M3
P26	GND15	GND37	P5
M25	GND16	GND38	T4
K27	GND17	GND39	Y5
H25	GND18	GND40	AA2
G26	GND19	GND41	AC3
D7	GND20	GND42	AB1
D10	GND21	GND43	AB2
D14	GND22		

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