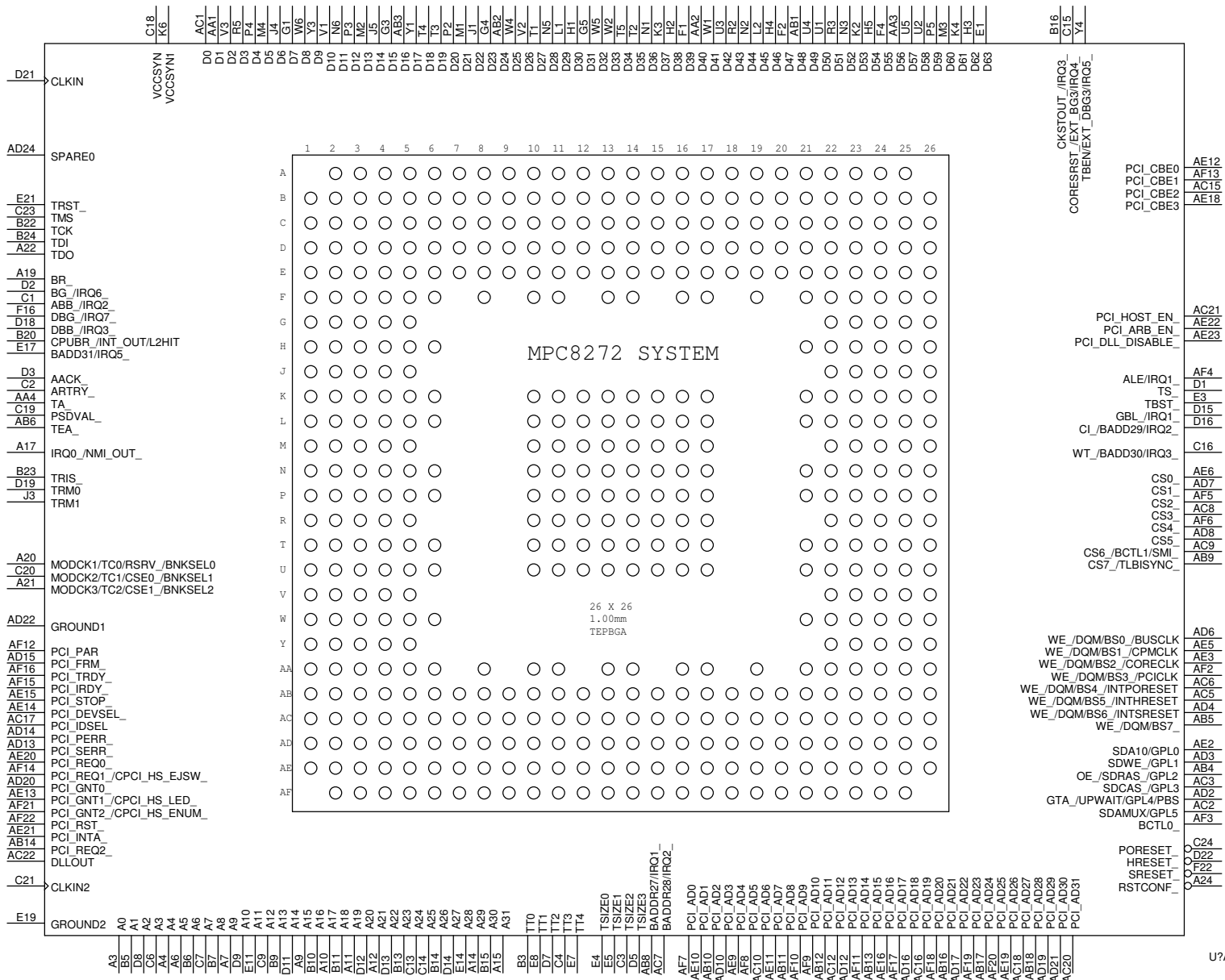
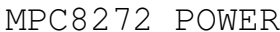




MPC8272 CPM

G22	PA31/UT_TXEN_/FM1_COL	PC29/BRG02/CLK3/TIN2/CTS1	F24
A25	PA30/UT_TXCLAV/FM1_CRS	PC28/CLK4/TOUT2_/TIN1/SPICLK	D25
B26	PA29/UT_TXSOC/FM1_TXER	PC27/BRG03/CLK5/TOUT1_/UTRXPRTY	K22
G23	PA28/UT_RXEN_/FM1_TXEN	PC26/CLK6/TOUT3_/TMCLK	H23
G24	PA27/UT_RXSOC/FM1_RXDV	PC25/BRG04/CLK7/DACK2_/SPISEL	F26
L22	PA26/UT_RXCLAV/FM1_RXER	PC24/BRG01/CLK8/TOUT4_/DREQ2	K23
G25	PA25/UT_TXD0/MSNUM0	PC23/BRG05/CLK9/DACK3_/CD1	K24
H26	PA24/UT_TXD1/MSNUM1	PC22/CLK10/UT_TXPRTY/DONE3	L23
N23	PA23/UT_TXD2	PC21/BRG06/CLK11/CP_INT	L24
N26	PA22/UT_TXD3	PC20/CLK12/USBOE	L26
N22	PA21/UT_TXD4/FM1_TXD3	PC19/BRG07/CLK13/TGATE1	P24
P25	PA20/UT_TXD5/FM1_TXD2	PC18/CLK14/TGATE2	R26
R23	PA19/UT_TXD6/FM1_TXD1	PC17/BRG08/CLK15/DONE2	T26
V26	PA18/UT_TXD7/FM1_TXD0	PC16/CLK16	T23
W26	PA17/UT_RXD7/FM1_RXD0	PC15/CTS1_/UT_TXADDR0	U24
T22	PA16/UT_RXD6/FM1_RXD1	PC14/CD1_/UT_RXADDR0	W24
Y24	PA15/UT_RXD5/FM1_RXD2	PC13/BRG05/UT_TXADDR1	V23
W22	PA14/UT_RXD4/FM1_RXD3	PC12/UT_RXADDR1	AA26
AA24	PA13/UT_RXD3/MSNUM2	PC11/CTS3_/USBRP	V22
AD25	PA12/UT_RXD2/MSNUM3	PC10/CD3_/USBRN	AB25
AD26	PA11/UT_RXD1/MSNUM4	PC9/CTS4_/L1TSYNC_A2	AA23
AB23	PA10/UT_RXD0/MSNUM5	PC8/RTS1_/CD4_/SI2_L1ST1/CTS3	AC24
AA22	PA9/SMTXD2	PC7/UT_TXADDR2	AE26
AF25	PA8/SMRXD2	PC6/SI2_L1ST2/UT_RXADDR2	AF24
		PC5/SMTXD1/SI2_L1ST3	AE24
		PC4/SMRXD1/SI2_L1ST4	AB22
		PC1/BRG06/L1RQ_A2	AD23
		PC0/BRG07/DREQ3/L1CLK0A2/SMSYN1	AF23
E23	PB31/FM2_TXER	PD31/RXD1	B25
D24	PB30/FM2_RXDV	PD30/TXD1	E24
K21	PB29/FM2_TXEN	PD29/RTS1_/UT_RXADDR3	C26
D26	PB28/FM2_RXER/TXD1	PD25/RXD3/USBRXD	J25
E25	PB27/FM2_COL/L1TXD_B2	PD24/TXD3/USBTN	K25
H24	PB26/FM2_CRS/L1RXD_B2	PD23/RTS3_/USBTP	K26
M23	PB25/FM2_TXD3/L1TSYNC_B2	PD22/RXD4/L1TXD_A2	N25
M24	PB24/FM2_TXD2/L1RSYNC_B2	PD21/TXD4/L1RXD_A2	P23
U26	PB23/FM2_TXD1	PD20/RTS4_/L1RSYNC_A2	R24
U23	PB22/FM2_TXD0	PD19/BRG01/UT_TXADDR4/SPISEL	V25
J26	PB21/FM2_RXD0	PD18/UT_RXADDR4/SPICLK	W25
L25	PB20/FM2_RXD1	PD17/BRG02/UT_RXPRTY/SPIMOSI	Y26
P22	PB19/FM2_RXD2/L1RQ_B2	PD16/UT_TXPRTY/SPIMISO	AA25
T25	PB18/FM2_RXD3/L1CLK0_B2	PD15/I2CSDA	Y23
		PD14/I2CSCL	AC26
		PD7/SMSYN2/UT_TXADDR3	AB21

U?B





U?A

D1	TS	D0	AC1
B22	TCK	D1	AA1
B24	TDI	D2	V3
A22	TDO	D3	R5
C23	TMS	D4	P4
		D5	M4
D21	CLKIN1	D6	J4
C21	CLKIN2	D7	G1
		D8	W6
A3	A0	D9	Y3
B5	A1	D10	V1
D8	A2	D11	N6
C6	A3	D12	P3
A4	A4	D13	M2
A6	A5	D14	J5
B6	A6	D15	G3
C7	A7	D16	AB3
B7	A8	D17	Y1
A7	A9	D18	T4
D9	A10	D19	T3
E11	A11	D20	P2
C9	A12	D21	M1
B9	A13	D22	J1
D11	A14	D23	G4
A9	A15	D24	AB2
B10	A16	D25	W4
A10	A17	D26	V2
B11	A18	D27	T1
A11	A19	D28	N5
D12	A20	D29	L1
A12	A21	D30	H1
D13	A22	D31	G5
B13	A23	D32	W5
C13	A24	D33	W2
C14	A25	D34	T5
B14	A26	D35	T2
D14	A27	D36	N1
E14	A28	D37	K3
A14	A29	D38	H2
B15	A30	D39	F1
A15	A31	D40	AA2
		D41	W1
AD8	CS5	D42	U3
AF6	CS4	D43	R2
AC8	CS3	D44	N2
AF5	CS2	D45	L2
AD7	CS1	D46	H4
AE6	CS0	D47	F2
		D48	AB1
E7	TT4	D49	U4
C4	TT3	D50	U1
D7	TT2	D51	R3
E8	TT1	D52	N3
B3	TT0	D53	K2
		D54	H5
D5	TSIZ3	D55	F4
C3	TSIZ2	D56	AA3
E5	TSIZ1	D57	U5
E4	TSIZ0	D58	U2
		D59	P5
E21	TRST2	D60	M3
B23	TRIS	D61	K4
AB6	TEA	D62	H3
E3	TBST	D63	E1
AA4	TA		

<Value>

U?B

AF7	PCI_AD0	PCI_CFG0(PCI_HOST_EN)	AC21
AE10	PCI_AD1	PCI_CFG1(PCI_ARB_EN)	AE22
AB10	PCI_AD2	PCI_CFG2(DLL_ENABLE)	AE23
AD10	PCI_AD3		
AE9	PCI_AD4	PCI_IDSEL	AC17
AF8	PCI_AD5		
AC10	PCI_AD6	ARTRY	C2
AE11	PCI_AD7	AACK	D3
AB11	PCI_AD8		
AF10	PCI_AD9	BR	A19
AF9	PCI_AD10		
AB12	PCI_AD11	BCTL0	AF3
AC12	PCI_AD12		
AD12	PCI_AD13	BADDR27/IRQ1	AB8
AF11	PCI_AD14		
AB13	PCI_AD15	ABB/IRQ2	C1
AE16	PCI_AD16	BADDR28/IRQ2	AC7
AF17	PCI_AD17	CI/BADDR29/IRQ2	D16
AD16	PCI_AD18		
AC16	PCI_AD19	WT/BADDR30/IRQ3	C16
AF18	PCI_AD20	DBB/IRQ3	D18
AB16	PCI_AD21	DBG/IRQ3	F16
AD17	PCI_AD22		
AF19	PCI_AD23	ALE/IRQ4	AF4
AB17	PCI_AD24	BADDR31/IRQ5/CINT	E17
AF20	PCI_AD25	BG/IRQ6	D2
AE19	PCI_AD26		
AC18	PCI_AD27	CS6/BCTL1/SMI	AC9
AB18	PCI_AD28	CS7/TLBISYNC	AB9
AD19	PCI_AD29		
AD21	PCI_AD30	DLLOUT	AC22
AC20	PCI_AD31		
W24	FCC1_UT_RXADDR0	FCC1_UT_TXENB	G22
AA26	FCC1_UT_RXADDR1	FCC1_UT_TXSOC	B26
AF24	FCC1_UT_RXADDR2		
C26	FCC1_UT_RXADDR3	CPU_BR/INT_OUT	B20
W25	FCC1_UT_RXADDR4	PSDA10/PGPL0	AE2
L22	FCC1_UT_RXCLAV	PSDAMUX/PGPL5	AC2
G23	FCC1_UT_RXENB		
G24	FCC1_UT_RXSOC	RSTCONF	A24
		SRESET	F22
AB23	FCC1_UT_RXD0		
AD26	FCC1_UT_RXD1		
AD25	FCC1_UT_RXD2		
AA24	FCC1_UT_RXD3		
W22	FCC1_UT_RXD4		
Y24	FCC1_UT_RXD5		
T22	FCC1_UT_RXD6		
W26	FCC1_UT_RXD7		
Y26	FCC1_UT_RXPTY_1		
K22	FCC1_UT_RXPTY_2		
U24	FCC1_UT_TXADDR0		
V23	FCC1_UT_TXADDR1		
AE26	FCC1_UT_TXADDR2		
AB21	FCC1_UT_TXADDR3		
V25	FCC1_UT_TXADDR4		

<Value>

AF25	PA8/SMRDXD2	GBL/IRQ1	D15
AA22	PA9/SMTXD2	HRESET	D22
T25	PB18/FCC2_MII_HDLC_RXD3	PCI_C0/BE0	AE12
P22	PB19/FCC2_MII_HDLC_RXD2	PCI_C1/BE1	AF13
L25	PB20/FCC2_MII_HDLC_RMII_RXD1	PCI_C2/BE2	AC15
J26	PB21/FCC2_MII_HDLC_RMII_RXD0/FCC2_TRAN_RXD	PCI_C3/BE3	AE18
U23	PB22/FCC2_MII_HDLC_TXD0/FCC2_TRAN_TXD/FCC2_RMII_TXD0		
U26	PB23/FCC2_MII_HDLC_TXD1/FCC2_RMII_TXD1	PCI_DEVSEL	AE14
M24	PB24/FCC2_MII_HDLC_TXD2/L1RSYNCB2	PCI_FRAME	AD15
M23	PB25/FCC2_MII_HDLC_TXD3/L1TSYNCB2		
H24	PB26/FCC2_MII_CRS/L1RXDB2	PCI_INTA	AE21
E25	PB27/FCC2_MII_COL/L1TXDB2	PCI_IRDY	AF15
D26	PB28/FCC2_MII_RMII_RX_ER/FCC2_RTS/TXD1	PCI_TRDY	AF16
K21	PB29/FCC2_MII_RMII_TX_EN	PCI_RST	AF22
D24	PB30/FCC2_MII_RX_DV/FCC2_RMII_CRS_DV	PCI_PAR	AF12
E23	PB31/FCC2_MII_TX_ER	PORESET2/PCI_RST	C24
AF23	PC0/DREQ3/BRGO7/SMSYN1/L1CLKOA2	IRQ0/NMI_OUT	A17
AD23	PC1/BRGO6/L1RQA2	IRQ3/CKSTP_OUT/EXT_BR3	B16
AB22	PC4/SMRDXD1/SI2_L1ST4/FCC2_CD	IRQ4/CORE_SRESET/EXT_BG3	C15
AE24	PC5/SMTXD1/SI2_L1ST3/FCC2_CTS	IRQ5/TBEN/EXT_DBG3/CINT	Y4
AC24	PC8/CD4/RTS1/SI2_L1ST2/CTS3		
AA23	PC9/CTS4/L1TSYNCA2	PCI_GNT0	AD20
AB25	PC10/CD3/USB_RN	PCI_GNT1/CPCI_HS_LED	AE13
V22	PC11/CTS3/USB_RP/L1TXD3A2	PCI_GNT2/CPCI_HS_ENUM	AF21
T23	PC16/CLK16		
T26	PC17/CLK15/BRGO8/DONE2	PCI_MODE1	AD22
R26	PC18/CLK14/TGATE2	PCI_PERR	AD14
P24	PC19/CLK13/BRGO7/TGATE1	PCI_REQ0	AE20
L26	PC20/CLK12/USBOE	PCI_REQ1/CPCI_HS_ES	AF14
L24	PC21/CLK11/BRGO6/CP_INT	PCI_REQ2	AB14
K24	PC23/CLK9/BRGO5/DACK3/CD1		
K23	PC24/CLK8/TIN3/TOUT4/DREQ2/BRGO1	PCI_SERR	AD13
F26	PC25/CLK7/BRGO4/DACK2/SPISEL	PCI_STOP	AE15
H23	PC26/CLK6/TOUT3/TMCLK		
D25	PC28/CLK4/TIN1/TOUT2/SPICLK	PSDWE/PGPL1	AD3
F24	PC29/CLK3/TIN2/BRGO2/CTS1	POE/PSDRAS/PGPL2	AB4
		PSDCAS/PGPL3	AC3
AC26	PD14/I2CSCL	PGTA/PUPMWAIT/PGPL4	AD2
Y23	PD15/I2CSDA		
R24	PD20/RTS4/L1RSYNCA2	PSDVAL	C19
P23	PD21/TXD4/L1RXD0A2		
N25	PD22/RXD4/L1TXD0A2	PWE7/PSDDQM7/PBS7	AB5
K26	PD23/RTS3/USB_TP	PWE6/PSDDQM6/PBS6	AD4
K25	PD24/TXD3/USB_TN	PWE5/PSDDQM5/PBS5	AC5
J25	PD25/RXD3/USB_RXD	PWE4/PSDDQM4/PBS4	AC6
E24	PD30/TXD1	PWE3/PSDDQM3/PBS3	AF2
B25	PD31/RXD1	PWE2/PSDDQM2/PBS2	AE3
		PWE1/PSDDQM1/PBS1	AE5
G25	FCC1_UT_TXD0	PWE0/PSDDQM0/PBS0	AD6
H26	FCC1_UT_TXD1		
N23	FCC1_UT_TXD2		
N26	FCC1_UT_TXD3		
N22	FCC1_UT_TXD4		
P25	FCC1_UT_TXD5		
R23	FCC1_UT_TXD6	MODCK1/RSRV/TC0/BNKSEL0	A20
V26	FCC1_UT_TXD7	MODCK2/CSE0/TC1/BNKSEL1	C20
		MODCK3/CSE1/TC2/BNKSEL2	A21
L23	FCC1_UT_TXPRTY_L23		
AA25	FCC1_UT_TXPRTY_AA25	NC_19	D19
		NC_AD24	AD24
A25	FCC1_UT_TXCLAV	NC_J3	J3

U?D

C18	VCCSYN	VDDH1	B4
K6	VCCSYN1	VDDH2	F3
		VDDH3	J2
F5	VDD1	VDDH4	N4
K5	VDD2	VDDH5	AD1
M5	VDD3	VDDH6	AD5
AA5	VDD4	VDDH7	AE8
AB7	VDD5	VDDH8	AC13
AA13	VDD6	VDDH9	AD18
AA19	VDD7	VDDH10	AB24
AA21	VDD8	VDDH11	AB26
Y22	VDD9	VDDH12	W23
AC25	VDD10	VDDH13	R25
		VDDH14	M25
U22	VDD11	VDDH15	F25
R22	VDD12	VDDH16	C25
L21	VDD13	VDDH17	C22
H22	VDD14	VDDH18	B17
E22	VDD15	VDDH19	B12
E20	VDD16	VDDH20	B8
E15	VDD17	VDDH21	E6
F13	VDD18	VDDH22	F6
F11	VDD19	VDDH23	H6
F8	VDD20	VDDH24	L5
		VDDH25	L6
L3	VDD21	VDDH26	P6
V4	VDD22	VDDH27	T6
W3	VDD23	VDDH28	U6
AC11	VDD24	VDDH29	V5
AD11	VDD25	VDDH30	Y5
AB15	VDD26	VDDH31	AA6
U25	VDD27	VDDH32	AA8
T24	VDD28	VDDH33	AA10
J24	VDD29	VDDH34	AA11
H25	VDD30	VDDH35	AA14
		VDDH36	AA16
F23	VDD31	VDDH37	AA17
B19	VDD32	VDDH38	AB19
D17	VDD33	VDDH39	AB20
C17	VDD34	VDDH40	W21
D10	VDD35	VDDH41	U21
C10	VDD36	VDDH42	T21
		VDDH43	P21
		VDDH44	N21
		VDDH45	M22
		VDDH46	J22
		VDDH47	H21
		VDDH48	F21
		VDDH49	F19
		VDDH50	F17
		VDDH51	E16
		VDDH52	F14
		VDDH53	E13
		VDDH54	E12
		VDDH55	F10
		VDDH56	E10
		VDDH57	E9

<Value>

U?E

E19	GND1	GND61	L15
E2	GND2	GND62	L16
K1	GND3	GND63	L17
Y2	GND4	GND64	M10
AE1	GND5	GND65	M11
AE4	GND6	GND66	M12
AD9	GND7	GND67	M13
AC14	GND8	GND68	M14
AE17	GND9	GND69	M15
AC19	GND10	GND70	M16
AE25	GND11	GND71	M17
V24	GND12	GND72	N10
P26	GND13	GND73	N11
M26	GND14	GND74	N12
G26	GND15	GND75	N13
E26	GND16	GND76	N14
B21	GND17	GND77	N15
C12	GND18	GND78	N16
C11	GND19	GND79	N17
C8	GND20	GND80	P10
A8	GND21	GND81	P11
B18	GND22	GND82	P12
A18	GND23	GND83	P13
A2	GND24	GND84	P14
B1	GND25	GND85	P15
B2	GND26	GND86	P16
A5	GND27	GND87	P17
C5	GND28	GND88	R10
D4	GND29	GND89	R11
D6	GND30	GND90	R12
G2	GND31	GND91	R13
L4	GND32	GND92	R14
P1	GND33	GND93	R15
R1	GND34	GND94	R16
R4	GND35	GND95	R17
AC4	GND36	GND96	T10
AE7	GND37	GND97	T11
AC23	GND38	GND98	T12
Y25	GND39	GND99	T13
N24	GND40	GND100	T14
J23	GND41	GND101	T15
A23	GND42	GND102	T16
D23	GND43	GND103	T17
D20	GND44	GND104	U10
E18	GND45	GND105	U11
A13	GND46	GND106	U12
A16	GND47	GND107	U13
K10	GND48	GND108	U14
K11	GND49	GND109	U15
K12	GND50	GND110	U16
K13	GND51	GND111	U17
K14	GND52		
K15	GND53		
K16	GND54		
K17	GND55		
L10	GND56		
L11	GND57		
L12	GND58		
L13	GND59		
L14	GND60		

<Value>