



	U?A	
F22	TRST	
A24	TCK	
A25	TDI	
B24	TDO	
C24	TMS	W4
		D0
		D1
G22	CLKIN1	D2
K21	CLKIN2	D3
		D4
D5	A0	D5
E8	A1	D6
C4	A2	D7
B4	A3	D8
A4	A4	D9
D7	A5	D10
D8	A6	D11
C6	A7	D12
B5	A8	D13
B6	A9	D14
C7	A10	D15
C8	A11	D16
A6	A12	D17
D9	A13	D18
F11	A14	D19
B7	A15	D20
B8	A16	D21
C9	A17	D22
A7	A18	D23
B9	A19	D24
E11	A20	D25
A8	A21	D26
D11	A22	D27
B10	A23	D28
C11	A24	D29
A9	A25	D30
B11	A26	D31
C12	A27	D32
D12	A28	D33
A10	A29	D34
B12	A30	D35
B13	A31	D36
		D37
AD8	CS9	D38
AE8	CS8	D39
AF8	CS7	D40
AD7	CS6	D41
AC7	CS5	D42
AF7	CS4	D43
AB7	CS3	D44
AE6	CS2	D45
AD6	CS1	D46
AC6	CS0	D47
		D48
C3	TT4	D49
A3	TT3	D50
F8	TT2	D51
B3	TT1	D52
E7	TT0	D53
		D54
E4	TSIZ3	D55
E1	TSIZ2	D56
E2	TSIZ1	D57
E3	TSIZ0	D58
		D59
D1	TS	D60
C19	TRIS	D61
D16	TEA	D62
F5	TBST	D63
Y4	TA	



U?B

		IRQ0/NMI_OUT	A11
		L2_HIT/IRQ4	A17
		CPU_BG/BADDR31/IRQ5	B14
AB9			
AB10	LCL_D0/AD0		F13
AC10	LCL_D1/AD1	CPU_DBG	B17
AD10	LCL_D2/AD2	CPU_BR	
AE10	LCL_D3/AD3		C2
AF10	LCL_D4/AD4	ARTRY	D3
AF11	LCL_D5/AD5	AACK	
AB12	LCL_D6/AD6		C16
AB11	LCL_D7/AD7	BR	AF4
AF12	LCL_D8/AD8	BCTL0	
AE11	LCL_D9/AD9		C1
AC13	LCL_D10/AD10	ABB/IRQ2	C13
AC12	LCL_D11/AD11	BADDR27	A12
AB13	LCL_D12/AD12	BADDR28	D14
AD12	LCL_D13/AD13	CI/BADDR29/IRQ2	
AF14	LCL_D14/AD14		E14
AF17	LCL_D15/AD15	WT/BADDR30/IRQ3	C15
AE16	LCL_D16/AD16	DBB/IRQ3	A14
AD16	LCL_D17/AD17	DBG	D13
AC16	LCL_D18/AD18	ALE	D2
AB16	LCL_D19/AD19	BG	
AF18	LCL_D20/AD20		AC8
AE17	LCL_D21/AD21	CS10/BCTL1	AB8
AD17	LCL_D22/AD22	CS11/AP0	
AB17	LCL_D23/AD23		AC9
AE18	LCL_D24/AD24	LWE0/LSDDQM0/LBS0/PCI_CFG0	AD9
AD18	LCL_D25/AD25	LWE1/LSDDQM1/LBS1/PCI_CFG1	AE9
AC18	LCL_D26/AD26	LWE2/LSDDQM2/LBS2/PCI_CFG2	AF9
AE19	LCL_D27/AD27	LWE3/LSDDQM3/LBS3/PCI_CFG3	
AF20	LCL_D28/AD28		AE12
AD19	LCL_D29/AD29	LCL_DP0/C0/BE0	AA13
AB18	LCL_D30/AD30	LCL_DP1/C1/BE1	AC15
	LCL_D31/AD31	LCL_DP2/C2/BE2	AF19
		LCL_DP3/C3/BE3	
			E24
		RSTCONF	E23
		SRESET	D18
		QREQ	

<Value>

AC21	PA0/RESTART1/DREQ3	GBL/IRQ1	E15
AF25	PA1/REJECT1/DONE3	HRESET	D24
AE24	PA2/CLK20/DACK3		
AA21	PA3/CLK19/DACK4/L1RXD1A2		AB3
AD25	PA4/REJECT2/DONE4	DP0/RSRV/EXT_BR2	
AC24	PA5/RESTART2/DREQ4		W5
AA22	PA6	IRQ1/DP1/EXT_BG2	AC2
AA23	PA7/SMSYN2	IRQ2/DP2/TLBISYNC/EXT_DBG2	AA3
Y26	PA8/SMRXD2	IRQ3/DP3/CKSTP_OUT/EXT_BR3	AD1
W22	PA9/SMTXD2	IRQ4/DP4/CORE_SRESET/EXT_BG3	AC1
W23	PA10/MSNUM5	IRQ5/DP5/TBEN/EXT_DBG3	AB2
V26	PA11/MSNUM4	IRQ6/DP6/CSE0	Y3
V25	PA12/MSNUM3	IRQ7/DP7/CSE1	
T22	PA13/MSNUM2		E5
T25	PA14/FCC1_RXD3	IRQ7/INT_OUT/APE	
R24	PA15/FCC1_RXD2		AF6
P22	PA16/FCC1_RXD1	LWR	
N26	PA17/FCC1_RXD0/FCC1_RXD		A18
N23	PA18/FCC1_TXD0/FCC1_TXD	XFC	
K26	PA19/FCC1_TXD1		AD24
L23	PA20/FCC1_TXD2	PCI_MODE	
K23	PA21/FCC1_TXD3		AB6
H26	PA22	LSDA10/LGPL0/PCI_MODCKH0	AF5
F25	PA23	LSDWE/LGPL1/PCI_MODCKH1	AE5
D26	PA24/MSNUM1	LOE/LSDRAS/LGPL2/PCI_MODCKH2	AD5
D25	PA25/MSNUM0	LSDCAS/LGPL3/PCI_MODCKH3	
C25	PA26/FCC1_MII_RX_ER		AC5
C22	PA27/FCC1_MII_RX_DV		AB5
B21	PA28/FCC1_MII_TX_EN	LGTA/LUPMWAIT/LGPL4/LPBS	
A20	PA29/FCC1_MII_TX_ER	LGPL5/LSDAMUX/PCI_MODCK	
A19	PA30/FCC1_MII_CRS/FCC1_RTS		AE2
	PA31/FCC1_MII_COL		AD2
AD21		PSDA10/PGPL0	AE1
AD22	PB4/FCC3_TXD3/L1RSYNCA2/ FCC3_RTS	PSDWE/PGPL1	AC3
AC22	PB5/FCC3_TXD2/L1TSYNCA2/ L1GNTA2	POE/PSDRAS/PGPL2	W6
AE26	PB6/FCC3_TXD1/L1RXDA2/L1RXD0A2	PSDCAS/PGPL3	AA4
AB23	PB7/FCC3_TXD0/FCC3_TXD/ L1TXDA2/L1TXD0A2	PGTA/PUPMWAIT/PGPL4/PPBS	
AC26	PB8/FCC3_RXD0/FCC3_RXD/TXD3	PSDAMUX/PGPL5	
AB26	PB9/FCC3_RXD1/L1TXD2A2		D15
AA25	PB10/FCC3_RXD2		
W26	PB11/FCC3_RXD3	PSDVAL	
W25	PB12/FCC3_MII_CRS/TXD2		AA5
V24	PB13/FCC3_MII_COL/L1TXD1A2		AE4
U24	PB14/FCC3_MII_TX_EN/RXD3	PWE0/PSDDQM0/PBS0	AD4
R22	PB15/FCC3_MII_TX_ER/RXD2	PWE1/PSDDQM1/PBS1	AF3
R23	PB16/FCC3_MII_RX_ER/CLK18	PWE2/PSDDQM2/PBS2	AB4
M23	PB17/FCC3_MII_RX_DV/CLK17	PWE3/PSDDQM3/PBS3	AE3
L24	PB18/FCC2_RXD3/L1CLKOD2/ L1RXD2A2	PWE4/PSDDQM4/PBS4	AF2
K24	PB19/FCC2_RXD2/L1RQD2/L1RXD3A2	PWE5/PSDDQM5/PBS5	AD3
L21	PB20/FCC2_RXD1/L1RSYNCD2/ L1TXD1A1	PWE6/PSDDQM6/PBS6	
P25	PB21/FCC2_RXD0/FCC2_RXD/ L1TSYNCD2/L1GNTD2	PWE7/PSDDQM7/PBS7	
N25	PB22/FCC2_TXD0/FCC2_TXD/ L1RXDD2		B16
E26	PB23/FCC2_TXD1/L1TXDD2		F16
H23	PB24/FCC2_TXD2/L1RSYNCC2	MODCK1/AP1/TC0/BNKSEL0	A15
C26	PB25/FCC2_TXD3/L1TSYNCC2/ L1GNTC2	MODCK2/AP2/TC1/BNKSEL1	
B26	PB26/FCC2_MII_CRS/L1RXDC2	MODCK3/AP3/TC2/BNKSEL2	
A22	PB27/FCC2_MII_COL/L1TXDC2		
A21	PB28/FCC2_MII_RX_ER/FCC2_RTS/ L1TSYNCB2/L1GNTB2/TXD1		
E20	PB29/L1RSYNCB2/ FCC2_MII_TX_EN		
C20	PB30/FCC2_MII_RX_DV/L1RXDB2		
	PB31/FCC2_MII_TX_ER/L1TXDB2		



U?D

2	PC0/DREQ1/BRGO7/SMSYN2/ L1CLKOA2		
9	PC1/DREQ2/BRGO6/L1RQA2		
AF24	PC2/FCC3_CD/DONE2		B25
AE25	PC3/FCC3_CTS/DACK2/CTS4	PORESET	
AB22	PC4/SI2_L1ST4/FCC2_CD		AE13
AC25	PC5/SI2_L1ST3/FCC2_CTS	L_A14/PAR	
AB25	PC6/FCC1_CD		AD15
AA24	PC7/FCC1_CTS	L_A15/FRAME/SMI	
Y24	PC8/CD4/RENA4/SI2_L1ST2/CTS3		AF16
U22	PC9/CTS4/CLSN4/SI2_L1ST1/ L1TSYNCA2/L1GNTA2	L_A16/TRDY	
V23	PC10/CD3/RENA3		AF15
U23	PC11/CTS3/CLSN3/L1TXD3A2	L_A17/IRDY/CKSTP_OUT	
T26	PC12/CD2/RENA2		AE15
R26	PC13/CTS2/CLSN2	L_A18/STOP	
P26	PC14/CD1/RENA1		AE14
P24	PC15/CTS1/CLSN1/SMTXD2	L_A19/DEVSEL	
M26	PC16/CLK16/TIN4		AC17
L26	PC17/CLK15/TIN3/BRGO8	L_A20/IDSEL	
M24	PC18/CLK14/TGATE2		AD14
L22	PC19/CLK13/BRGO7/SPICLK	L_A21/PERR	
K25	PC20/CLK12/TGATE1		AF13
J25	PC21/CLK11/BRGO6	L_A22/SERR	
G26	PC22/CLK10/DONE1		AE20
F26	PC23/CLK9/BRGO5/DACK1	L_A23/REQ0	
G24	PC24/CLK8/TOUT4		AC14
E25	PC25/CLK7/BRGO4	L_A24/REQ1/HSEJSW	
G23	PC26/CLK6/TOUT3/TMCLK		AC19
B23	PC27/FCC3_TXD/FCC3_TXD0/CLK5/ BRGO3	L_A25/GNT0	
E22	PC28/CLK4/TIN1/TOUT2/CTS2/CLSN2		AD13
E21	PC29/CLK3/TIN2/BRGO2/CTS1/CLSN1	L_A26/GNT1/HSLED	
D21	PC30/CLK2/TOUT1		AF21
B20	PC31/CLK1/BRGO1	L_A27/GNT2/HSENUM	
AF23	PD4/BRGO8/FCC3_RTS/SMRXD2	L_A28/RST/CORE_SRESET	AF22
AE23	PD5/DONE1		AE21
AB21	PD6/DACK1	L_A29/INTA	
AD23	PD7/SMSYN1/FCC1_TXCLAV2		AB14
AD26	PD8/SMRXD1/BRGO5	L_A30/REQ2	
Y22	PD9/SMTXD1/BRGO3		AD20
AB24	PD10/L1CLKOB2/BRGO4	L_A31/DLLOUT	
Y23	PD11/L1RQB2		
AA26	PD12		
W24	PD13		
V22	PD14/L1CLKOC2/I2CSCL		
U26	PD15/L1RQC2/I2CSDA		
T23	PD16/SPIMISO		
R25	PD17/BRGO2/SPIMOSI		E17
P23	PD18/SPICLK	THERMAL0	C23
N22	PD19/SPISEL/BRGO1	THERMAL1	
M25	PD20/RTS4/TENA4/L1RSYNCA2		
L25	PD21/TXD4/L1RXD0A2/L1RXDA2		
J26	PD22/RXD4L1TXD0A2/L1TXDA2		
K22	PD23/RTS3/TENA3		
G25	PD24/TXD3		
H24	PD25/RXD3		
F24	PD26/RTS2/TENA2		
H22	PD27/TXD2	SPARE4	C14
B22	PD28/RXD2	SPARE6	B15
D22	PD29/RTS1/TENA1		
C21	PD30/TXD1		
E19	PD31/RXD1		



U?E

D19	VCCSYN	VDDH1	E6
		VDDH2	F6
		VDDH3	H6
		VDDH4	L5
K6	VCCSYN1	VDDH5	L6
		VDDH6	P6
		VDDH7	T6
		VDDH8	U6
L3	VDD1	VDDH9	V5
		VDDH10	Y5
		VDDH11	AA6
		VDDH12	AA8
V4	VDD2	VDDH13	AA10
W3	VDD3	VDDH14	AA11
AC11	VDD4	VDDH15	AA14
AD11	VDD5	VDDH16	AA16
AB15	VDD6	VDDH17	AA17
U25	VDD7	VDDH18	AB19
T24	VDD8	VDDH19	AB20
J24	VDD9	VDDH20	W21
H25	VDD10	VDDH21	U21
F23	VDD11	VDDH22	T21
B19	VDD12	VDDH23	P21
D17	VDD13	VDDH24	N21
C17	VDD14	VDDH25	M22
D10	VDD15	VDDH26	J22
C10	VDD16	VDDH27	H21
		VDDH28	F21
		VDDH29	F19
		VDDH30	F17
		VDDH31	E16
		VDDH32	F14
		VDDH33	E13
		VDDH34	E12
		VDDH35	F10
		VDDH36	E10
		VDDH37	E9

<Value>



U?F			
B18	GNDSYN	GND43	M11
		GND44	M12
		GND45	M13
		GND46	M14
A2		GND47	M15
B1	GND1	GND48	M16
B2	GND2	GND49	M17
A5	GND3	GND50	N10
C5	GND4	GND51	N11
C18	GND5	GND52	N12
D4	GND6	GND53	N13
D6	GND7	GND54	N14
G2	GND8	GND55	N15
L4	GND9	GND56	N16
P1	GND10	GND57	N17
R1	GND11	GND58	P10
R4	GND12	GND59	P11
AC4	GND13	GND60	P12
AE7	GND14	GND61	P13
AC23	GND15	GND62	P14
Y25	GND16	GND63	P15
N24	GND17	GND64	P16
J23	GND18	GND65	P17
A23	GND19	GND66	R10
D23	GND20	GND67	R11
D20	GND21	GND68	R12
E18	GND22	GND69	R13
A13	GND23	GND70	R14
A16	GND24	GND71	R15
K10	GND25	GND72	R16
K11	GND26	GND73	R17
K12	GND27	GND74	T10
K13	GND28	GND75	T11
K14	GND29	GND76	T12
K15	GND30	GND77	T13
K16	GND31	GND78	T14
K17	GND32	GND79	T15
L10	GND33	GND80	T16
L11	GND34	GND81	T17
L12	GND35	GND82	U10
L13	GND36	GND83	U11
L14	GND37	GND84	U12
L15	GND38	GND85	U13
L16	GND39	GND86	U14
L17	GND40	GND87	U15
M10	GND41	GND88	U16
	GND42	GND89	U17