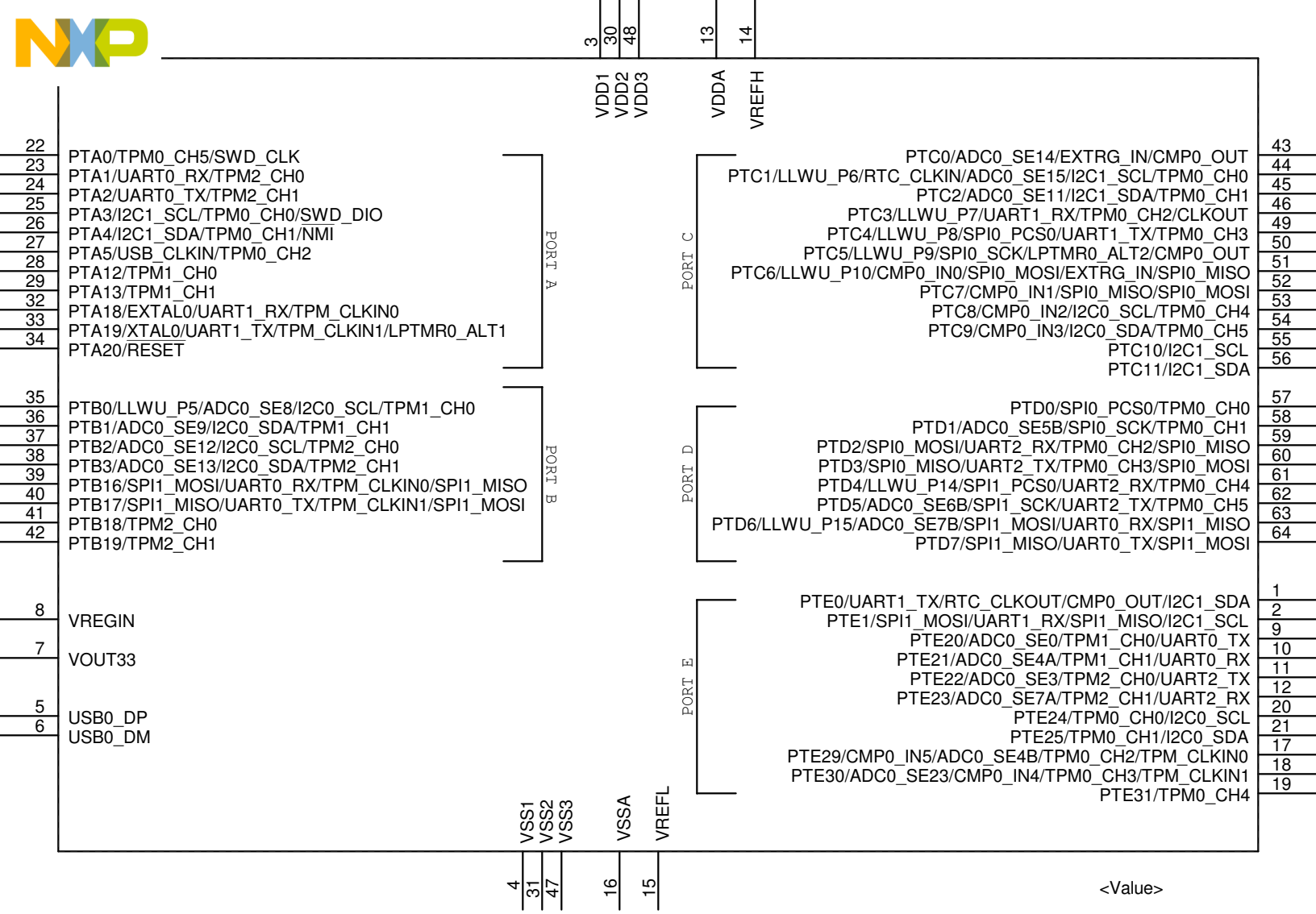
3
30
48
13
14
VDD1
VDD2
VDD3
VDDA
VREFH
22 PTA0/TPM0_CH5/SWD_CLK
23 PTA1/UART0_RX/TPM2_CH0
24 PTA2/UART0_TX/TPM2_CH1
25 PTA3/I2C1_SCL/TPM0_CH0/SWD_DIO
26 PTA4/I2C1_SDA/TPM0_CH1/NMI
27 PTA5/USB_CLKIN/TPM0_CH2
28 PTA12/TPM1_CH0
29 PTA13/TPM1_CH1
32 PTA18/EXTAL0/UART1_RX/TPM_CLKIN0
33 PTA19/XTAL0/UART1_TX/TPM_CLKIN1/LPTMR0_ALT1
34 PTA20/RESET
PORT A
35 PTB0/LLWU_P5/ADC0_SE8/I2C0_SCL/TPM1_CH0
36 PTB1/ADC0_SE9/I2C0_SDA/TPM1_CH1
37 PTB2/ADC0_SE12/I2C0_SCL/TPM2_CH0
38 PTB3/ADC0_SE13/I2C0_SDA/TPM2_CH1
39 PTB16/SPI1_MOSI/UART0_RX/TPM_CLKIN0/SPI1_MISO
40 PTB17/SPI1_MISO/UART0_TX/TPM_CLKIN1/SPI1_MOSI
41 PTB18/TPM2_CH0
42 PTB19/TPM2_CH1
PORT B
8 VREGIN
7 VOUT33
5 USB0_DP
6 USB0_DM
PORT C
43 PTC0/ADC0_SE14/EXTRG_IN/CMP0_OUT
44 PTC1/LLWU_P6/RTC_CLKIN/ADC0_SE15/I2C1_SCL/TPM0_CH0
45 PTC2/ADC0_SE11/I2C1_SDA/TPM0_CH1
46 PTC3/LLWU_P7/UART1_RX/TPM0_CH2/CLKOUT
49 PTC4/LLWU_P8/SPI0_PCS0/UART1_TX/TPM0_CH3
50 PTC5/LLWU_P9/SPI0_SCK/LPTMR0_ALT2/CMP0_OUT
51 PTC6/LLWU_P10/CMP0_IN0/SPI0_MOSI/EXTRG_IN/SPI0_MISO
52 PTC7/CMP0_IN1/SPI0_MISO/SPI0_MOSI
53 PTC8/CMP0_IN2/I2C0_SCL/TPM0_CH4
54 PTC9/CMP0_IN3/I2C0_SDA/TPM0_CH5
55 PTC10/I2C1_SCL
56 PTC11/I2C1_SDA
PORT D
57 PTD0/SPI0_PCS0/TPM0_CH0
58 PTD1/ADC0_SE5B/SPI0_SCK/TPM0_CH1
59 PTD2/SPI0_MOSI/UART2_RX/TPM0_CH2/SPI0_MISO
60 PTD3/SPI0_MISO/UART2_TX/TPM0_CH3/SPI0_MOSI
61 PTD4/LLWU_P14/SPI1_PCS0/UART2_RX/TPM0_CH4
62 PTD5/ADC0_SE6B/SPI1_SCK/UART2_TX/TPM0_CH5
63 PTD6/LLWU_P15/ADC0_SE7B/SPI1_MOSI/UART0_RX/SPI1_MISO
64 PTD7/SPI1_MISO/UART0_TX/SPI1_MOSI
PORT E
1 PTE0/UART1_TX/RTC_CLKOUT/CMP0_OUT/I2C1_SDA
2 PTE1/SPI1_MOSI/UART1_RX/SPI1_MISO/I2C1_SCL
9 PTE20/ADC0_SE0/TPM1_CH0/UART0_TX
10 PTE21/ADC0_SE4A/TPM1_CH1/UART0_RX
11 PTE22/ADC0_SE3/TPM2_CH0/UART2_TX
12 PTE23/ADC0_SE7A/TPM2_CH1/UART2_RX
20 PTE24/TPM0_CH0/I2C0_SCL
21 PTE25/TPM0_CH1/I2C0_SDA
17 PTE29/CMP0_IN5/ADC0_SE4B/TPM0_CH2/TPM_CLKIN0
18 PTE30/ADC0_SE23/CMP0_IN4/TPM0_CH3/TPM_CLKIN1
19 PTE31/TPM0_CH4
VSS1
VSS2
VSS3
VSSA
VREFL
4
31
47
16
15
<Value>