

U?	5	31	6		24
46	PTA0/IRQ_0/LLWU_P7/ADC0_SE12/CMP0_IN2/TPM1_CH0/SWD_CLK	VDD1		PTB0/IRQ_8/LLWU_P4/ADC0_SE6/EXTRG_IN/SPI0_SCK	24
47	PTA1/IRQ_1/LPTMR0_ALT1/TPM_CLKIN0/RESET	VDD2		PTB1/IRQ_9/ADC0_SE5/CMP0_IN3/UART0_TX/UART0_RX	25
48	PTA2/CMP0_OUT/SWD_DIO		VREFH	PTB2/IRQ_10/ADC0_SE4/LLWU_P5/UART0_RX/UART0_TX	26
9	PTA3/EXTAL0/I2C0_SCL/I2C0_SDA			PTB20	29
10	PTA4/LLWU_P0/XTAL0/I2C0_SDA/I2C0_SCL			PTB3/IRQ_14/I2C0_SCL/UART0_TX	35
14	PTA5/LLWU_P1/RTC_CLK_IN/TPM0_CH5/SPI0_SS			PTB4/IRQ_15/LLWU_P6/I2C0_SDA/UART0_RX	36
15	PTA6/LLWU_P2/TPM0_CH4/SPI0_MISO			PTB5/IRQ_16/ADC0_SE1/CMP0_IN1/TPM1_CH1/NMI	37
23	PTA7/IRQ_7/LLWU_P3/ADC0_SE7/SPI0_MISO/SPI0_MOSI			PTB6/IRQ_2/LPTMR0_ALT3/TPM0_CH3/TPM_CLKIN1	1
27	PTA8/ADC0_SE3			PTB7/IRQ_3/TPM0_CH2	2
28	PTA9/ADC0_SE2			PTB8/ADC0_SE11/TPM0_CH3	16
33	PTA10/IRQ_12			PTB9/ADC0_SE10/TPM0_CH2	17
34	PTA11/IRQ_13			PTB10/ADC0_SE9/TPM0_CH1	21
38	PTA12/IRQ_17/LPTMR0_ALT2/ADC0_SE0/CMP0_IN0/TPM1_CH0/TPM_CLKIN0			PTB11/ADC0_SE8/TPM0_CH0	22
39	PTA13			PTB12	40
3	PTA14/TPM_CLKIN0			PTB13/ADC0_SE13/TPM1_CH1/RTC_CLKOUT	45
4	PTA15/CLKOUT			PTB14/IRQ_11/EXTRG_IN	32
18	PTA16/IRQ_4			PTB15/SPI0_MOSI/SPI0_MISO	42
19	PTA17/IRQ_5			PTB16/SPI0_MISO/SPI0_MOSI	43
20	PTA18/IRQ_6			PTB17/TPM_CLKIN1/SPI0_SCK	44
41	PTA19/SPI0_SS			PTB18	12
		VSS1	VREFL	PTB19	13
		VSS2			
		VSS3			
	8	11	30	7	<Value>