

		UP	
		RESET	R16
R6	TAMPER4		
N6	TAMPER5		
M6	TAMPER6		
	TAMPER7		
R1	PGA0_DP/ADC0_DP0/ADC1_DP3		L8
R2	PGA0_DM/ADC0_DM0/ADC1_DM3	PTD0/LLWU_P12/SPI0_PCS0/UART2_RTS/FTM3_CH0/FB_ALE/FB_CS1/FB_TS/I2S1_RXD1	F8
T1	PGA1_DP/ADC1_DP0/ADC0_DP3	PTD1/ADC0_SE5B/SPI0_SCK/UART2_CTS/FTM3_CH1/FB_CS0/I2S1_RXD0	K6
T2	PGA1_DM/ADG1_DM0/ADC0_DM3	PTD2/LLWU_P13/SPI0_SOUT/UART2_RX/FTM3_CH2/FB_AD4/I2S1_RX_FS	J6
N1	PGA2_DP/ADC2_DP0/ADC3_DP3/ADC0_DP1	PTD3/SPI0_SIN/UART2_TX/FTM3_CH3/FB_AD3/I2S1_RX_BCLK	K5
N2	PGA2_DM/ADC2_DM0/ADC3_DM3/ADC0_DM1	PTD4/LLWU_P14/SPI0_PCS1/UART0_RTS/FTM0_CH4/FB_AD2/NFC_DATA1/EWM_IN	J5
P1	PGA3_DP/ADC3_DP0/ADC2_DP3/ADC1_DP1	PTD5/ADC0_SE6B/SPI0_PCS2/UART0_CTS/UART0_COL/FTM0_CH5/FB_AD1/NFC_DATA0/EWM_OUT	K4
P2	PGA3_DM/ADC3_DM0/ADC2_DM3/ADC1_DM1	PTD6/LLWU_P15/ADC0_SE7B/SPI0_PCS3/UART0_RX/FTM0_CH6/FB_AD0/FTM0_FLT0	E7
R3	DAC0_OUT/CMP1_IN3/ADC0_SE23	PTD7/CMT_IRO/UART0_TX/FTM0_CH7/FTM0_FLT1	J4
R4	DAC1_OUT/CMP0_IN4/CMP2_IN3/ADC1_SE23	PTD8/I2C0_SCL/UART5_RX/FB_A16/NFC_CLE	F7
N3	ADC0_SE16/CMP1_IN2/ADC0_SE21	PTD9/I2C0_SDA/UART5_TX/FB_A17/NFC_ALE	E6
P3	ADC1_SE16/CMP2_IN2/ADC0_SE22	PTD10/UART5_RTS/FB_A18/NFC_RE	G5
M1	USB0_DP	PTD11/SPI2_PCS0/UART5_CTS/SDHC0_CLKIN/FB_A19/GLCD_CONTRAST	F5
M2	USB0_DM	PTD12/SPI2_SCK/FTM3_FLT0/SDHC0_D4/FB_A20/GLCD_PCLK	F4
T5	EXTAL32	PTD13/SPI2_SOUT/SDHC0_D5/FB_A21/GLCD_DE	E5
T6	XTAL32	PTD14/SPI2_SIN/SDHC0_D6/FB_A22/GLCD_HFS	E4
		PTD15/SPI2_PCS1/SDHC0_D7/FB_A23/GLCD_VFS	
T7	PTA0/JTAG_TCLK/SWD_CLK/EZP_CLK/TSIO_CH1/UART0_CTS/UART0_COL/FTM0_CH5	PTE0/ADC1_SE4A/SPI1_PCS1/UART1_TX/SDHC0_D1/GLCD_D0/I2C1_SDA/RTC_CLKOUT	E2
N6	PTA1/JTAG_TD1/EZP_DI/TSIO_CH2/UART0_RX/FTM0_CH6	PTE1/LLWU_P0/ADC1_SE5A/SPI1_SOUT/UART1_RX/SDHC0_D0/GLCD_D1/I2C1_SCL/SPI1_SIN	F2
T8	PTA2/JTAG_TDO/TRACE_SWO/EZP_DO/TSIO_CH3/UART0_TX/FTM0_CH7	PTE2/LLWU_P1/ADC1_SE6A/SPI1_SCK/UART1_CTS/SDHC0_DCLK/GLCD_D2	F3
P8	PTA3/JTAG_TMS/SWD_DIO/TSIO_CH4/UART0_RTS/FTM0_CH0	PTE3/ADC1_SE7A/SPI1_SIN/UART1_RTS/SDHC0_CMD/GLCD_D3/SPI1_SOUT	G2
R8	PTA4/LLWU_P3/NMI/EZP_CS/TSIO_CH5/FTM0_CH1	PTE4/LLWU_P2/SPI1_PCS0/UART3_TX/SDHC0_D3/GLCD_D4	G3
T12	PTA5/USB_CLKIN/FTM0_CH2/RMII0_RXER/MII0_RXER/I2S0_TX_BCLK/JTAG_TRST/CMP2_OUT	PTE5/SPI1_PCS2/UART3_RX/SDHC0_D2/GLCD_D5/FTM3_CH0	G4
R12	PTA6/ADC3_SE6A/ULPI_CLK/FTM0_CH3/I2S1_RXD0/TRACE_CLKOUT/CLKOUT	PTE6/SPI1_PCS3/UART3_CTS/I2S0_MCLK/GLCD_D6/FTM3_CH1/USB_SOF_OUT	H3
P12	PTA7/ADC0_SE10/ULPI_DIR/FTM0_CH4/I2S1_RX_BCLK/TRACE_D3	PTE7/UART3_RTS/I2S0_RXD0/GLCD_D7/FTM3_CH2	H4
N12	PTA8/ADC0_SE11/ULPI_NXT/FTM1_CH0/I2S1_RX_FS/FTM1_QD_PHA/TRACE_D2	PTE8/ADC2_SE16/I2S0_RXD1/UART5_TX/I2S0_RX_FS/GLCD_D8/FTM3_CH3	J1
T13	PTA9/ADC3_SE5A/ULPI_STP/FTM1_CH1/MII0_RXD3/FTM1_QD_PHB/TRACE_D1	PTE9/ADC2_SE17/I2S0_TXD1/UART5_RX/I2S0_RX_BCLK/GLCD_D9/FTM3_CH4	J2
P13	PTA10/ADC3_SE4A/ULPI_DATA0/FTM2_CH0/MII0_RXD2/FTM2_QD_PHA/TRACE_D0	PTE10/UART5_CTS/I2S0_TXD0/GLCD_D10/FTM3_CH5	K1
R13	PTA11/ADC3_SE15/ULPI_DATA1/FTM2_CH1/MII0_RXCLK/FTM2_QD_PHB	PTE11/ADC3_SE16/UART5_RTS/I2S0_TX_FS/GLCD_D11/FTM3_CH6	K3
M10	PTA12/CAN0_TX/FTM1_CH0/RMII0_RXD1/MII0_RXD1/I2S0_TXD0/FTM1_QD_PHA/CMP2_IN0	PTE12/ADC3_SE17/I2S0_TX_BCLK/GLCD_D12/FTM3_CH7	J3
N10	PTA13/LLWU_P4/CMP2_IN1/CAN0_RX/FTM1_CH1/RMII0_RXD0/MII0_RXD0/I2S0_TX_FS/FTM1_QD_PHB	PTE16/ADC0_SE4A/SPI0_PCS0/UART2_TX/FTM_CLKIN0/FTM0_FLT3	K2
R11	PTA14/CMP3_IN0/SPI0_PCS0/UART0_TX/RMII0_CRS_DV/MII0_RXDV/I2S0_RX_BCLK/I2S0_TXD1	PTE17/ADC0_SE5A/SPI0_SCK/UART2_RX/FTM_CLKIN1/LPTMR0_ALT3	L4
P11	PTA15/CMP3_IN1/SPI0_SCK/UART0_RX/RMII0_TXEN/MII0_TXEN/I2S0_RXD0	PTE18/ADC0_SE6A/SPI0_SOUT/UART2_RTS/I2C0_SCL/CMP3_OUT	M3
N11	PTA16/CMP3_IN2/SPI0_SOUT/UART0_CTS/UART0_COL/RMII0_TXD0/MII0_TXD0/I2S0_RX_FS/I2S0_RXD1	PTE19/ADC0_SE7A/SPI0_SIN/UART2_RTS/I2C0_SCL/CMP3_OUT	P7
T11	PTA17/ADC1_SE17/SPI0_SIN/UART0_RTS/RMII0_TXD1/MII0_TXD1/I2S0_MCLK	PTE24/ADC0_SE17/EXTAL1/CAN1_TX/UART4_TX/I2S1_TX_FS/GLCD_D13/SDHC0_OUT/I2S1_RXD1	R7
T15	PTA18/EXTAL0/FTM0_FLT2/FTM_CLKIN0	PTE25/ADC0_SE18/XTAL1/CAN1_RX/UART4_RX/I2S1_TX_BCLK/GLCD_D14/EWM_IN/I2S1_TXD1	M7
N15	PTA19/XTAL0/FTM1_FLT0/FTM_CLKIN1/LPTMR0_ALT1	PTE26/ADC3_SE5B/ENET_1588_CLKIN/UART4_CTS/I2S1_TXD0/GLCD_D15/RTC_CLKOUT/USB_CLKIN	K7
R14	PTA24/CMP3_IN4/ULPI_DATA2/MII0_TXD2/FB_A29	PTE27/ADC3_SE4B/UART4_RTS/I2S1_MCLK/GLCD_D16	L7
M13	PTA25/CMP3_IN5/ULPI_DATA3/MII0_TXCLK/FB_A28	PTE28/ADC3_SE7A/GLCD_D17	
R15	PTA26/ADC2_SE15/ULPI_DATA4/MII0_TXD3/FB_A27		
P14	PTA27/ADC2_SE14/ULPI_DATA5/MII0_CRS/FB_A26		
N14	PTA28/ADC2_SE13/ULPI_DATA6/MII0_TXER/FB_A25		
	PTA29/ADC2_SE12/ULPI_DATA7/MII0_COL/FB_A24		
M12	PTB0/LLWU_P5/ADC0_SE8/ADC1_SE8/ADC2_SE8/ADC3_SE8/TSIO_CH0/I2C0_SCL/FTM1_CH0/RMII0_MDIO/MII0_MDIO/FTM1_QD_PHA	PTF0/ADC2_SE11/CAN0_TX/FTM3_CH0/I2S1_RXD1/GLCD_PCLK	P16
P15	PTB1/ADC0_SE9/ADC1_SE9/ADC2_SE9/ADC3_SE9/TSIO_CH6/I2C0_SDA/FTM1_CH1/RMII0_MDC/MII0_MDC/FTM1_QD_PHB	PTF1/ADC2_SE10/CAN0_RX/FTM3_CH1/I2S1_RX_BCLK/GLCD_DE	L13
M14	PTB2/ADC0_SE12/TSIO_CH7/I2C0_SCL/UART0_RTS/ENET0_1588_TMR0/FTM0_FLT3	PTF2/ADC2_SE6A/I2C1_SCL/FTM3_CH2/I2S1_RX_FS/GLCD_HFS	N16
N15	PTB4/ADC0_SE13/TSIO_CH4/I2C0_SDA/UART0_CTS/UART0_COL/ENET0_1588_TMR1/FTM0_FLT0	PTF3/ADC2_SE7A/I2C1_SDA/FTM3_CH3/I2S1_RXD0/GLCD_VFS	M16
M15	PTB5/ADC1_SE11/ENET0_1588_TMR2/FTM1_FLT0	PTF4/ADC2_SE4B/FTM3_CH4/I2S1_TXD0/GLCD_D0	K16
L14	PTB6/ADC1_SE12/FB_AD23	PTF5/ADC2_SE5B/FTM3_CH5/I2S1_TX_FS/GLCD_D1	J16
L15	PTB7/ADC1_SE13/FB_AD22	PTF6/ADC2_SE6B/FTM3_CH6/I2S1_TX_BCLK/GLCD_D2	F15
K14	PTB8/UART3_RTS/FB_AD21	PTF7/ADC2_SE7B/FTM3_CH7/UART3_RX/I2S1_TXD1/GLCD_D3	F16
K15	PTB9/SPI1_PCS1/UART3_CTS/FB_AD20	PTF8/FTM3_FLT0/UART3_TX/I2S1_MCLK/GLCD_D4	K12
J13	PTB10/ADC1_SE14/SPI1_PCS0/UART3_RX/I2S1_TX_BCLK/FB_AD19/FTM0_FLT1	PTF9/CMP2_IN4/UART3_RTS/GLCD_D5	L12
J14	PTB11/ADC1_SE15/SPI1_SCK/UART3_TX/I2S1_TX_FS/FB_AD18/FTM0_FLT2	PTF10/CMP2_IN5/UART3_CTS/GLCD_D6	K11
H13	PTB16/TSIO_CH9/SPI1_SOUT/UART0_RX/I2S1_TXD0/FB_AD17/EWM_IN	PTF11/UART2_RTS/GLCD_D7	L11
H14	PTB17/TSIO_CH10/SPI1_SIN/UART0_TX/I2S1_TXD1/FB_AD16/EWM_OUT	PTF12/UART2_CTS/GLCD_D8	H6
H15	PTB18/TSIO_CH11/CAN0_TX/FTM2_CH0/I2S0_TX_BCLK/FB_AD15/FTM2_QD_PHA	PTF13/UART2_RX/GLCD_D9	G6
G13	PTB19/TSIO_CH12/CAN0_RX/FTM2_CH1/I2S0_TX_FS/FB_OE/FTM2_QD_PHB	PTF14/UART2_TX/GLCD_D10	F6
G14	PTB20/ADC2_SE4A/SPI2_PCS0/FB_AD31/NFC_DATA15/CMP0_OUT	PTF15/UART0_RTS/GLCD_D11	E1
G15	PTB21/ADC2_SE5A/SPI2_SCK/FB_AD30/NFC_DATA14/CMP1_OUT	PTF16/SPI2_PCS0/FTM0_CH3/UART0_CTS/UART0_COL/GLCD_D12	F1
H16	PTB22/SPI2_SOUT/FB_AD29/NFC_DATA13/CMP2_OUT	PTF17/SPI2_SCK/FTM0_CH4/UART0_RX/GLCD_D13	G1
	PTB23/SPI2_SIN/SPI0_PCS5/FB_AD28/NFC_DATA12/CMP3_OUT	PTF18/SPI2_SOUT/FTM1_CH0/UART0_TX/GLCD_D14	H1
G16	PTC0/ADC0_SE14/TSIO_CH13/SPI0_PCS4/PDB0_EXTRG/FB_AD14/NFC_DATA11/I2S0_TXD1	PTF19/SPI2_SIN/FTM1_CH1/UART5_RX/GLCD_D15	H5
F13	PTC1/LLWU_P6/ADC0_SE15/TSIO_CH14/SPI0_PCS3/UART1_RTS/FTM0_CH0/FB_AD13/NFC_DATA10/I2S0_TXD0	PTF20/SPI2_PCS1/FTM2_CH0/UART5_TX/GLCD_D16	P9
F14	PTC2/ADC0_SE4B/CMP1_IN0/TSIO_CH15/SPI0_PCS2/UART1_CTS/FTM0_CH1/FB_AD12/NFC_DATA9/I2S0_TX_FS	PTF21/ADC3_SE6B/FTM2_CH1/UART5_RTS/GLCD_D17	N9
E13	PTC3/LLWU_P7/CMP1_IN1/SPI0_PCS1/UART1_RX/FTM0_CH2/CLKOUT/I2S0_TX_BCLK	PTF22/ADC3_SE7B/I2C0_SCL/FTM1_CH0/UART5_CTS/GLCD_D18	P10
E14	PTC4/LLWU_P8/SPI0_PCS0/UART1_TX/FTM0_CH3/FB_AD11/NFC_DATA8/I2S1_TX_BCLK/CMP1_OUT	PTF23/ADC3_SE10/I2C0_SDA/FTM1_CH1/TRACE_CLKOUT/GLCD_D19	R10
E15	PTC5/LLWU_P9/SPI0_SCK/LPTMR0_ALT2/I2S0_RXD0/FB_AD10/NFC_DATA7/CMP0_OUT/I2S1_TX_FS	PTF24/ADC3_SE11/CAN1_RX/FTM1_QD_PHA/TRACE_D3/GLCD_D20	R9
F12	PTC6/LLWU_P10/CMP0_IN0/SPI0_SOUT/PDB0_EXTRG/I2S0_RX_BCLK/FB_AD9/NFC_DATA6/I2S0_MCLK	PTF25/ADC3_SE12/CAN1_TX/FTM1_QD_PHB/TRACE_D2/GLCD_D21	T9
G12	PTC7/CMP0_IN1/SPI0_SIN/USB_SOF_OUT/I2S0_RX_FS/FB_AD8/NFC_DATA5	PTF26/ADC3_SE13/FTM2_QD_PHA/TRACE_D1/GLCD_D22	T10
H12	PTC8/ADC1_SE4B/CMP0_IN2/FTM3_CH4/I2S0_MCLK/FB_AD7/NFC_DATA4	PTF27/ADC3_SE14/FTM2_QD_PHB/TRACE_D0/GLCD_D23	
F11	PTC9/ADC1_SE5B/CMP0_IN3/FTM3_CH5/I2S0_RX_BCLK/FB_AD6/NFC_DATA3/FTM2_FLT0		
G11	PTC10/ADC1_SE6B/I2C1_SCL/FTM3_CH6/I2S0_RX_FS/FB_AD5/NFC_DATA2/I2S1_MCLK		
H11	PTC11/LLWU_P11/ADC1_SE7B/I2C1_SDA/FTM3_CH7/I2S0_RXD1/FB_RW/NFC_WE		
J12	PTC12/UART4_RTS/FB_AD27/FTM3_FLT0		
K13	PTC13/UART4_CTS/FB_AD26		
J11	PTC14/UART4_RX/FB_AD25		
F10	PTC15/UART4_TX/FB_AD24		
F9	PTC16/CAN1_RX/UART3_RX/ENET0_1588_TMR0/FB_CS5/FB_TSI21/FB_BE23_16/NFC_RB		
E9	PTC17/CAN1_TX/UART3_TX/ENET0_1588_TMR1/FB_CS4/FB_TSI20/FB_BE31_24/NFC_CE0		
M9	PTC18/UART3_RTS/ENET0_1588_TMR2/FB_TBST/FB_CS2/FB_BE15_8/NFC_CE1		
M8	PTC19/UART3_CTS/ENET0_1588_TMR3/FB_CS3/FB_BE7_0/FB_TA		
		<Value>	

NXP			VREFL	M4
			VSSA	N4
B11	DDR_A1	VREF_OUT/CMP1_IN5/CMP0_IN5/ADC1_SE18		T3
C12	DDR_A2			P5
C10	DDR_A3		VBAT	
A13	DDR_A4			C5
A14	DDR_A5		DDR_DQ0	B4
D11	DDR_A6		DDR_DQ1	A4
A15	DDR_A7		DDR_DQ2	B6
B15	DDR_A8		DDR_DQ3	D6
B13	DDR_A9		DDR_DQ4	A6
B14	DDR_A10		DDR_DQ5	B7
C15	DDR_A11		DDR_DQ6	A8
D16	DDR_A12		DDR_DQ7	D1
D15	DDR_A13		DDR_DQ8	C1
	DDR_A14		DDR_DQ9	C2
A9			DDR_DQ10	B2
B10	DDR_BA0		DDR_DQ11	C3
B9	DDR_BA1		DDR_DQ12	C4
	DDR_BA2		DDR_DQ13	B3
A11			DDR_DQ14	A2
A10	DDR_CK		DDR_DQ15	
B16	DDR_CK			C13
	DDR_CKE		DDR_CAS	D14
D9			DDR_CS	E16
A3	DDR_DM0		DDR_RAS	D13
	DDR_DM1		DDR_WE	
C8				C6
D3	DDR_DQS0		DDR_VREF	A7
	DDR_DQS1		DDR_ODT	
B1				A1
D2	DDR_VDD1		DDR_VSS1	A12
D4	DDR_VDD2		DDR_VSS2	A16
B5	DDR_VDD3		DDR_VSS3	A5
D7	DDR_VDD4		DDR_VSS4	C7
B8	DDR_VDD5		DDR_VSS5	C9
E10	DDR_VDD6		DDR_VSS6	D5
B12	DDR_VDD7		DDR_VSS7	C14
E12	DDR_VDD8		DDR_VSS8	D8
C16	DDR_VDD9		DDR_VSS9	E11
	DDR_VDD10		DDR_VSS10	E3
			DDR_VSS11	D12
			DDR_VSS12	E8
			DDR_VSS13	
H7	VDDINT1			L2
H10	VDDINT2		VSS1	T4
G7			VSS2	N7
J7	VDD1		VSS3	H8
G8	VDD2		VSS4	J8
G9	VDD3		VSS5	K8
G10	VDD4		VSS6	H9
J10	VDD5		VSS7	J9
K10	VDD6		VSS8	K9
L10	VDD7		VSS9	L9
N5	VDD8		VSS10	T14
	VDDA		VSS11	
P4	VREFH			
L3	VREGIN			L1
		VOUT33		

<Value>