U?

Pin	Signal
38	VBAT
7	VDD1
61	VDD3
48	VDD2
75	VDD4
89	VDD5
21	VDDA
22	VREFH
37	EXTAL32
36	XTAL32
39	PTA0/TSI0_CH1/UART0_CTS/UART0_COL/FTM0_CH5/JTAG_TCLK/SWD_CLK/EZP_CLK
40	PTA1/TSI0_CH2/UART0_RX/FTM0_CH6/JTAG_TDI/EZP_DI
41	PTA2/TSI0_CH3/UART0_TX/FTM0_CH7/JTAG_TDO/TRACE_SWO/EZP_DO
42	PTA3/TSI0_CH4/UART0_RTS/FTM0_CH0/JTAG_TMS/SWD_DIO
43	PTA4/LLWU_P3/TSI0_CH5/FTM0_CH1/NMI/EZP_CS
44	PTA12/CMP2_IN0/FTM1_CH0/I2S0_TXD0/FTM1_QD_PHA
45	PTA13/LLWU_P4/CMP2_IN1/FTM1_CH1/I2S0_TX_FS/FTM1_QD_PHB
46	PTA14/SPI0_PCS0/UART0_TX/I2S0_RX_BCLK/I2S0_TXD1
47	PTA15/SPI0_SCK/UART0_RX/I2S0_RXD0
50	PTA18/EXTAL0/FTM0_FLT2/FTM_CLKIN0
51	PTA19/XTAL0/FTM1_FLT0/FTM_CLKIN1/LPTMR0_ALT1
53	PTB0/LLWU_P5/ADC0_SE8/ADC1_SE8/TSI0_CH0/I2C0_SCL/FTM1_CH0/FTM1_QD_PHA
54	PTB1/ADC0_SE9/ADC1_SE9/TSI0_CH6/I2C0_SDA/FTM1_CH1/FTM1_QD_PHB
55	PTB2/ADC0_SE12/TSI0_CH7/I2C0_SCL/UART0_RTS/FTM0_FLT3
56	PTB3/ADC0_SE13/TSI0_CH8/I2C0_SDA/UART0_CTS/UART0_COL/FTM0_FLT0
57	PTB9/SPI1_PCS1/UART3_CTS/FB_AD20
58	PTB10/ADC1_SE14/SPI1_PCS0/UART3_RX/FB_AD19/FTM0_FLT1
59	PTB11/ADC1_SE15/SPI1_SCK/UART3_TX/FB_AD18/FTM0_FLT2
62	PTB16/TSI0_CH9/SPI1_SOUT/UART0_RX/FB_AD17/EWM_IN
63	PTB17/TSI0_CH10/SPI1_SIN/UART0_TX/FB_AD16/EWM_OUT
64	PTB18/TSI0_CH11/FTM2_CH0/I2S0_TX_BCLK/FB_AD15/FTM2_QD_PHA
65	PTB19/TSI0_CH12/FTM2_CH1/I2S0_TX_FS/FB_OE/FTM2_QD_PHB
66	PTB20/FB_AD31/CMP0_OUT
67	PTB21/FB_AD30/CMP1_OUT
68	PTB22/FB_AD29/CMP2_OUT
69	PTB23/SPI0_PCS5/FB_AD28
13	ADC0_DP1/OP0_DP0
14	ADC0_DM1/OP0_DM0
15	ADC1_DP1/OP1_DP0/OP1_DM1
16	ADC1_DM1/OP1_DM0
17	PGA0_DP/ADC0_DP0/ADC1_DP3
18	PGA0_DM/ADC0_DM0/ADC1_DM3
19	PGA1_DP/ADC1_DP0/ADC0_DP3
20	PGA1_DM/ADC1_DM0/ADC0_DM3
35	CMP0_IN4/CMP2_IN3/ADC1_SE23/OP0_DP5/OP1_DP5
33	CMP2_IN5/ADC1_SE22
30	TRI0_DP
29	TRI0_DM
28	TRI0_OUT/OP1_DM2
52	RESET
31	NC_31
32	NC_32
70	PTC0/ADC0_SE14/TSI0_CH13/SPI0_PCS4/PDB0_EXTRG/FB_AD14/I2S0_TXD1
71	PTC1/LLWU_P6/ADC0_SE15/TSI0_CH14/SPI0_PCS3/UART1_RTS/FTM0_CH0/FB_AD13/I2S0_TXD0
72	PTC2/ADC0_SE4B/CMP1_IN0/TSI0_CH15/SPI0_PCS2/UART1_CTS/FTM0_CH1/FB_AD12/I2S0_TX_FS
73	PTC3/LLWU_P7/CMP1_IN1/SPI0_PCS1/UART1_RX/FTM0_CH2/CLKOUT/I2S0_TX_BCLK
76	PTC4/LLWU_P8/SPI0_PCS0/UART1_TX/FTM0_CH3/FB_AD11/CMP1_OUT
77	PTC5/LLWU_P9/SPI0_SCK/LPTMR0_ALT2/I2S0_RXD0/FB_AD10/CMP0_OUT
78	PTC6/LLWU_P10/CMP0_IN0/SPI0_SOUT/PDB0_EXTRG/I2S0_RX_BCLK/FB_AD9/I2S0_MCLK
79	PTC7/CMP0_IN1/SPI0_SIN/USB_SOF_OUT/I2S0_RX_FS/FB_AD8
80	PTC8/ADC1_SE4B/CMP0_IN2/I2S0_MCLK/FB_AD7
81	PTC9/ADC1_SE5B/CMP0_IN3/I2S0_RX_BCLK/FB_AD6/FTM2_FLT0
82	PTC10/ADC1_SE6B/I2C1_SCL/I2S0_RX_FS/FB_AD5
83	PTC11/LLWU_P11/ADC1_SE7B/I2C1_SDA/I2S0_RXD1/FB_RW
84	PTC12/UART4_RTS/FB_AD27
85	PTC13/UART4_CTS/FB_AD26
86	PTC14/UART4_RX/FB_AD25
87	PTC15/UART4_TX/FB_AD24
90	PTC16/UART3_RX/FB_CS5/FB_TSIZ1/FB_BE23_16
91	PTC17/UART3_TX/FB_CS4/FB_TSIZ0/FB_BE31_24
92	PTC18/UART3_RTS/FB_TBST/FB_CS2/FB_BE15_8
93	PTD0/LLWU_P12/SPI0_PCS0/UART2_RTS/FB_ALE/FB_CS1/FB_TS
94	PTD1/ADC0_SE5B/SPI0_SCK/UART2_CTS/FB_CS0
95	PTD2/LLWU_P13/SPI0_SOUT/UART2_RX/FB_AD4
96	PTD3/SPI0_SIN/UART2_TX/FB_AD3
97	PTD4/LLWU_P14/SPI0_PCS1/UART0_RTS/FTM0_CH4/FB_AD2/EWM_IN
98	PTD5/ADC0_SE6B/SPI0_PCS2/UART0_CTS/UART0_COL/FTM0_CH5/FB_AD1/EWM_OUT
99	PTD6/LLWU_P15/ADC0_SE7B/SPI0_PCS3/UART0_RX/FTM0_CH6/FB_AD0/FTM0_FLT0
100	PTD7/CMT_IRO/UART0_TX/FTM0_CH7/FTM0_FLT1
1	PTE0/ADC1_SE4A/SPI1_PCS1/UART1_TX/I2C1_SDA/RTC_CLKOUT
2	PTE1/LLWU_P0/ADC1_SE5A/SPI1_SOUT/UART1_RX/I2C1_SCL/SPI1_SIN
3	PTE2/LLWU_P1/ADC1_SE6A/SPI1_SCK/UART1_CTS
4	PTE3/ADC1_SE7A/SPI1_SIN/UART1_RTS/SPI1_SOUT
5	PTE4/LLWU_P2/SPI1_PCS0/UART3_TX
6	PTE5/SPI1_PCS2/UART3_RX
11	VOUT33
12	VREGIN
9	USB0_DP
10	USB0_DM
27	VREF_OUT/CMP1_IN5/CMP0_IN5/ADC1_SE18
34	DAC0_OUT/CMP1_IN3/ADC0_SE23/OP0_DP4/OP1_DP4
26	ADC0_SE16/OP0_OUT/CMP1_IN2/ADC0_SE21/OP0_DP1/OP1_DP1
25	ADC1_SE16/OP1_OUT/CMP2_IN2/ADC0_SE22/OP0_DP2/OP1_DP2
8	VSS1
49	VSS2
60	VSS3
74	VSS4
88	VSS5
24	VSSA
23	VREFL

<Value>