Pin	Signal
L8	VBAT
E7	VDD1
F7	VDD2
G5	VDD3
L2	VDD4
E5	VDD5
G7	VDD6
C4	VDD7
K2	VDD8
K11	VDDA
K10	VREFH
L10	XTAL32
K6	PTA0/TSI0_CH1/UART0_CTS/FTM0_CH5/JTAG_TCLK/SWD_CLK/EZP_CLK
J6	PTA1/TSI0_CH2/UART0_RX/FTM0_CH6/JTAG_TDI/EZP_DI
H5	PTA2/TSI0_CH3/UART0_TX/FTM0_CH7/JTAG_TDO/TRACE_SWO/EZP_DO
J5	PTA3/TSI0_CH4/UART0_RTS/FTM0_CH0/JTAG_TMS/SWD_DIO
K5	PTA4/LLWU_P3/TSI0_CH5/FTM0_CH1/NMI/EZP_CS
L5	PTA5/FTM0_CH2/CMP2_OUT/I2S0_RX_BCLK/JTAG_TRST
L4	PTA12/CMP2_IN0/CAN0_TX/FTM1_CH0/I2S0_TXD/FTM1_QD_PHA
K4	PTA13/LLWU_P4/CMP2_IN1/CAN0_RX/FTM1_CH1/I2S0_TX_FS/FTM1_QD_PHB
J4	PTA14/SPI0_PCS0/UART0_TX/I2S0_TX_BCLK
L3	PTA15/SPI0_SCK/UART0_RX/I2S0_RXD
K3	PTA16/SPI0_SOUT/UART0_CTS/I2S0_RX_FS
J3	PTA17/ADC1_SE17/SPI0_SIN/UART0_RTS/I2S0_MCLK/I2S0_CLKIN
L1	PTA18/EXTAL/FTM0_FLT2/FTM_CLKIN0
K1	PTA19/XTAL/FTM1_FLT0/FTM_CLKIN1/LPT0_ALT1
H4	PTB0/LLWU_P5/ADC0_SE8/ADC1_SE8/TSI0_CH0/I2C0_SCL/FTM1_CH0/FTM1_QD_PHA
H3	PTB1/ADC0_SE9/ADC1_SE9/TSI0_CH6/I2C0_SDA/FTM1_CH1/FTM1_QD_PHB
H2	PTB2/ADC0_SE12/TSI0_CH7/I2C0_SCL/UART0_RTS/FTM0_FLT3
H1	PTB3/ADC0_SE13/TSI0_CH8/I2C0_SDA/UART0_CTS/FTM0_FLT0
G4	PTB4/ADC1_SE10/FTM1_FLT0
G3	PTB5/ADC1_SE11/FTM2_FLT0
G2	PTB6/ADC1_SE12/FB_AD23
G1	PTB7/ADC1_SE13/FB_AD22
F4	PTB8/UART3_RTS/FB_AD21
F3	PTB9/SPI1_PCS1/UART3_CTS/FB_AD20
F2	PTB10/ADC1_SE14/SPI1_PCS0/UART3_RX/FB_AD19/FTM0_FLT1
F1	PTB11/ADC1_SE15/SPI1_SCK/UART3_TX/FB_AD18/FTM0_FLT2
E1	PTB16/TSI0_CH9/SPI1_SOUT/UART0_RX/FB_AD17/EWM_IN
E2	PTB17/TSI0_CH10/SPI1_SIN/UART0_TX/FB_AD16/EWM_OUT
E3	PTB18/TSI0_CH11/CAN0_TX/FTM2_CH0/I2S0_TX_BCLK/FB_AD15/FTM2_QD_PHA
E4	PTB19/TSI0_CH12/CAN0_RX/FTM2_CH1/I2S0_TX_FS/FB_OE/FTM2_QD_PHB
D1	PTB20/SPI2_PCS0/FB_AD31/CMP0_OUT
D2	PTB21/SPI2_SCK/FB_AD30/CMP1_OUT
D5	PTB22/SPI2_SOUT/FB_AD29/CMP2_OUT
D4	PTB23/SPI2_SIN/SPI0_PCS5/FB_AD28
H11	ADC0_DP1
H10	ADC0_DM1
H9	ADC1_DP1
H8	ADC1_DM1
J11	PGA0_DP/ADC0_DP0/ADC1_DP3
J10	PGA0_DM/ADC0_DM0/ADC1_DM3
J9	PGA1_DP/ADC1_DP0/ADC0_DP3
J8	PGA1_DM/ADC1_DM0/ADC0_DM3
L11	DAC0_OUT/CMP1_IN3/ADC0_SE23
K7	DAC1_OUT/CMP2_IN3/ADC1_SE23
J1	RESET
A1	NC_A1
A2	NC_A2
B2	NC_B2
B3	NC_B3
C3	NC_C3
D3	PTC0/ADC0_SE14/TSI0_CH13/SPI0_PCS4/PDB0_EXTRG/I2S0_TXD/FB_AD14
C1	PTC1/LLWU_P6/ADC0_SE15/TSI0_CH14/SPI0_PCS3/UART1_RTS/FTM0_CH0/FB_AD13
C2	PTC2/ADC0_SE4B/CMP1_IN0/TSI0_CH15/SPI0_PCS2/UART1_CTS/FTM0_CH1/FB_AD12
B1	PTC3/LLWU_P7/CMP1_IN1/SPI0_PCS1/UART1_RX/FTM0_CH2/FB_CLKOUT
A3	PTC4/LLWU_P8/SPI0_PCS0/UART1_TX/FTM0_CH3/FB_AD11/CMP1_OUT
B4	PTC5/LLWU_P9/SPI0_SCK/LPT0_ALT2/FB_AD10/CMP0_OUT
C5	PTC6/LLWU_P10/CMP0_IN0/SPI0_SOUT/PDB0_EXTRG/FB_AD9
B5	PTC7/CMP0_IN1/SPI0_SIN/FB_AD8
A4	PTC8/ADC1_SE4B/CMP0_IN2/I2S0_MCLK/I2S0_CLKIN/FB_AD7
C6	PTC9/ADC1_SE5B/CMP0_IN3/I2S0_RX_BCLK/FB_AD6/FTM2_FLT0
D6	PTC10/ADC1_SE6B/CMP0_IN4/I2C1_SCL/I2S0_RX_FS/FB_AD5
A5	PTC11/LLWU_P11/ADC1_SE7B/I2C1_SDA/I2S0_RXD/FB_RW
D7	PTC12/UART4_RTS/FB_AD27
B6	PTC13/UART4_CTS/FB_AD26
C7	PTC14/UART4_RX/FB_AD25
A6	PTC15/UART4_TX/FB_AD24
A7	PTC16/CAN1_RX/UART3_RX/FB_CS5/FB_TSIZ1/FB_BE23_16
B7	PTC17/CAN1_TX/UART3_TX/FB_CS4/FB_TSIZ0/FB_BE31_24
C8	PTC18/UART3_RTS/FB_TBST/FB_CS2/FB_BE15_8
D8	PTC19/UART3_CTS/FB_CS3/FB_BE7_0/FB_TA
B8	PTD0/LLWU_P12/SPI0_PCS0/UART2_RTS/FB_ALE/FB_CS1/FB_TS
A8	PTD1/ADC0_SE5B/SPI0_SCK/UART2_CTS/FB_CS0
D9	PTD2/LLWU_P13/SPI0_SOUT/UART2_RX/FB_AD4
C9	PTD3/SPI0_SIN/UART2_TX/FB_AD3
B9	PTD4/LLWU_P14/SPI0_PCS1/UART0_RTS/FTM0_CH4/FB_AD2/EWM_IN
A9	PTD5/ADC0_SE6B/SPI0_PCS2/UART0_CTS/FTM0_CH5/FB_AD1/EWM_OUT
B10	PTD6/LLWU_P15/ADC0_SE7B/SPI0_PCS3/UART0_RX/FTM0_CH6/FB_AD0/FTM0_FLT0
A10	PTD7/CMT_IRO/UART0_TX/FTM0_CH7/FTM0_FLT1
A11	PTE0/ADC1_SE4A/SPI1_PCS1/UART1_TX/SDHC0_D1/I2C1_SDA
C10	PTE1/LLWU_P0/ADC1_SE5A/SPI1_SOUT/UART1_RX/SDHC0_D0/I2C1_SCL
B11	PTE2/LLWU_P1/ADC1_SE6A/SPI1_SCK/UART1_CTS/SDHC0_DCLK
D10	PTE3/ADC1_SE7A/SPI1_SIN/UART1_RTS/SDHC0_CMD
C11	PTE4/LLWU_P2/SPI1_PCS0/UART3_TX/SDHC0_D3
D11	PTE5/SPI1_PCS2/UART3_RX/SDHC0_D2
E9	PTE6/SPI1_PCS3/UART3_CTS/I2S0_MCLK/I2S0_CLKIN
E10	PTE7/UART3_RTS/I2S0_RXD
E11	PTE8/UART5_TX/I2S0_RX_FS
F8	PTE9/UART5_RX/I2S0_RX_BCLK
F9	PTE10/UART5_CTS/I2S0_TXD
F10	PTE11/UART5_RTS/I2S0_TX_FS
F11	PTE12/I2S0_TX_BCLK
L7	PTE24/ADC0_SE17/CAN1_TX/UART4_TX/EWM_OUT
H7	PTE25/ADC0_SE18/CAN1_RX/UART4_RX/EWM_IN
H6	PTE26/UART4_CTS/RTC_CLKOUT/USB_CLKIN
L6	PTE27/UART4_RTS
G8	VREGIN
G9	VOUT33
G11	USB0_DP
G10	USB0_DM
J7	VREF_OUT/CMP1_IN5/CMP0_IN5/ADC1_SE18
E8	VSS1
E6	VSS2
F5	VSS3
J2	VSS4
G6	VSS5
K8	VSSA
K9	VREFL

<Value>