Pin	Signal
E5	VDD1
E6	VDD2
L10	VDD3
F5	VDDA
L1	ADC0_DM0
L2	ADC0_DP3
	ADC0_DM3
K5	DAC0_OUT/CMP1_IN3/ADC0_SE23
L5	EXTAL32
L4	XTAL32
L7	RTC_WAKEUP
J11	RESET
J6	PTA0/UART0_CTS/UART0_COL/FTM0_CH5/JTAG_TCLK/SWD_CLK/EZP_CLK
H8	PTA1/UART0_RX/FTM0_CH6/JTAG_TDI/EZP_DI
J7	PTA2/UART0_TX/FTM0_CH7/JTAG_TDO/TRACE_SWO/EZP_DO
H9	PTA3/UART0_RTS/FTM0_CH0/JTAG_TMS/SWD_DIO
J8	PTA4/LLWU_P3/FTM0_CH1/NMI/EZP_CS
K7	PTA5/FTM0_CH2/I2S0_TX_BCLK/JTAG_TRST
K8	PTA12/FTM1_CH0/I2S0_TXD0/FTM1_QD_PHA
L8	PTA13/LLWU_P4/FTM1_CH1/I2S0_TX_FS/FTM1_QD_PHB
K9	PTA14/SPI0_PCS0/UART0_TX/I2S0_RX_BCLK/I2S0_TXD1
L9	PTA15/SPI0_SCK/UART0_RX/I2S0_RXD0
J10	PTA16/SPI0_SOUT/UART0_CTS/UART0_COL/I2S0_RX_FS/I2S0_RXD1
H10	PTA17/SPI0_SIN/UART0_RTS/I2S0_MCLK
L11	PTA18/EXTAL0/FTM0_FLT2/FTM_CLKIN0
K11	PTA19/XTAL0/FTM1_FLT0/FTM_CLKIN1/LPTMR0_ALT1
G11	PTB0/LLWU_P5/ADC0_SE8/I2C0_SCL/FTM1_CH0/FTM1_QD_PHA
G10	PTB1/ADC0_SE9/I2C0_SDA/FTM1_CH1/FTM1_QD_PHB
G9	PTB2/ADC0_SE12/I2C0_SCL/UART0_RTS/FTM0_FLT3
G8	PTB3/ADC0_SE13/I2C0_SDA/UART0_CTS/UART0_COL/FTM0_FLT0
D10	PTB10/SPI1_PCS0/UART3_RX/FTM0_FLT1
C10	PTB11/SPI1_SCK/UART3_TX/FTM0_FLT2
B11	PTB12/UART3_RTS/FTM1_CH0/FTM0_CH4/FTM1_QD_PHA
C11	PTB13/UART3_CTS/FTM1_CH1/FTM0_CH5/FTM1_QD_PHB
B10	PTB16/SPI1_SOUT/UART0_RX/EWM_IN/FTM_CLKIN0
E9	PTB17/SPI1_SIN/UART0_TX/EWM_OUT/FTM_CLKIN1
D9	PTB18/FTM2_CH0/I2S0_TX_BCLK/FTM2_QD_PHA
C9	PTB19/FTM2_CH1/I2S0_TX_FS/FTM2_QD_PHB
B9	PTC0/ADC0_SE14/SPI0_PCS4/PDB0_EXTRG/I2S0_TXD1
D8	PTC1/LLWU_P6/SPI0_PCS3/ADC0_SE15/UART1_RTS/FTM0_CH0/I2S0_TXD0
C8	PTC2/ADC0_SE4b/CMP1_IN0/SPI0_PCS2/UART1_CTS/FTM0_CH1/I2S0_TX_FS
B8	PTC3/LLWU_P7/CMP1_IN1/SPI0_PCS1/UART1_RX/FTM0_CH2/I2S0_TX_BCLK
A8	PTC4/LLWU_P8/SPI0_PCS0/UART1_TX/FTM0_CH3/CMP1_OUT
D7	PTC5/LLWU_P9/SPI0_SCK/LPTMR0_ALT2/I2S0_RXD0/CMP0_OUT/FTM0_CH2
C7	PTC6/LLWU_P10/CMP0_IN0/SPI0_SOUT/PDB0_EXTRG/I2S0_RX_BCLK/I2S0_MCLK
B7	PTC7/CMP0_IN1/SPI0_SIN/I2S0_RX_FS
A7	PTC8/CMP0_IN2/I2S0_MCLK
D6	PTC9/CMP0_IN3/I2S0_RX_BCLK/FTM2_FLT0
C6	PTC10/I2C1_SCL/I2S0_RX_FS
C5	PTC11/LLWU_P11/I2C1_SDA/I2S0_RXD1
B6	PTC12
A6	PTC13
D5	PTC16/UART3_RX
C4	PTC17/UART3_TX
D4	PTD0/LLWU_P12/SPI0_PCS0/UART2_RTS
D3	PTD1/ADC0_SE5b/SPI0_SCK/UART2_CTS
C3	PTD2/LLWU_P13/SPI0_SOUT/UART2_RX/I2C0_SCL
B3	PTD3/SPI0_SIN/UART2_TX/I2C0_SDA
A3	PTD4/LLWU_P14/ADC0_SE21/SPI0_PCS1/UART0_RTS/FTM0_CH4/EWM_IN
A2	PTD5/ADC0_SE6b/SPI0_PCS2/UART0_CTS/UART0_COL/FTM0_CH5/EWM_OUT
B2	PTD6/LLWU_P15/ADC0_SE7b/SPI0_PCS3/UART0_RX/FTM0_CH6/FTM0_FLT0
A1	PTD7/ADC0_SE22/CMT_IRO/UART0_TX/FTM0_CH7/FTM0_FLT1
E4	PTE0/ADC0_SE10/SPI1_PCS1/UART1_TX/TRACE_CLKOUT/I2C1_SDA/RTC_CLKOUT
E3	PTE1/LLWU_P0/ADC0_SE11/SPI1_SOUT/UART1_RX/TRACE_D3/I2C1_SCL/SPI1_SIN
E2	PTE2/LLWU_P1/ADC0_DP1/SPI1_SCK/UART1_CTS/TRACE_D2
F4	PTE3/ADC0_DM1/SPI1_SIN/UART1_RTS/TRACE_D1/SPI1_SOUT
H7	PTE4/LLWU_P2/SPI1_PCS0/UART3_TX/TRACE_D0
G4	PTE5/SPI1_PCS2/UART3_RX
F1	PTE16/ADC0_SE4a/SPI0_PCS0/UART2_TX/FTM_CLKIN0/FTM0_FLT3
F2	PTE17/ADC0_SE5a/SPI0_SCK/UART2_RX/FTM_CLKIN1/LPTMR0_ALT3
G1	PTE18/ADC0_SE6a/SPI0_SOUT/UART2_CTS/I2C0_SDA
G2	PTE19/ADC0_SE7a/SPI0_SIN/UART2_RTS/I2C0_SCL
K6	VBAT
L3	VREF_OUT/CMP1_IN5/CMP0_IN5
G5	VREFH
G6	VREFL
A10	NC_A10
A11	NC_A11
A4	NC_A4
A5	NC_A5
A9	NC_A9
B1	NC_B1
B4	NC_B4
B5	NC_B5
C1	NC_C1
C2	NC_C2
D1	NC_D1
D11	NC_D11
D2	NC_D2
E1	NC_E1
E10	NC_E10
E11	NC_E11
E7	NC_E7
E8	NC_E8
F10	NC_F10
F11	NC_F11
F3	NC_F3
F7	NC_F7
F8	NC_F8
F9	NC_F9
H1	NC_H1
H11	NC_H11
H2	NC_H2
H3	NC_H3
H4	NC_H4
H5	NC_H5
H6	NC_H6
J1	NC_J1
J2	NC_J2
J3	NC_J3
J4	NC_J4
J5	NC_J5
J9	NC_J9
K3	NC_K3
K4	NC_K4
G3	VSS1
G7	VSS2
K10	VSS3
L6	VSS4
F6	VSSA

<Value>