	DP/ADC0_DP0/ADC1_DP3		
	DM/ADC0_DM0/ADC1_DM3		
		DAC0_OUT/CMP1_IN3/ADC0_SE23	22
15	PGA1_DP/ADC1_DP0/ADC0_DP3	VREF_OUT/CMP1_IN5/CMP0_IN5/ADC1_SE18	21
16	PGA1_DM/ADC1_DM0/ADC0_DM3		
26	TSI0_CH1/PTA0/UART0_CTS/FTM0_CH5/JTAG_TCLK/SWD_CLK/EZP_CLK		
27	TSI0_CH2/PTA1/UART0_RX/FTM0_CH6/JTAG_TDI/EZP_DI		
28	TSI0_CH3/PTA2/UART0_TX/FTM0_CH7/JTAG_TDO/TRACE_SWO/EZP_DO	VREFH	18
29	TSI0_CH4/PTA3/UART0_RTS/FTM0_CH0/JTAG_TMS/SWD_DIO	VREFL	19
30	TSI0_CH5/PTA4/FTM0_CH1/NMI/EZP_CS		
31	PTA5/FTM0_CH2/CMP2_OUT/I2S0_RX_BCLK/JTAG_TRST	VBAT	25
32	CMP2_IN0/PTA12/CAN0_TX/FTM1_CH0/I2S0_TXD/FTM1_QD_PHA		
33	CMP2_IN1/PTA13/CAN0_RX/FTM1_CH1/I2S0_TX_FS/FTM1_QD_PHB		
34	PTA14/SPI0_PCS0/UART0_TX/I2S0_TX_BCLK	VDD1	7
35	PTA15/SPI0_SCK/UART0_RX/I2S0_RXD	VDD2	38
36	PTA16/SPI0_SOUT/UART0_CTS/I2S0_RX_FS	VDD3	50
37	ADC1_SE17/PTA17/SPI0_SIN/UART0_RTS/I2S0_MCLK/I2S0_CLKIN	VDD4	60
40	EXTAL/PTA18/FTM0_FLT2/FTM_CLKIN0	VDD5	70
41	XTAL/PTA19/FTM1_FLT0/FTM_CLKIN1/LPT0_ALT1		
43	ADC0_SE8/ADC1_SE8/TSI0_CH0/PTB0/I2C0_SCL/FTM1_CH0/FTM1_QD_PHA	VDDA	17
44	ADC0_SE9/ADC1_SE9/TSI0_CH6/PTB1/I2C0_SDA/FTM1_CH1/FTM1_QD_PHB		
45	ADC0_SE12/TSI0_CH7/PTB2/I2C0_SCL/UART0_RTS/FTM0_FLT3		
46	ADC0_SE13/TSI0_CH8/PTB3/I2C0_SDA/UART0_CTS/FTM0_FLT0		
47	ADC1_SE14/PTB10/SPI1_PCS0/UART3_RX/FB_AD19/FTM0_FLT1		
48	ADC1_SE15/PTB11/SPI1_SCK/UART3_TX/FB_AD18/FTM0_FLT2		
51	TSI0_CH9/PTB16/SPI1_SOUT/UART0_RX/FB_AD17/EWM_IN		
52	TSI0_CH10/PTB17/SPI1_SIN/UART0_TX/FB_AD16/EWM_OUT		
53	TSI0_CH11/PTB18/CAN0_TX/FTM2_CH0/I2S0_TX_BCLK/FB_AD15/FTM2_QD_PHA		
54	TSI0_CH12/PTB19/CAN0_RX/FTM2_CH1/I2S0_TX_FS/FB_OE/FTM2_QD_PHB		
55	ADC0_SE14/TSI0_CH13/PTC0/SPI0_PCS4/PDB0_EXTRG/I2S0_TXD/FB_AD14		
56	ADC0_SE15/TSI0_CH14/PTC1/SPI0_PCS3/UART1_RTS/FTM0_CH0/FB_AD13		
57	ADC0_SE4B/CMP1_IN0/TSI0_CH15/PTC2/SPI0_PCS2/UART1_CTS/FTM0_CH1/FB_AD12		
58	CMP1_IN1/PTC3/SPI0_PCS1/UART1_RX/FTM0_CH2/FB_CLKOUT		
61	PTC4/SPI0_PCS0/UART1_TX/FTM0_CH3/FB_AD11/CMP1_OUT		
62	PTC5/SPI0_SCK/LPT0_ALT2/FB_AD10/CMP0_OUT		
63	CMP0_IN0/PTC6/SPI0_SOUT/PDB0_EXTRG/FB_AD9		
64	CMP0_IN1/PTC7/SPI0_SIN/FB_AD8		
65	ADC1_SE4B/CMP0_IN2/PTC8/I2S0_MCLK/I2S0_CLKIN/FB_AD7		
66	ADC1_SE5B/CMP0_IN3/PTC9/I2S0_RX_BCLK/FB_AD6/FTM2_FLT0		
67	ADC1_SE6B/CMP0_IN4/PTC10/I2C1_SCL/I2S0_RX_FS/FB_AD5		
68	ADC1_SE7B/PTC11/I2C1_SDA/I2S0_RXD/FB_RW		
71	PTC16/CAN1_RX/UART3_RX/FB_CS5/FB_TSIZ1/FB_BE23_16_BLS15_8	EXTAL32	24
72	PTC17/CAN1_TX/UART3_TX/FB_CS4/FB_TSIZ0/FB_BE31_24_BLS7_0	XTAL32	23
73	PTD0/SPI0_PCS0/UART2_RTS/FB_ALE/FB_CS1/FB_TS		
74	ADC0_SE5B/PTD1/SPI0_SCK/UART2_CTS/FB_CS0		
75	PTD2/SPI0_SOUT/UART2_RX/FB_AD4		
76	PTD3/SPI0_SIN/UART2_TX/FB_AD3		
77	PTD4/SPI0_PCS1/UART0_RTS/FTM0_CH4/FB_AD2/EWM_IN	RESET	42
78	ADC0_SE6B/PTD5/SPI0_PCS2/UART0_CTS/FTM0_CH5/FB_AD1/EWM_OUT		
79	ADC0_SE7B/PTD6/SPI0_PCS3/UART0_RX/FTM0_CH6/FB_AD0/FTM0_FLT0		
80	PTD7/CMT_IRO/UART0_TX/FTM0_CH7/FTM0_FLT1		
1	ADC1_SE4A/PTE0/SPI1_PCS1/UART1_TX/SDHC0_D1/I2C1_SDA	VSSA	20
2	ADC1_SE5A/PTE1/SPI1_SOUT/UART1_RX/SDHC0_D0/I2C1_SCL		
3	ADC1_SE6A/PTE2/SPI1_SCK/UART1_CTS/SDHC0_DCLK	VSS1	8
4	ADC1_SE7A/PTE3/SPI1_SIN/UART1_RTS/SDHC0_CMD	VSS2	39
5	PTE4/SPI1_PCS0/UART3_TX/SDHC0_D3	VSS3	49
6	PTE5/SPI1_PCS2/UART3_RX/SDHC0_D2	VSS4	59
9	ADC0_SE4A/PTE16/SPI0_PCS0/UART2_TX/FTM_CLKIN0/FTM0_FLT3	VSS5	69
10	ADC0_SE5A/PTE17/SPI0_SCK/UART2_RX/FTM_CLKIN1/LPT0_ALT3		
11	ADC0_SE6A/PTE18/SPI0_SOUT/UART2_CTS/I2C0_SDA		
12	ADC0_SE7A/PTE19/SPI0_SIN/UART2_RTS/I2C0_SCL		