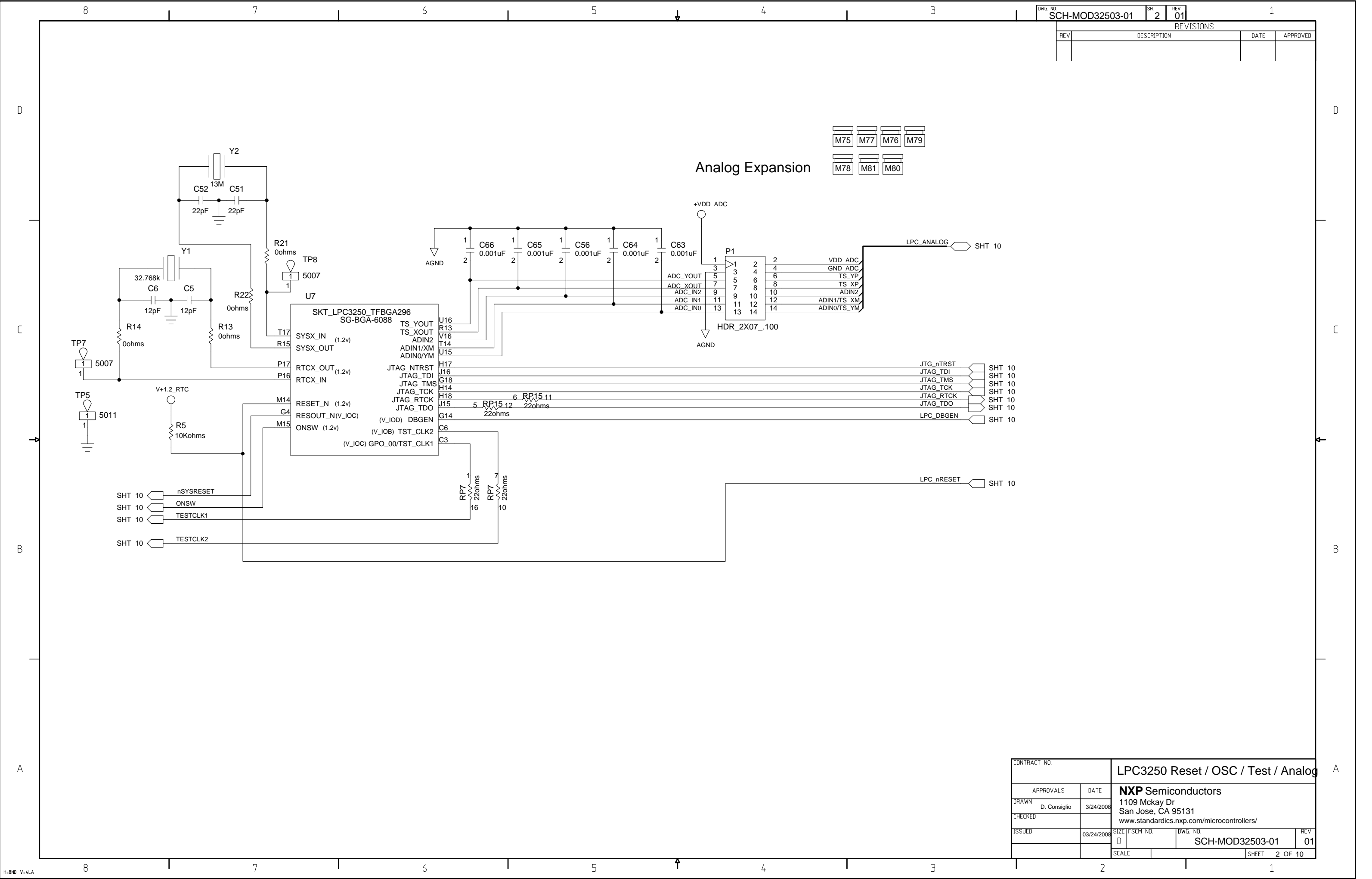


NXP Semiconductors  
LPC3250 Mobile DDR Module Board Schematics  
Mar. 24, 2008

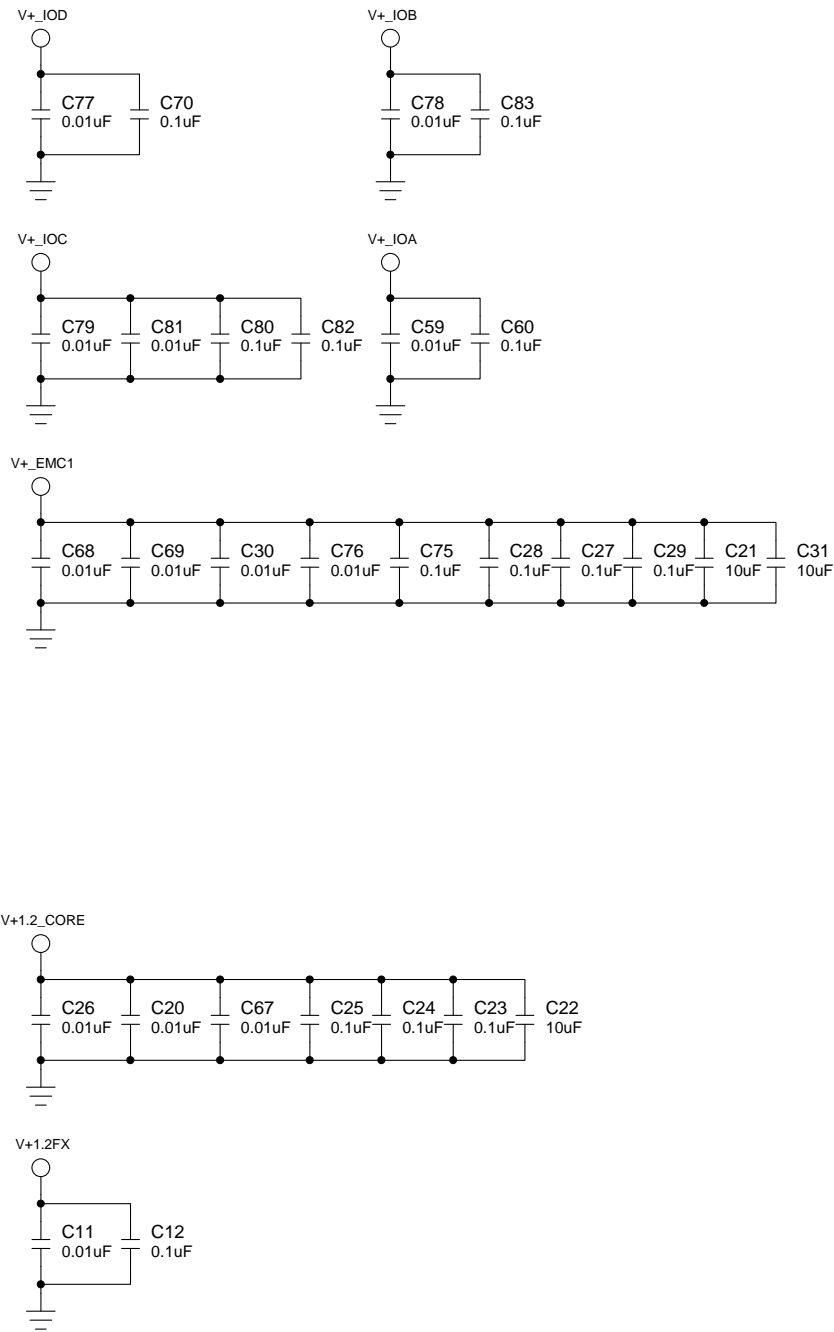
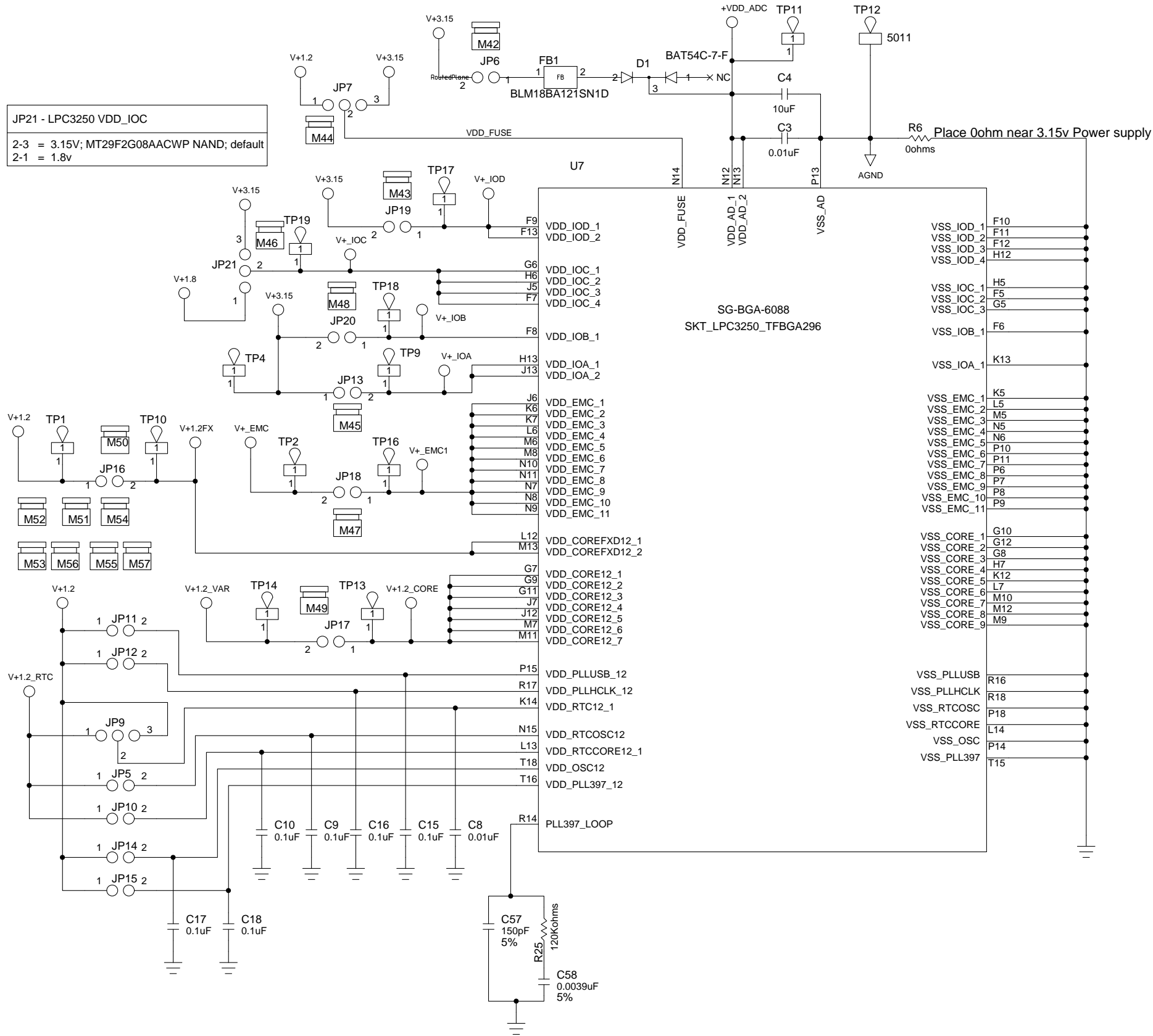
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Page 2 - LPC3250 Reset / OSC / Test / Analog  
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DWG. NO.		SH.	REV.
SCH-MOD32503-01		1	01
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00	Release for review and Layout quotation	01/04/08	
01	Re-order REFDES; Release for Assembly	03/17/08	

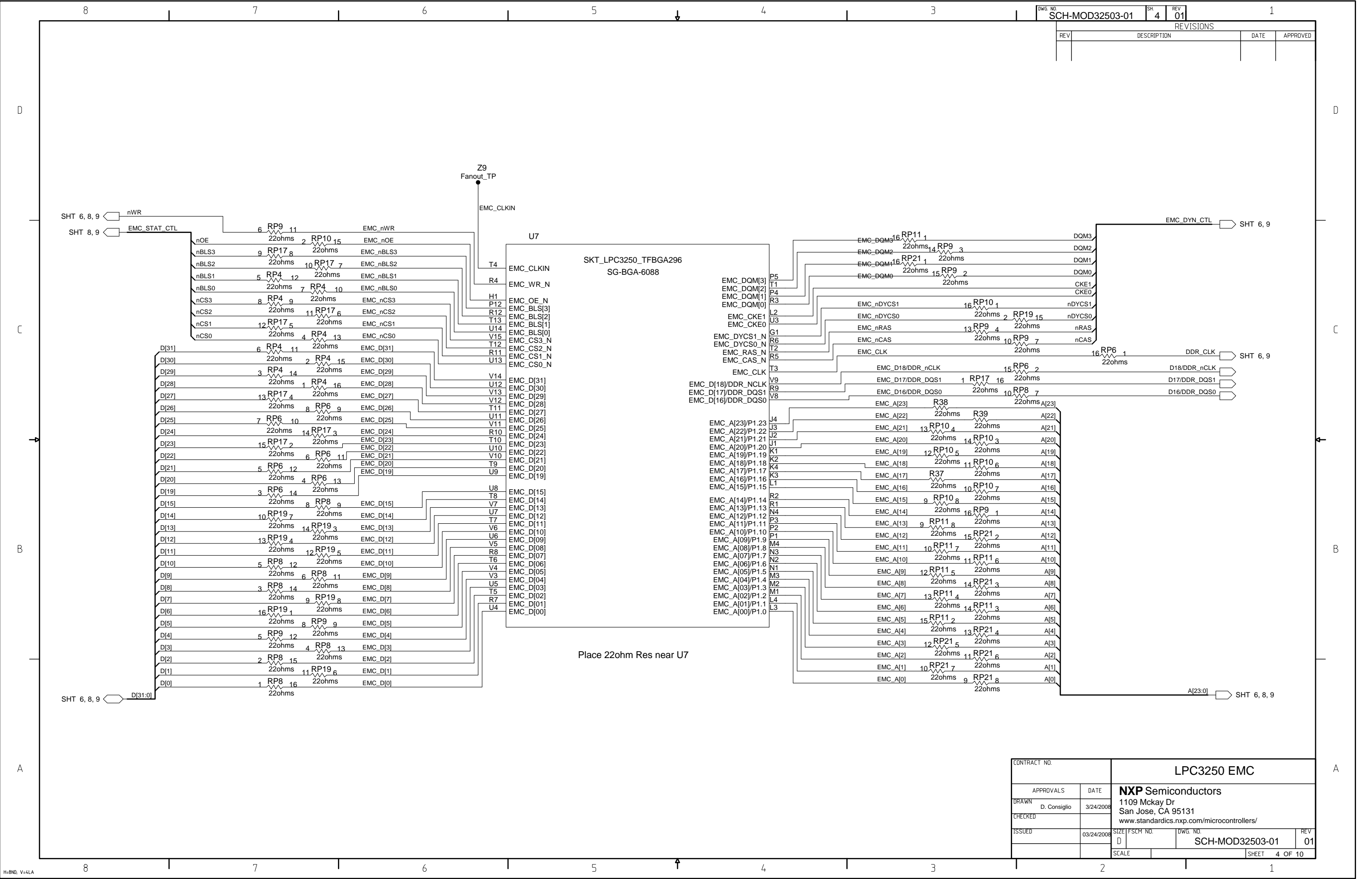
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APPROVALS		DATE	<b>NXP Semiconductors</b> 1109 Mckay Dr San Jose, CA 95131 <a href="http://www.standardics.nxp.com/microcontrollers/">www.standardics.nxp.com/microcontrollers/</a>		
DRAWN		D. Consiglio			
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ISSUED		03/24/2008	SIZE	FSCM NO.	DWG. NO.
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			SCALE		REV
					01
					SHEET 1 OF 10



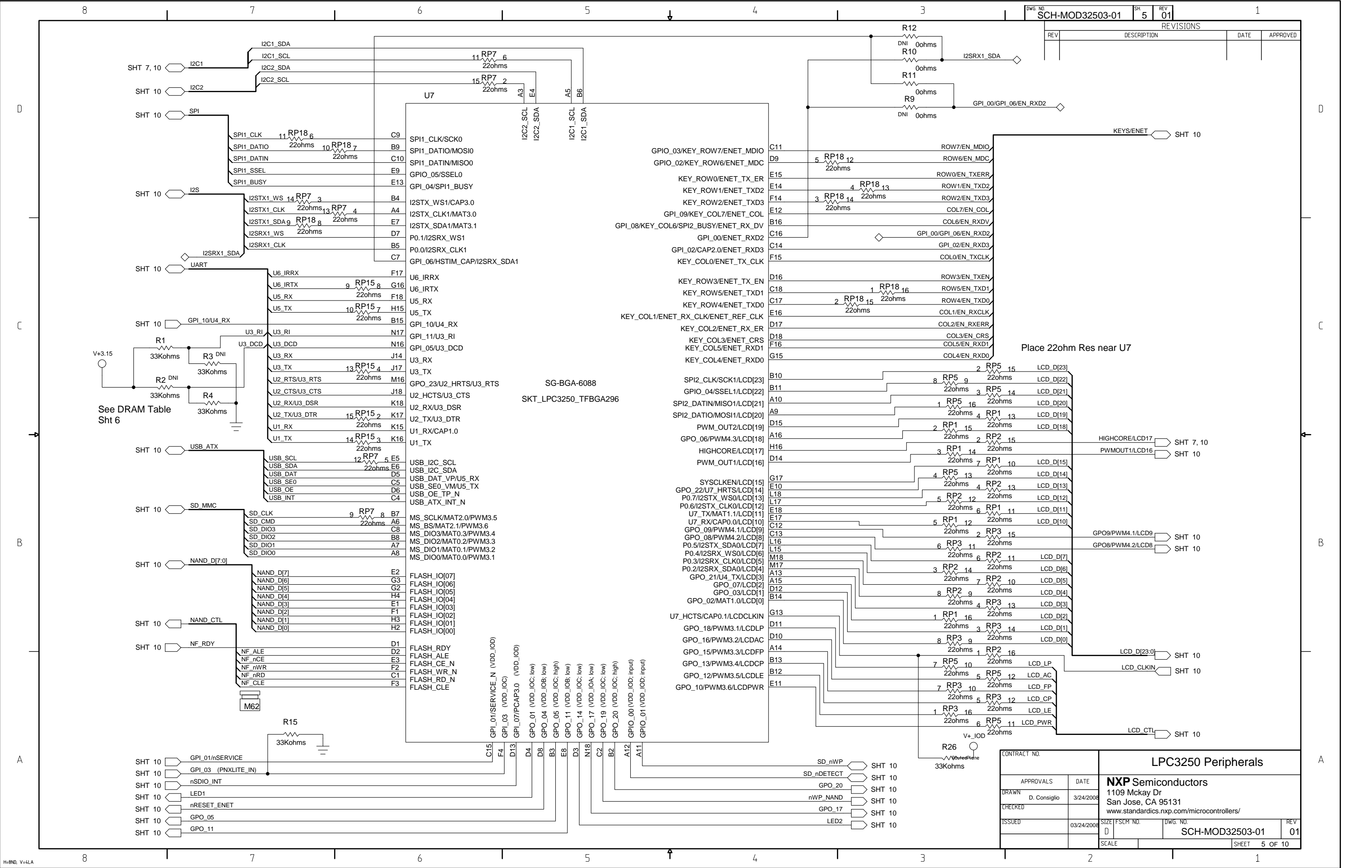
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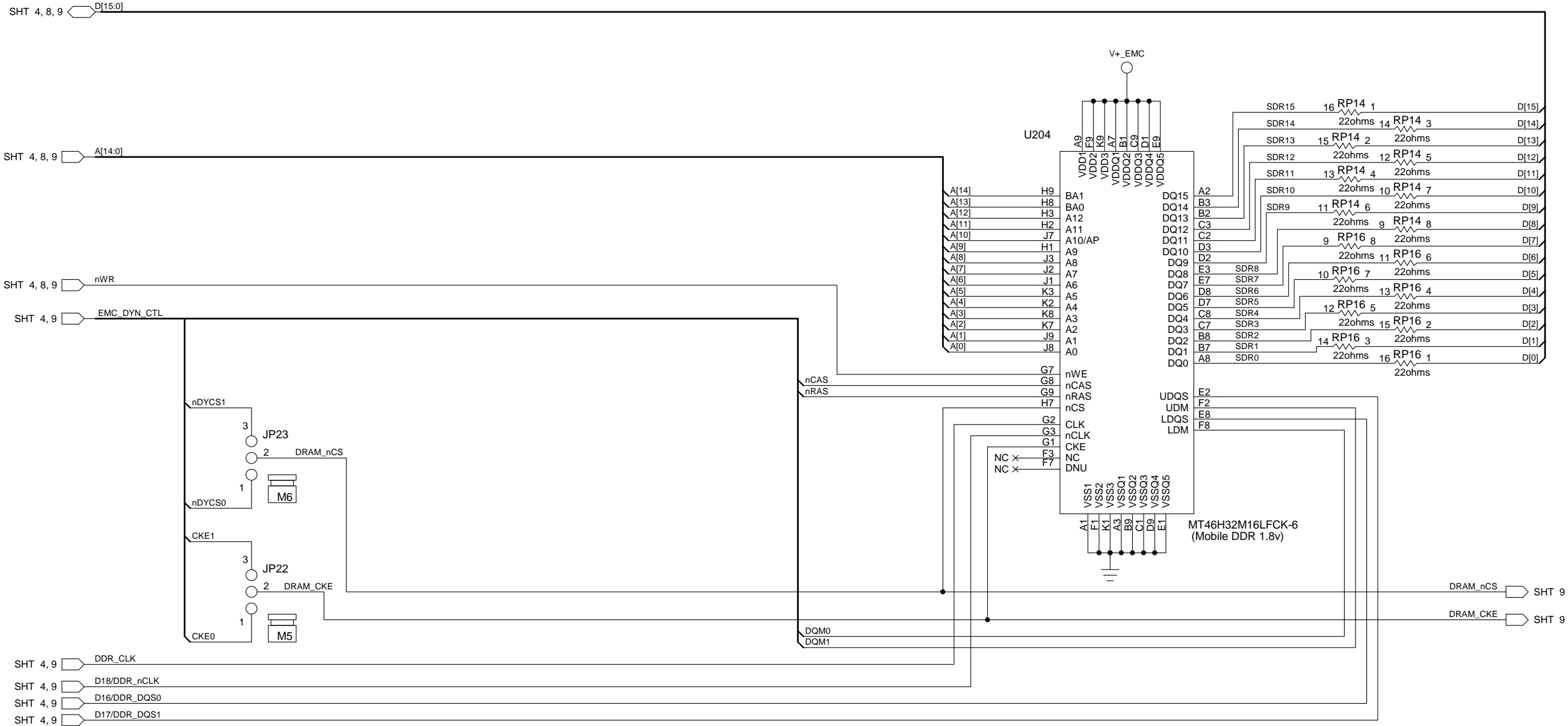


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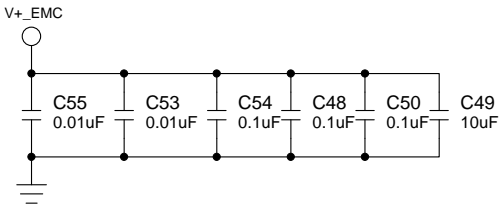
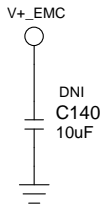
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ISSUED		03/24/2008			
		SIZE	FSCM NO.	DWG. NO.	REV
		D		SCH-MOD32503-01	01
		SCALE	SHEET 4 OF 10		





DRAM Table (PU/PD on Sht 5)

MEMORY	GPI_11 Mobile/STD	GPI_05 SDR/DDR
MT48H16M16LFBF	PU	PU
MT48LC16M16A2BG	PD	PU
MT46V32M16BN	PD	PD
MT46H32M16LFCK	PU	PD



CONTRACT NO.		SDR SDRAM Memory		
APPROVALS	DATE	NXP Semiconductors 1109 Mckay Dr San Jose, CA 95131 www.standardics.nxp.com/microcontrollers/		
DRAWN D. Consiglio	3/24/2008			
CHECKED		SIZE	FSCM NO.	DWG. NO.
ISSUED	03/24/2008	D		SCH-MOD32503-01
SCALE		SHEET		6 OF 10



