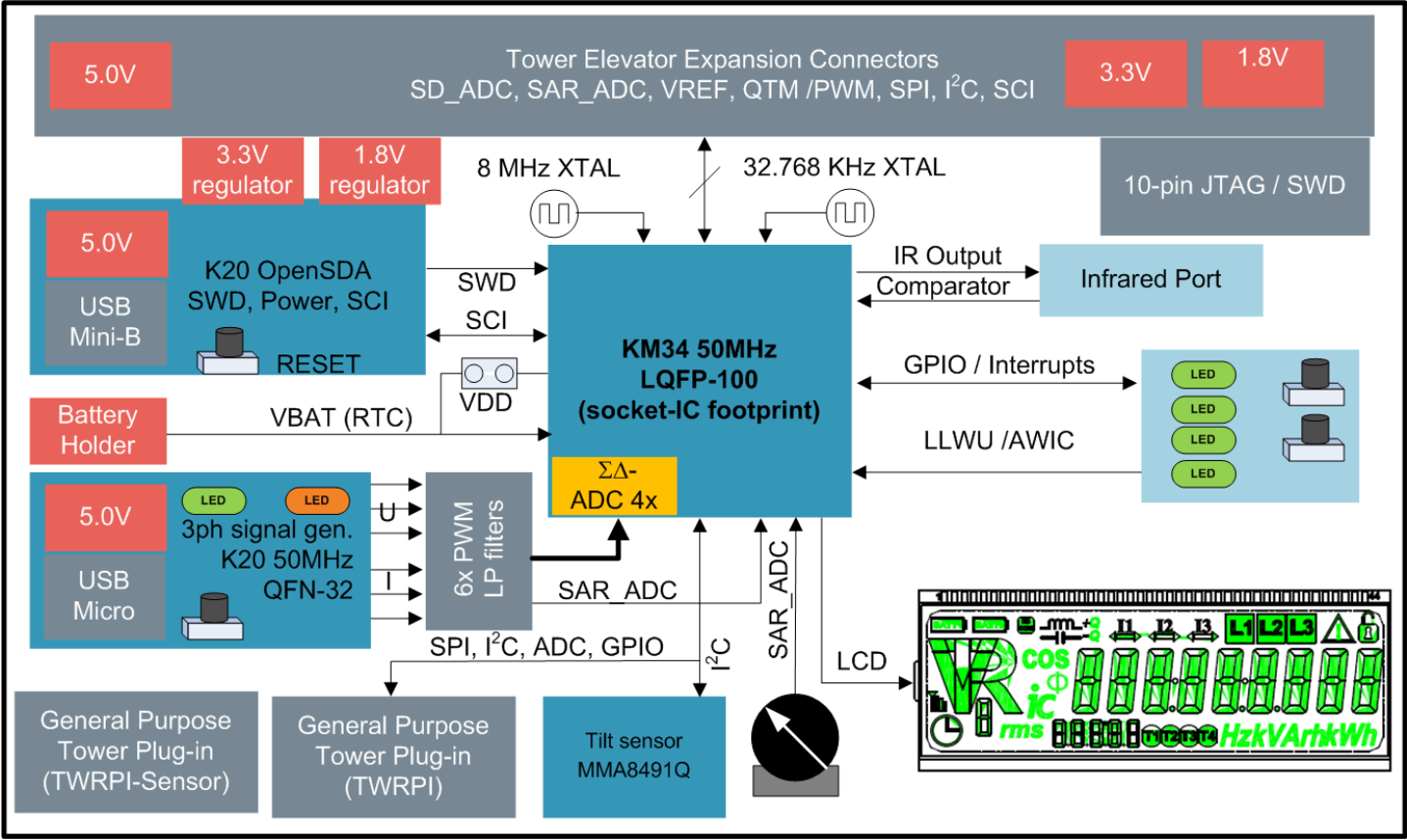


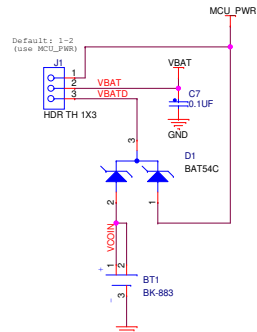
1. Unless Otherwise Specified:
All resistors are in ohms
All capacitors are in uF
All voltages are DC
2. Interrupted lines coded with the same letter or letter combinations are electrically connected.
3. Device type number is for reference only. The number varies with the manufacturer.
4. Special signal usage:
_B Denotes - Active-Low Signal
<> or [] Denotes - Vectored Signals
5. Interpret diagram in accordance with American National Standards Institute specifications, current revision, with the exception of logic block symbology.

Power & Ground Nets

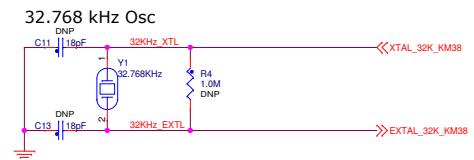
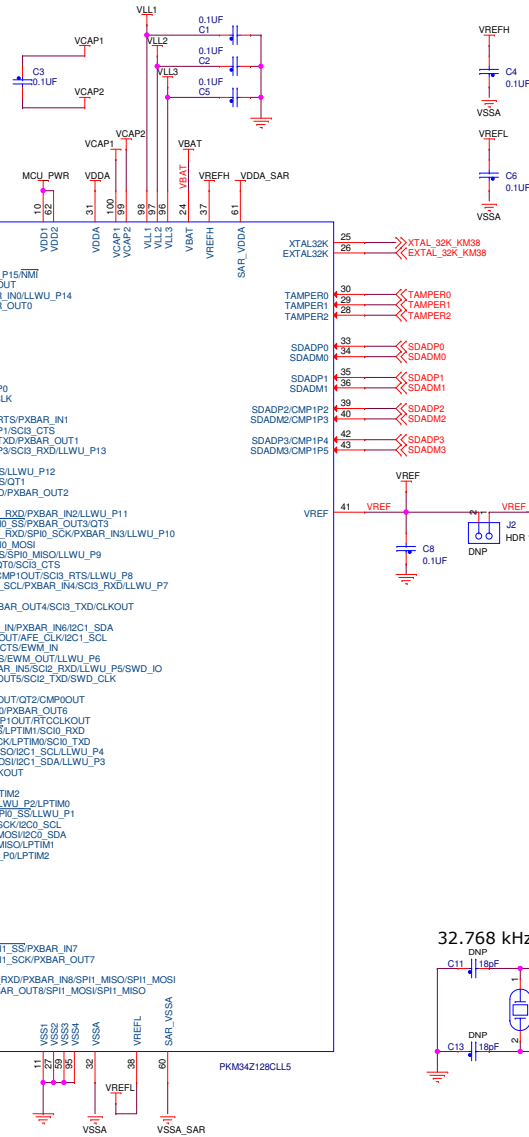
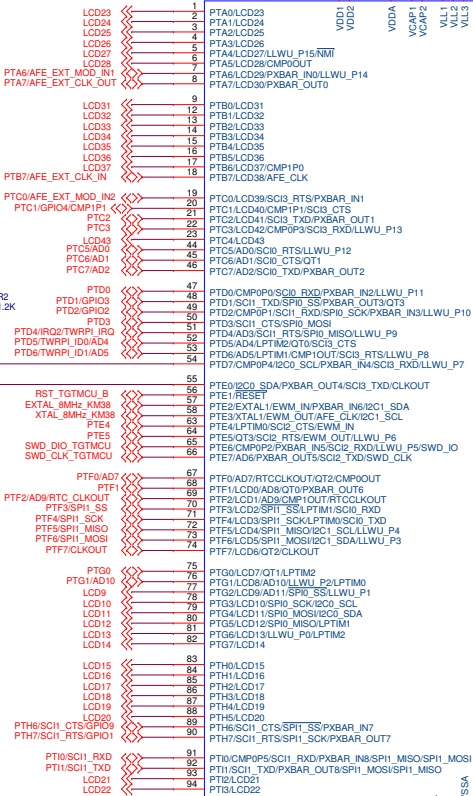
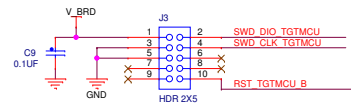
NET	VOLTAGE	DESCRIPTION
P5V_USB	5V	Primary input power. Filtered from USB connector. Input to USB power switch.
P5V_TRG_USB	5V	Output of USB power switch controlled by the VTRG_EN signal from the SWD K20 MCU. Provides input to regulator.
P3V3_REG	3.3V	Output of regulator U503 or from the Elevator connector
P1V8	1.8V	Output of regulator U504
V_BRD	3.3V or 1.8V	MCU & Interface circuit input power
VDDA	3.3V	VDDA power for SD-ADC Filtered from MCU_PWR.
VREFH	1.2V	Upper reference voltage for ADC. Generated internally by MCU.
VREFL	0.4V	Lower reference voltage for ADC. Generated internally by MCU.
VDDA_SAR	3.3V	VDDA power for SAR-ADC Filtered from MCU_PWR.
VBAT	3.3V	VBAT power domain from Coin battery
VSSA	0V	Analog ground for SD-ADC. Filtered from GND.
VSSA_SAR	0V	Analog ground for SAR-ADC. Filtered from GND.
GND	0V	Digital Ground (common)

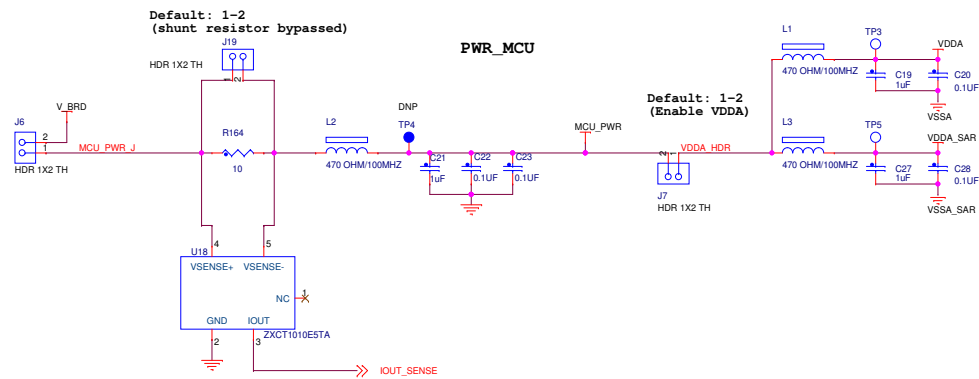
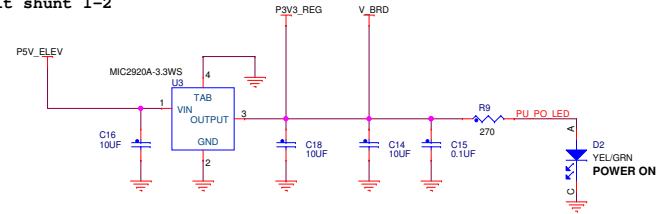


Freescle Device External Connectors Interface Circuits Power



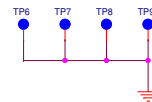
KM34 JTAG / SWD CONNECTOR





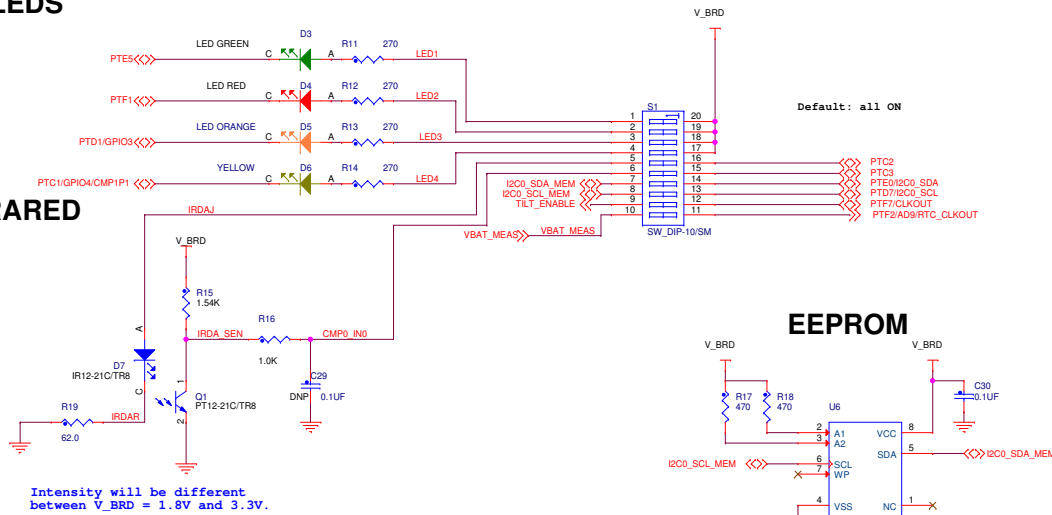
VSSA_SAR can be connected to VSS by replacing the bead with 0R0

GND LOOP TEST POINTS

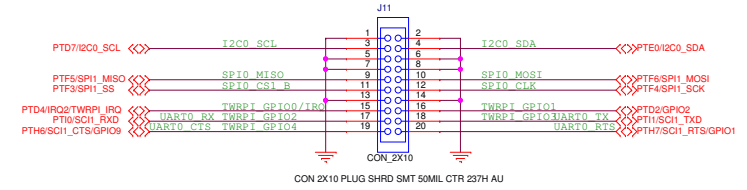
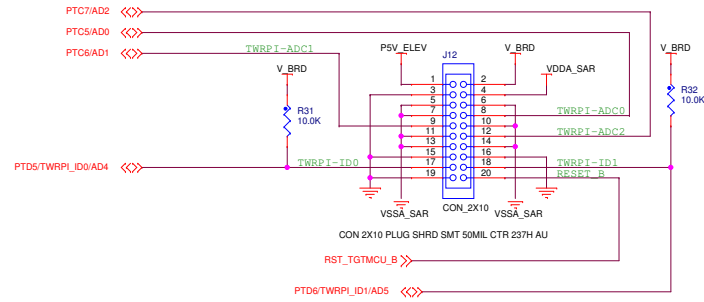


LEDs

INFRARED

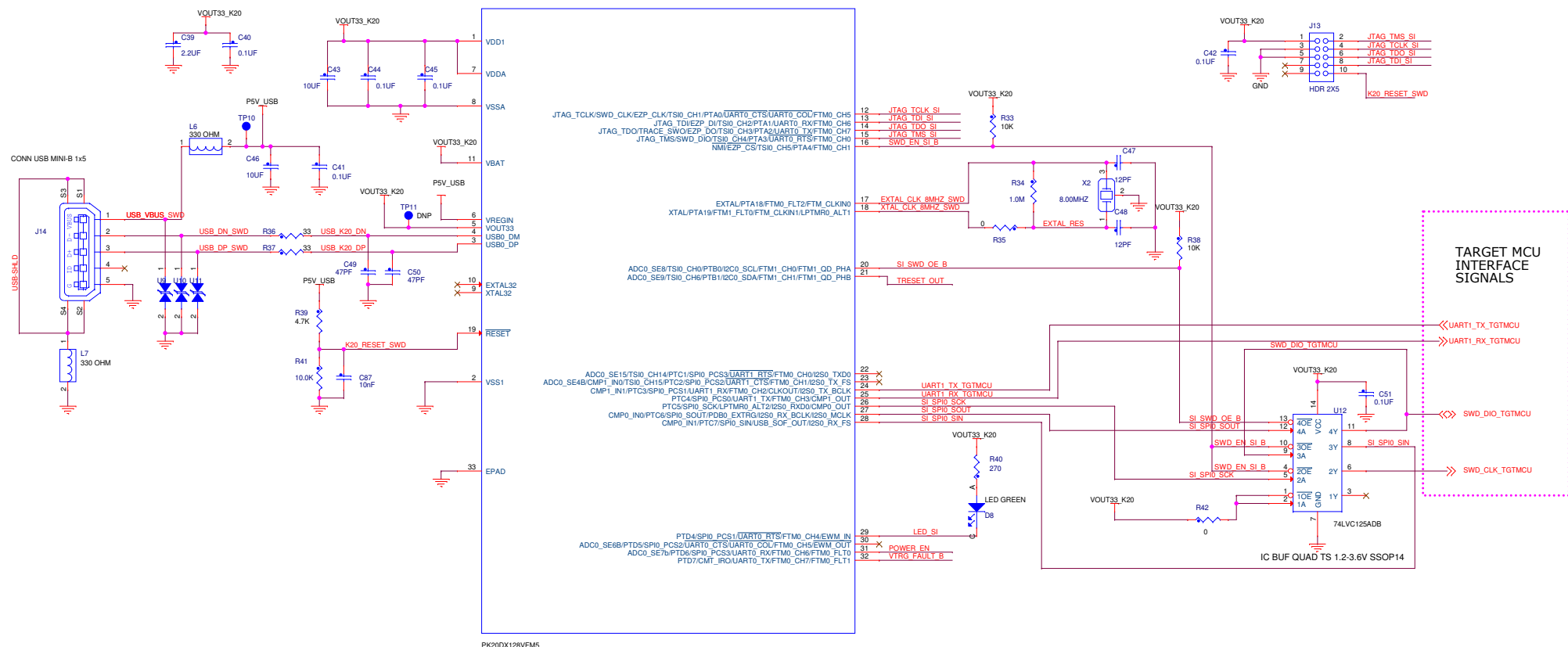


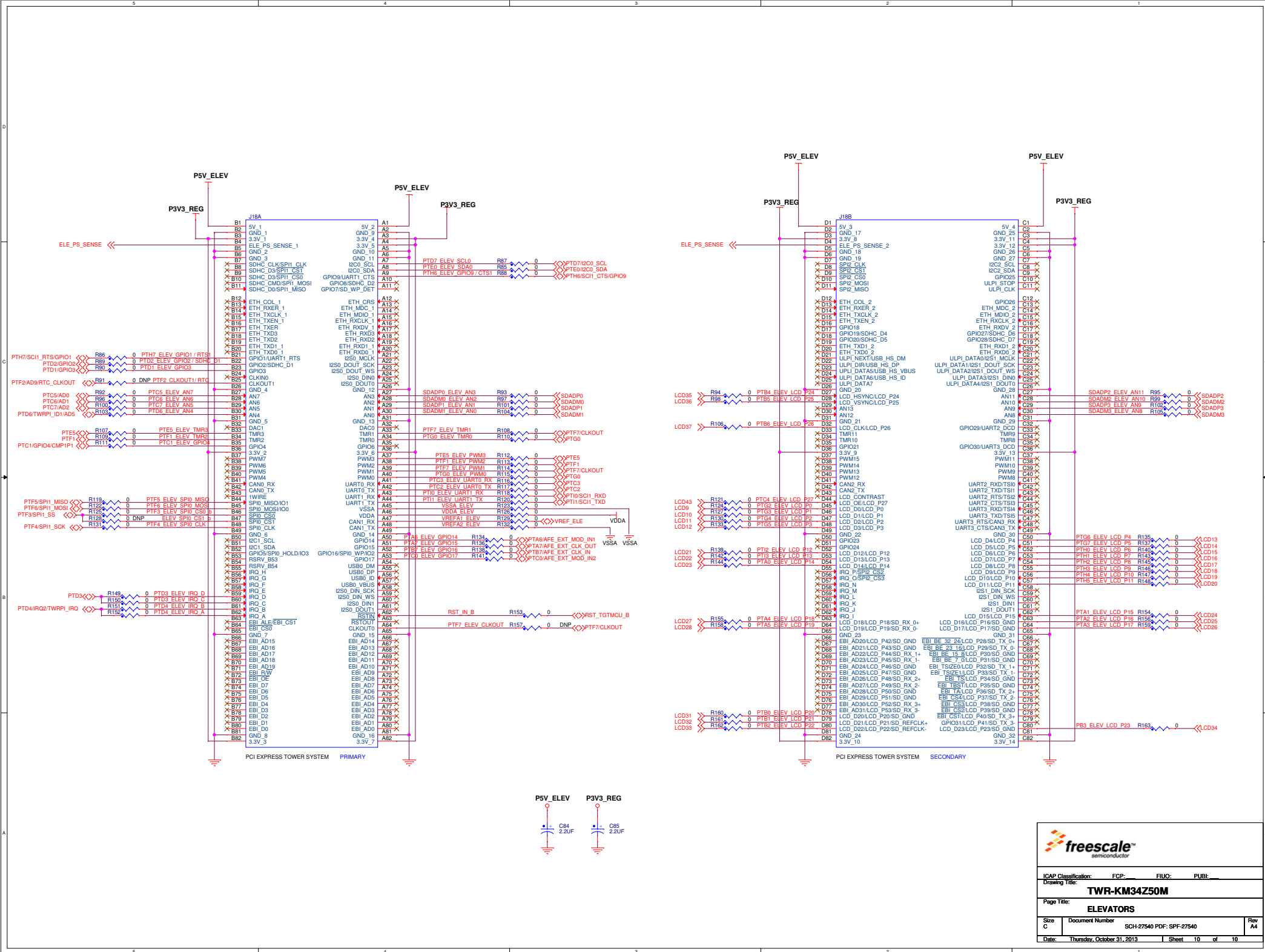
GENERAL PURPOSE TWRPI 1




ICAP Classification:		FCP:	FIUC:	PUBI:
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Size C	Document Number SCH-27540 PDF: SPF-27540			Rev A4
Date:	Thursday, October 31, 2013		Sheet 7 of 10	

K20 USB-SERIAL INTERFACE JTAG CONNECTOR







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Drawing file: TWR-KM34Z50M				
Page Title: ELEVATORS				
Size C	Document Number	SCH-27540 PDF: SPF-27540		Rev A4
Date: Thursday, October 31, 2013		Sheet 10 of 10		