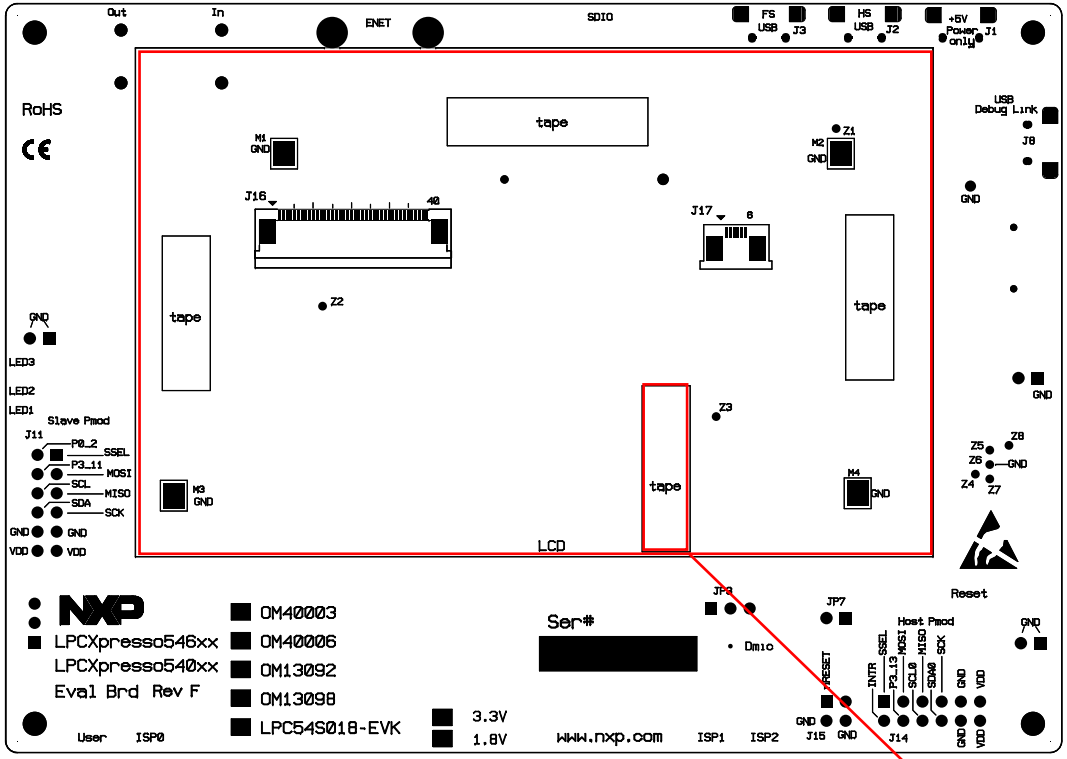

LPC546xx-540xx Eval brd 3.3V LCD assembly

NXP LPC546xx/540xx Eval Brd 07\07\18
PCB-LPC546xx/540xx-EVK Rev F
Generated:SilkscreenBottom.gdo

Release 07\07\18

2nd Operation LCD assembly steps.
1) With LCD screen facing towards pcb, insert 40 pin FFC and 6 pin FFC into respective J16 and J17 connectors.
2) place 2-side tape near each identified "tape" box. Ensure the tape is placed so it does not stick to the FFC but only to the metal case of LCD.
3) Fold back the LCD, pressing down to compress the EMI fingers (M1 - M4) and secure to 2-sided tape.

position tape to be between the two FFC cables.