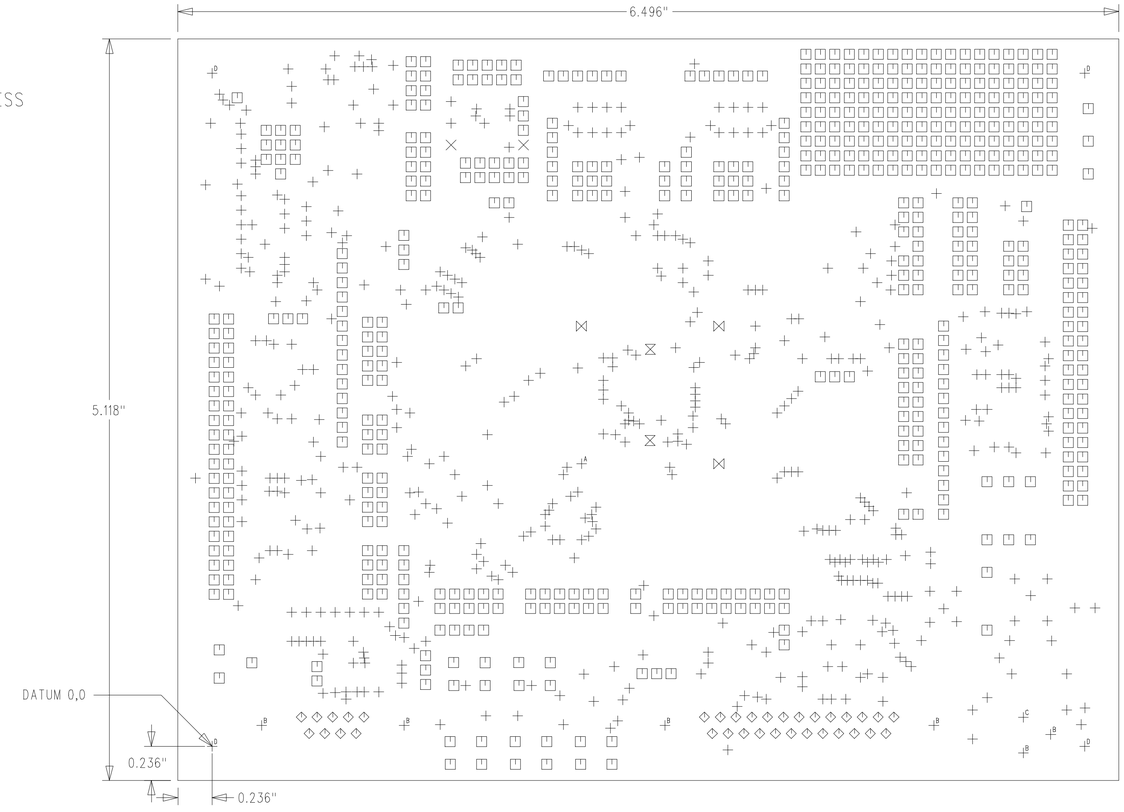
1. MINIMUM CLEARANCE ON THE BOARD SHALL BE .007 INCH.
2. INNER LAYERS SHALL HAVE A MINIMUM COPPER THICKNESS OF 1 OZ. OUTER LAYERS SHALL HAVE A MINIMUM COPPER THICKNESS OF 1.5 OZ AFTER PLATING.
3. MINIMUM THICKNESS OF COPPER ON THE WALL OF A PLATED HOLE SHALL BE .001 INCH.
4. HOLE SIZE TOLERANCES SHALL BE:
   PLATED THRU HOLES =.015 SHALL BE +/- .005 INCH.
   PLATED THRU HOLES =.020 SHALL BE +/- .005 INCH.
   PLATED THRU HOLES >.020 SHALL BE +/- .003 INCH.
   UNPLATED THRU HOLES SHALL BE +/- .002 INCH.
5. SOLDER MASK ON BOTH SIDES OF THE BOARD BE S.M.O.B.C, LPI, COLOR GREEN.
6. ALL MATERIAL SHALL BE NEMA GRADE FR-4 OR EQUIVALENT.
7. TOTAL BOARD THICKNESS SHALL BE .062 +/- .007 INCHES.
8. DRC'S MUST BE RAN ON THE GERBERS BEFORE BUILDING BOARD UNLESS PRIOR APPROVAL IS GIVEN IN WRITING.
9. SILKSCREEN BOTH SIDES WITH NON-CONDUCTIVE EPOXY-BASED WHITE INK.
10. MANUFACTURE TO IPC-A-600D SPECIFICATIONS.
11. MINIMUM ANNULAR RING SHALL BE .001".
12. MINIMUM ANNULAR RING AT EMERGENT CONDUCTORS SHALL BE .003".
13. BOW AND TWIST SHALL NOT EXCEED .010" PER INCH.

REVISIONS			
LTR	DESCRIPTION	DATE	APPROVAL

	FILE NAMES FROM ZIP FILE
TOP SILKSCREEN	TDMOT031-TOPSILK.PHO
TOP SOLDER MASK	TDMOT031-TOPMASK.PHO
COMPONENT	TDMOT031-L1.PHO
GROUND PLANE	TDMOT031-L2.PHO
POWER PLANE	TDMOT031-L3.PHO
SOLDER	TDMOT031-L4.PHO
BOTTOM SOLDER MASK	TDMOT031-BOTMASK.PHO
BOTTOM SILKSCREEN	TDMOT031-BOTSILK.PHO

LAYER SCHEDULE
SCALE: NONE 7



SIZE	QTY	SYM	PLTD
15	515	+	PLTD
38	2	X	PLTD
40	568	□	PLTD
43	34	◇	PLTD
60	2	⧖	PLTD
64	3	⋈	PLTD
90	1	A	PLTD
120	6	B	PLTD
140	1	C	PLTD
125	4	D	NPLTD

NEXT ASSY	USED ON

MATERIAL:

FINISH:

UNLESS OTHERWISE SPECIFIED
DIMENSIONS ARE IN INCHES AND APPLY AFTER FINISH
DIMENSIONS IN BRACKETS [ ] ARE IN MILLIMETERS
INTERPRET DRAWING PER MIL-D-1000
TOLERANCES
HOLE TOLERANCES PER 78027

DECIMALS	ANGLES	SURFACES
.XX +/-		
.XXX +/-	+/-	MICROINCHES

PART TO BE FREE OF BURRS

BREAK EDGES	BEND RADIUS	BEND RELIEF
MAX	MAX	MAX

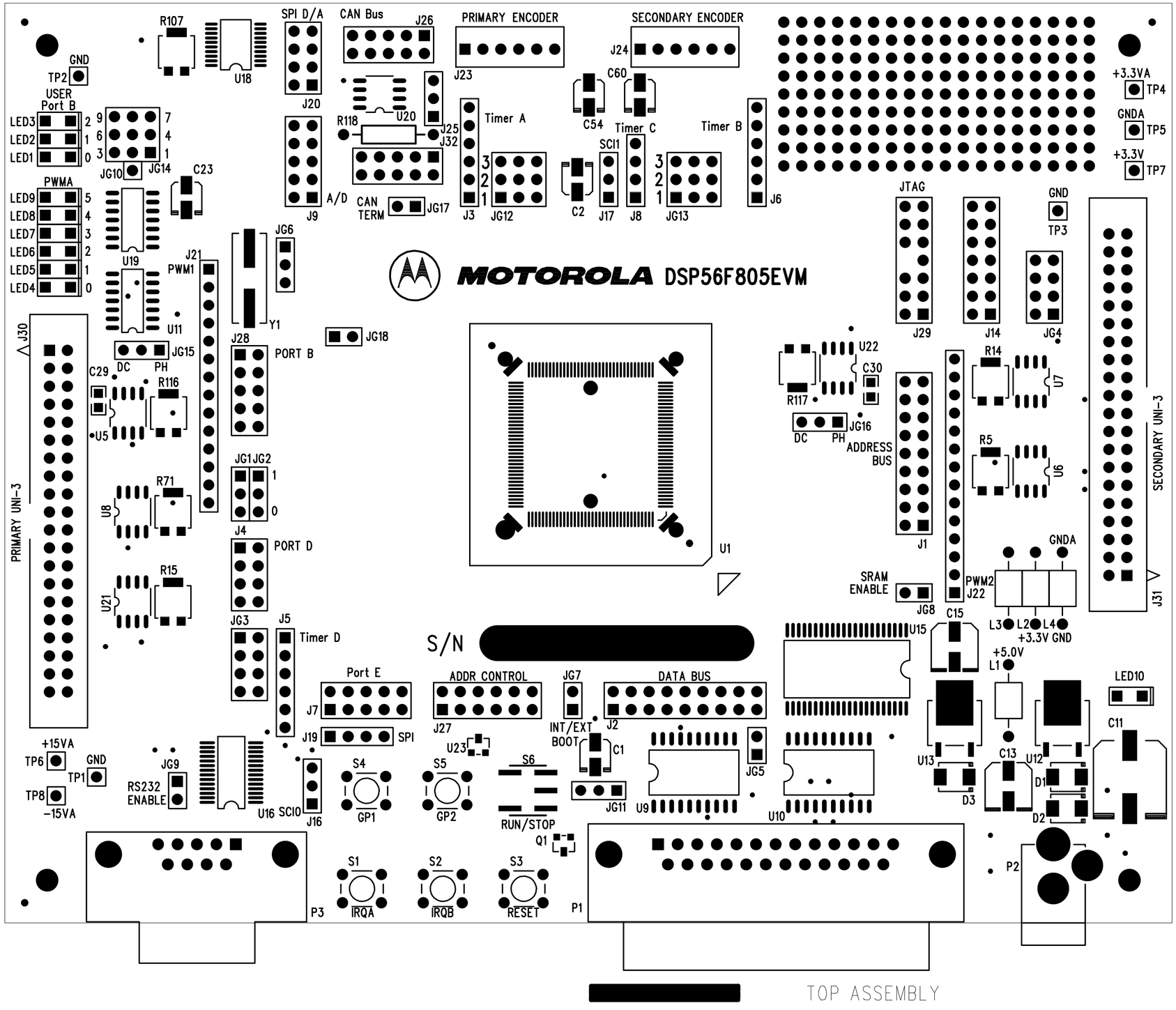
ENGINEER		
CHECKER		
DRAFTSMAN	JK	08-20-02

DO NOT SCALE DRAWING

ARTWORK DESIGNED BY:
TRAX Design L.C.
9500 W. PARMER LANE, #721
AUSTIN, TEXAS 78717 (512) 733-5075

FABRICATION DRAWING, DSP56F805EVM

SIZE B
ARTWORK FOR: Motorola
SCALE 1:1
TD# TDMOT031
SHEET 1 OF 1



TOP ASSEMBLY

ARTWORK DESIGNED BY: TRAX Design L.C. 9500 W. PARMER LANE, #721 AUSTIN, TEXAS 78717 (512) 733-5075	ARTWORK FOR: Motorola		
	BOARD NAME: DSP56F805EVM	JOB #: TDMOT031	DATE: 08-20-02

ARTWORK DESIGNED BY: TRAX Design L.L.C. 9500 W. PARMER LANE, #121 AUSTIN, TEXAS 78717 (512) 133-2012	BOARD NAME: DS25BT802EVM	JOB #: TDM01021	DATE: 10-02-08
	ARTWORK FOR: Motorola		

BOTTOM ASSEMBLY [redacted]