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Revisions

Rev	Description	Date
A	Original Release	9-29-16
A1	DNP J1, J2	11-18-16

BRKTSTBC-A8451

Analog Sensor Product Group
6501 William Cannon Drive West
Austin, TX 78735-8598

ICAP Classification: CP: IUO: X PUBI:

Designer: Aswin S	Drawing Title: BRKTSTBC-A8451		
Drawn by: Aswin S	Page Title: TITLE PAGE		
Approved: Team	Size C	Document Number SCH-29412 PDF: SPF-29412	Rev A1
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1. Unless Otherwise Specified:
   All resistors are in ohms, most are 1%, 1/10 Watt. Otherwise are 5%, 1/8 Watt.
   All capacitors are in uF, some are 10% or 20%
   All voltages are DC
   All polarized capacitors are tantalum

2. Interrupted lines coded with the same letter or letter combinations are electrically connected.

3. Use resistors R2 or R4 for selecting 7 bit I2C address between 1D and 1C respectively.

4. Resistors R5/R6 and R7/R8 can be used if INT1 and/or INT2 is used under open-drain/open-source configuration respectively.

5. Device type number is for reference only. The number varies with the manufacturer.

6. Special signal usage:
   _B Denotes - Active-Low Signal
   <> or [] Denotes - Vectored Signals

7. Interpret diagram in accordance with American National Standards Institute specifications, current revision, with the exception of logic block symbology.

ICAP Classification: CP: __ IUO: X PUBI: __

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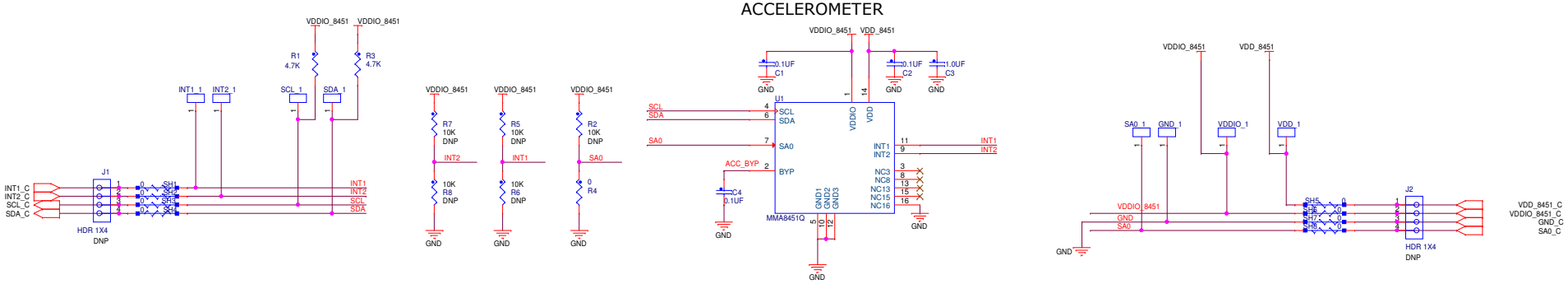
EDGE OF BRKOUT BOARD

MMA8451 I2C ACCELEROMETER



I2C address : 0x1C

ICAP Classification:	CP: ___	IUO: X	PUBI: ___
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