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FRDM-STBC-SA9500

		RF, Analog & Sensor Group 6501 William Cannon Drive West Austin, TX 78758-6599	
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Designer: Krithi S		ICAP Classification: FCP: FUK2: X PUB:	
Drawn by: Krithi S		FRDM-STBC-SA9500	
Approved: Team		CONTENT	
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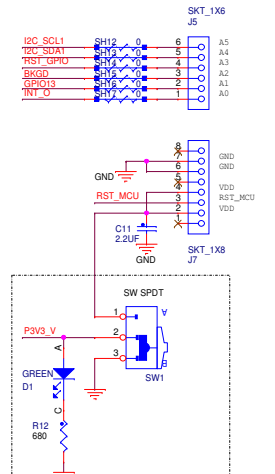
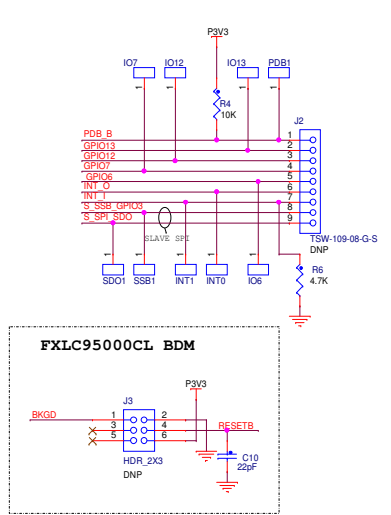
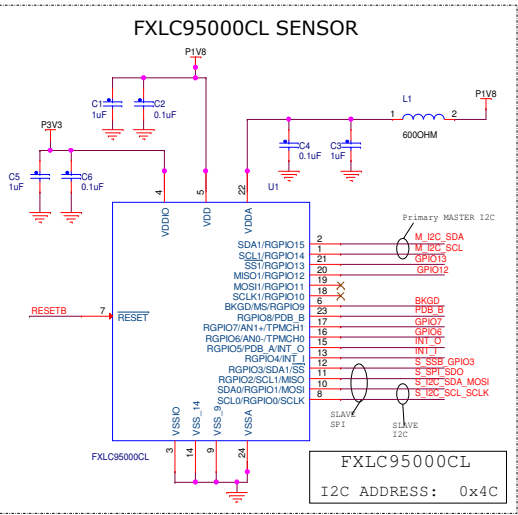
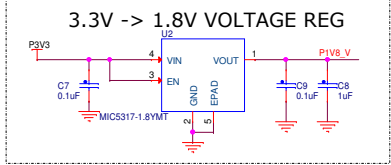
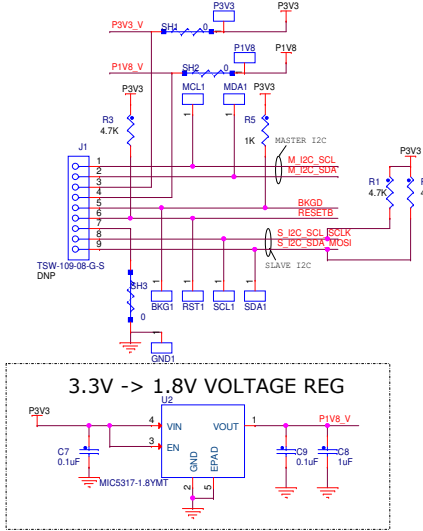
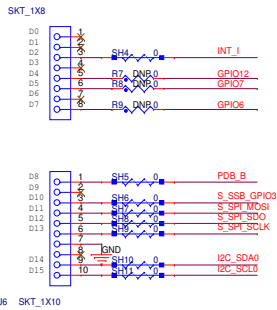
1. Unless Otherwise Specified:
All resistors are in ohms, 5%, 1/8 Watt
All capacitors are in uF, 20%, 50V
All voltages are DC
All polarized capacitors are aluminum electrolytic
2. Interrupted lines coded with the same letter or letter combinations are electrically connected.
3. Device type number is for reference only. The number varies with the manufacturer.
4. Special signal usage:
_B Denotes - Active-Low Signal
<> or [] Denotes - Vectored Signals
5. Interpret diagram in accordance with American National Standards Institute specifications, current revision, with the exception of logic block symbology.

			
ICAP Classification: FCP: _____ FIUC: X PUB: _____			
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EDGE OF BRKOUT BOARD

SHIELD BOARD ARDUINO HEADER

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MCU/GPIO RESET

I2C SLAVE BUS SELECT

I2C/SPI SHARED PINS

RGB LED FEATURE

