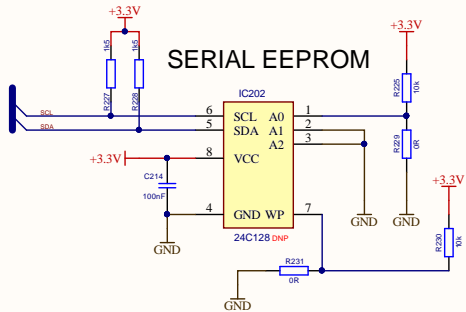
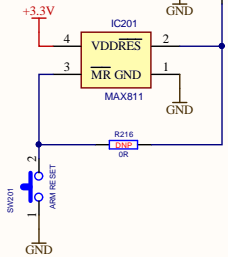
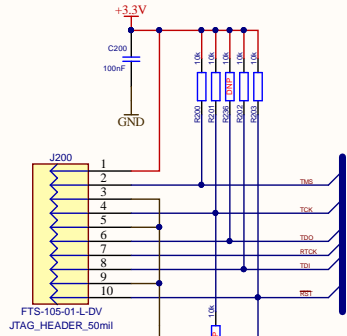
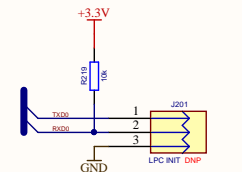
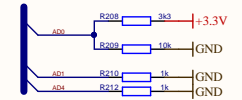
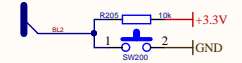
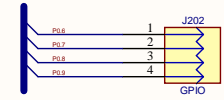
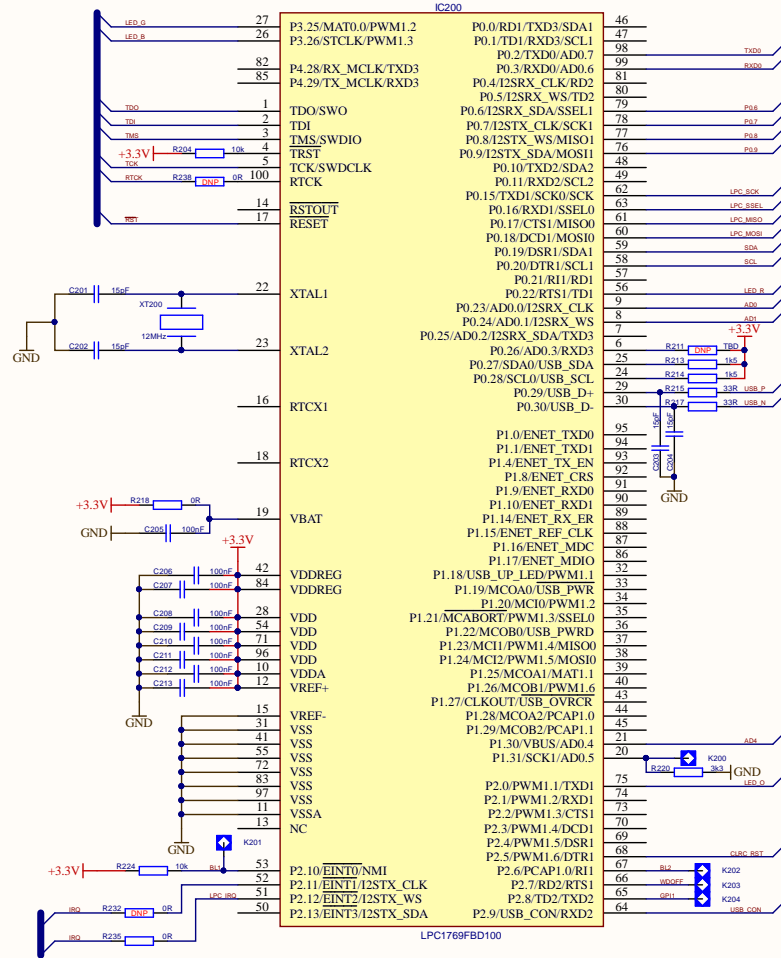


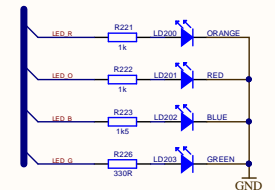
## JTAG INTERFACE



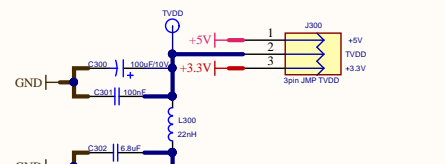
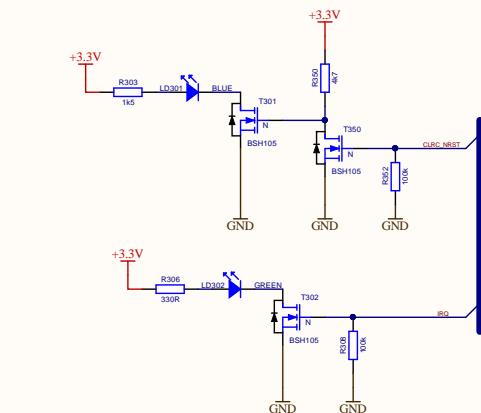
## LPC1769



## STATUS LEDS



## CLRC663 STATUS LEDS



## CLRC663

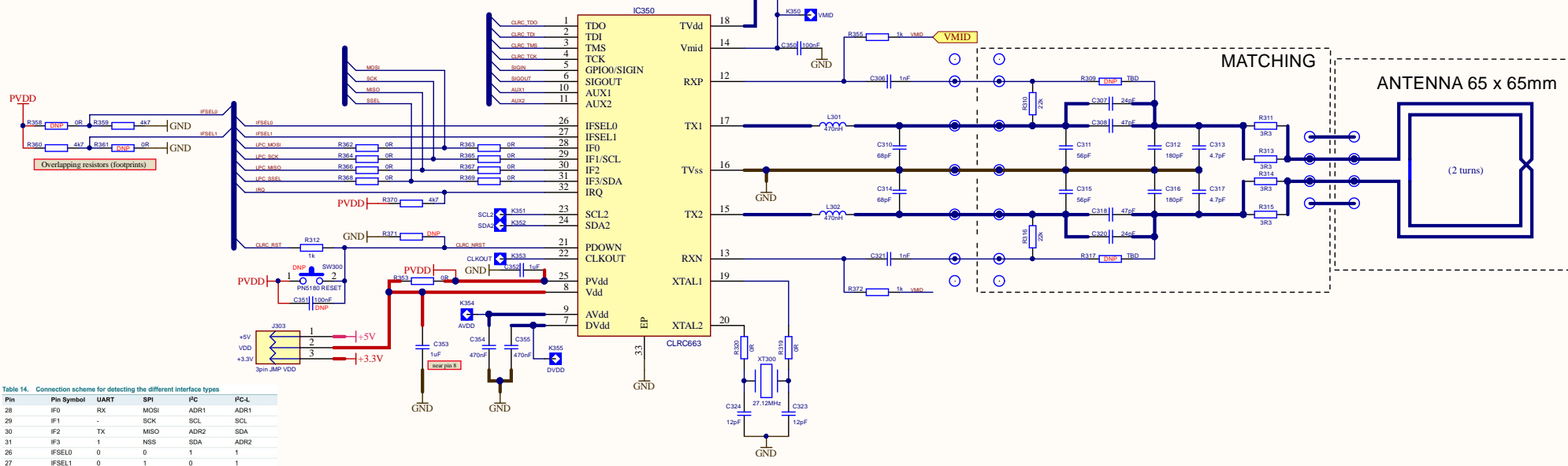
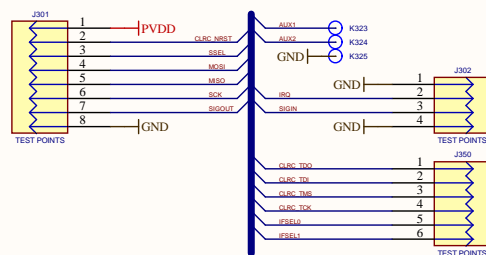


Table 14. Connection scheme for detecting the different interface types

Pin	Pin Symbol	UART	SPI	PC	PC-L
28	IF0	RX	MOSI	ADR1	ADR1
29	IF1	-	SCK	SCL	SCL
30	IF2	TX	MISO	ADR2	SDA
31	IF3	1	NSS	SDA	ADR2
26	IFSEL0	0	0	1	1
27	IFSEL1	0	1	0	1

## INTERFACE TEST POINTS



Title: CLEV6630B V2.0

Date: 11.05.2017	Drawn By:
File: Reader_CLRC663.SchDoc	Sheet: 3 of 3

