

| Table of Contents |                       |
|-------------------|-----------------------|
| 2                 | Notes & Block Diagram |
| 3                 | System power          |
| 4                 | Peripheral            |
| 5                 | Other interface       |
| 6                 | Link2 MCU             |
| 7                 | Link2 SWD             |
| 8                 | Link2 ISP             |
| 9                 | LPC55S06              |
| 10                | Jumper & DNP Tables   |
|                   |                       |
|                   |                       |

| Revisions |                        |             |          |
|-----------|------------------------|-------------|----------|
| Rev       | Description            | Date        | Approved |
| X1        | Preliminary SCH design | 08-JUL-2020 | Amish D. |
| A         | Agile Release          | 06-AUG-2020 | Amish D. |
| A1        | Replaced D3 & Y2       | 20-AUG-2021 | Amish D. |

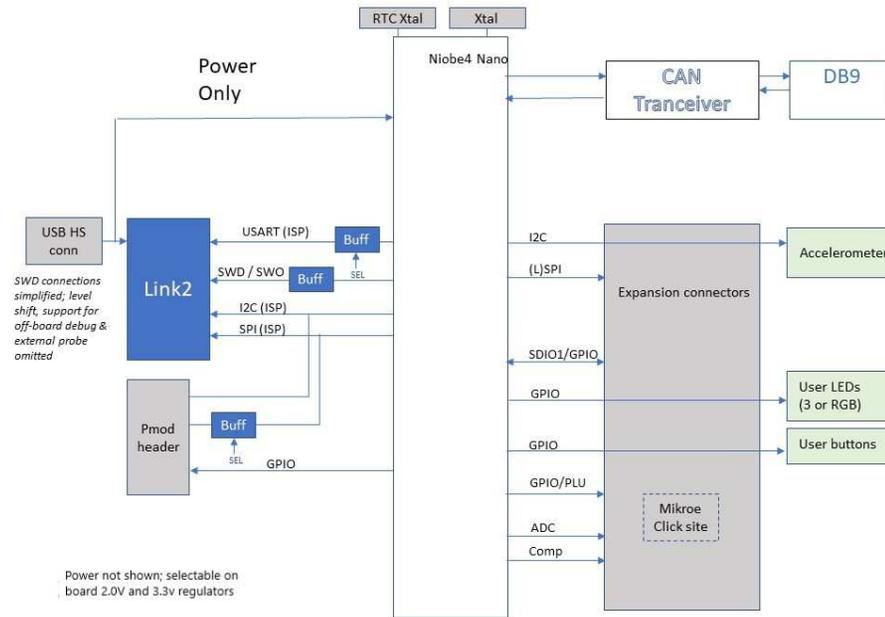
# LPC55S06-EVK

## Niobe4-Nano

This Color is for Silk  
 This Color is for User Notes  
 This Color is for CAD Notes

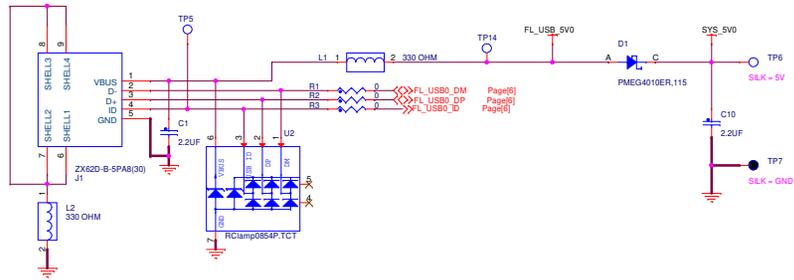
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|---|--|---|-------------|
|    |  | <b>Microcontroller Solutions Group</b>                  |             |
|   |  | 6501 William Cannon Drive West<br>Austin, TX 78735-8598 |             |
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| Designer:<br>Fernanda Islas   | Drawing Title:<br><b>LPC55S06-EVK</b>      | KAP Classification: CP:                                 | IND: X PUR: |
| Drawn by:<br>Fernanda Islas   | Page Title:<br><b>Title, TOC &amp; Rev</b> |   |             |
| Approved:<br>Amish D.   | Size:<br>C                                 | Document Number:<br>SCH-47516 PDF: SPF-47516            | Rev:<br>A1  |
| Date:<br>Thursday, August 20, 2020  | Sheet 1 of 10                              |   |             |

# System Block Diagram

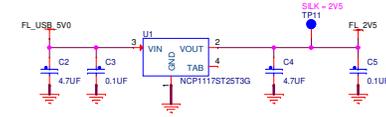


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| Classification: Company Internal/Proprietary |  |        |
| Drawing Title: <b>LPC55S06-EVK</b>           |  |        |
| Page Title: <b>Block Diagram</b>             |  |        |
| Size C                                       | Document Number SCH-47516 PDF: SPF-47516 | Rev A1 |
| Date: Thursday, August 20, 2020              | Sheet 2                                  | of 10  |

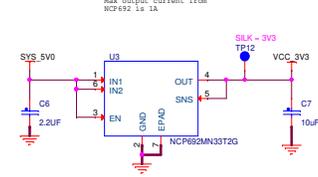
Link2  
MICRO B



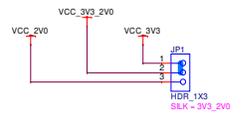
### 2V5 Regulator



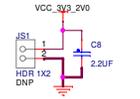
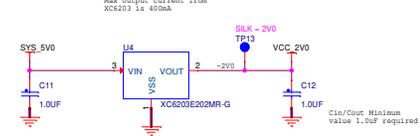
### 3V3 Regulator



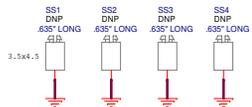
### Selector 2V0 || 3V3



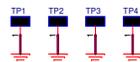
### 2V0 Regulator



### Mounting Holes

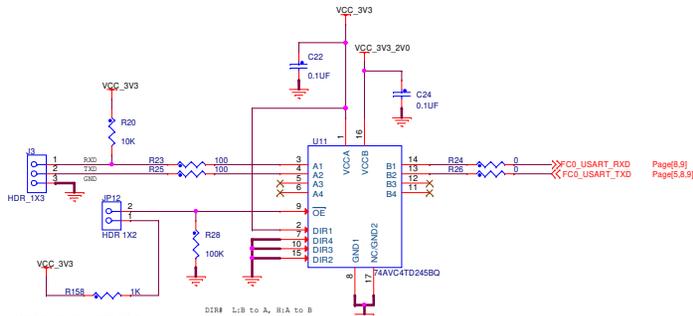
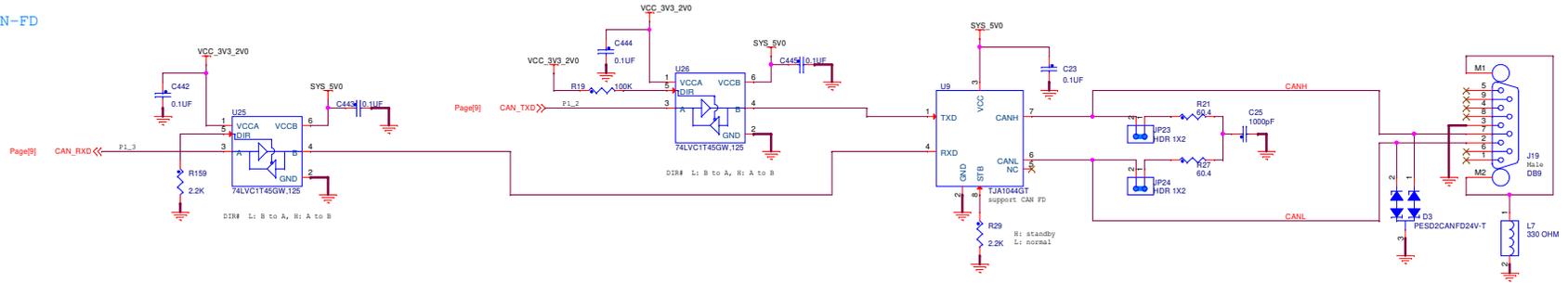


### GND TestPads



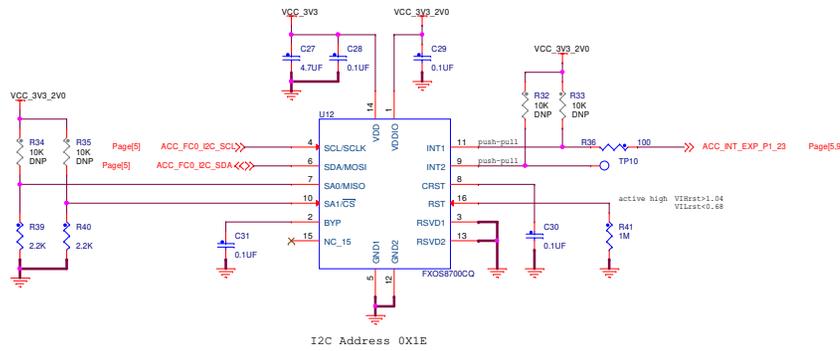
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| Drawing Title: <b>LPC55S06-EVK</b>           |  |        |
| Page Title: <b>System power</b>              |  |        |
| Size C                                       | Document Number SCH-47516 PDF: SPF-47516 | Rev A1 |
| Date: Thursday, August 20, 2009              | Sheet 3                                  | of 10  |

CAN-FD



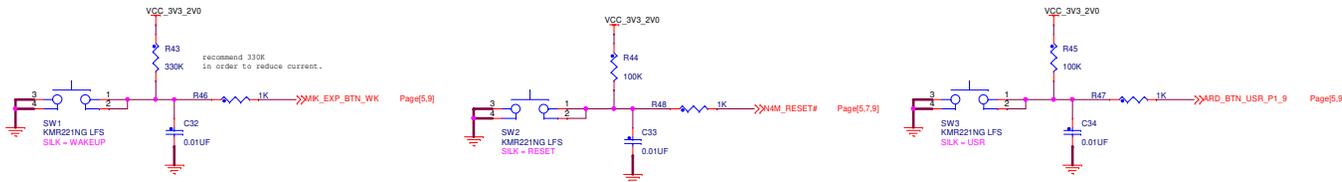
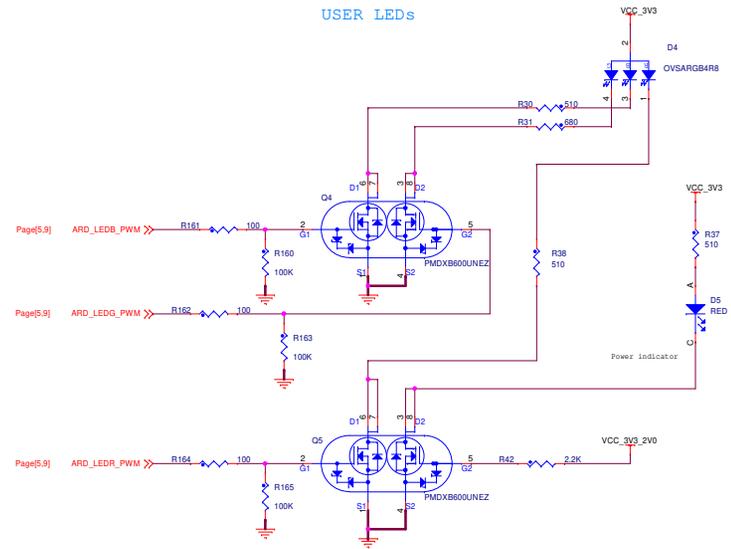
JP12: open all the time, or will disable the UART0 completely, either through external or on-board USB to UART.

ACCELEROMETER



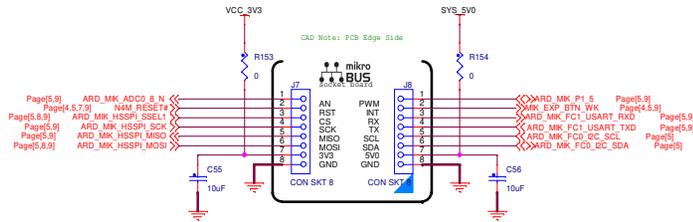
I2C Address 0X1E

USER LEDs

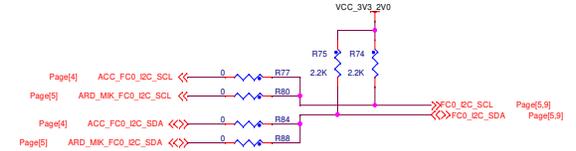


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|--|--|--------|
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| Drawing Title: <b>LPC5506-EVK</b>            |  |        |
| Page Title: <b>Peripheral</b>                |  |        |
| Size C                                       | Document Number SCH-47516 PDF: SPF-47516 | Rev A1 |
| Date: Thursday, August 20, 2020              | Sheet 4 of 10                            |        |

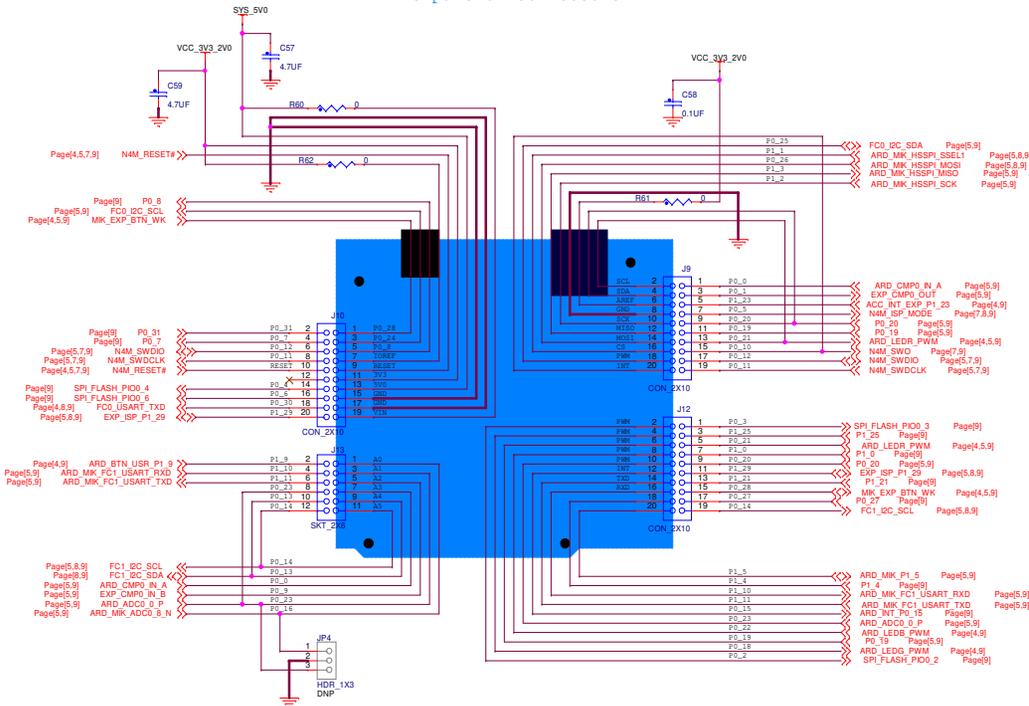
### Mikroe Click Board



1. mikroBUS<sup>®</sup> logo positioned above the socket
2. Pin names corresponding to the routing of the microboard
3. A line that encloses the socket on all sides except the top, as shown above.
4. A diagonal notch below the right-hand side pinout (serves as a guideline for add-on board insertion)



### LPCXpresso V3-style expansion connectors





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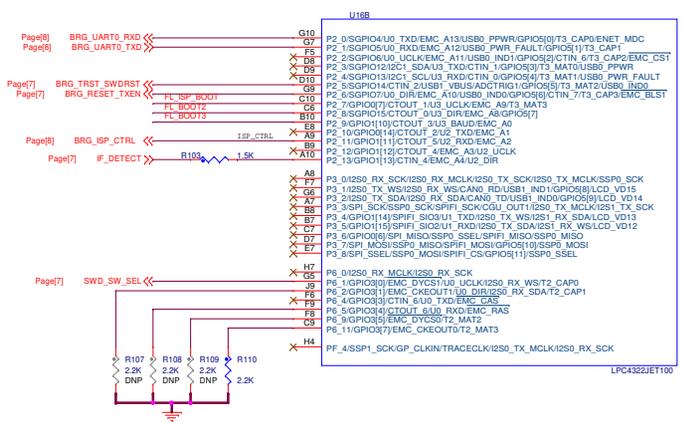
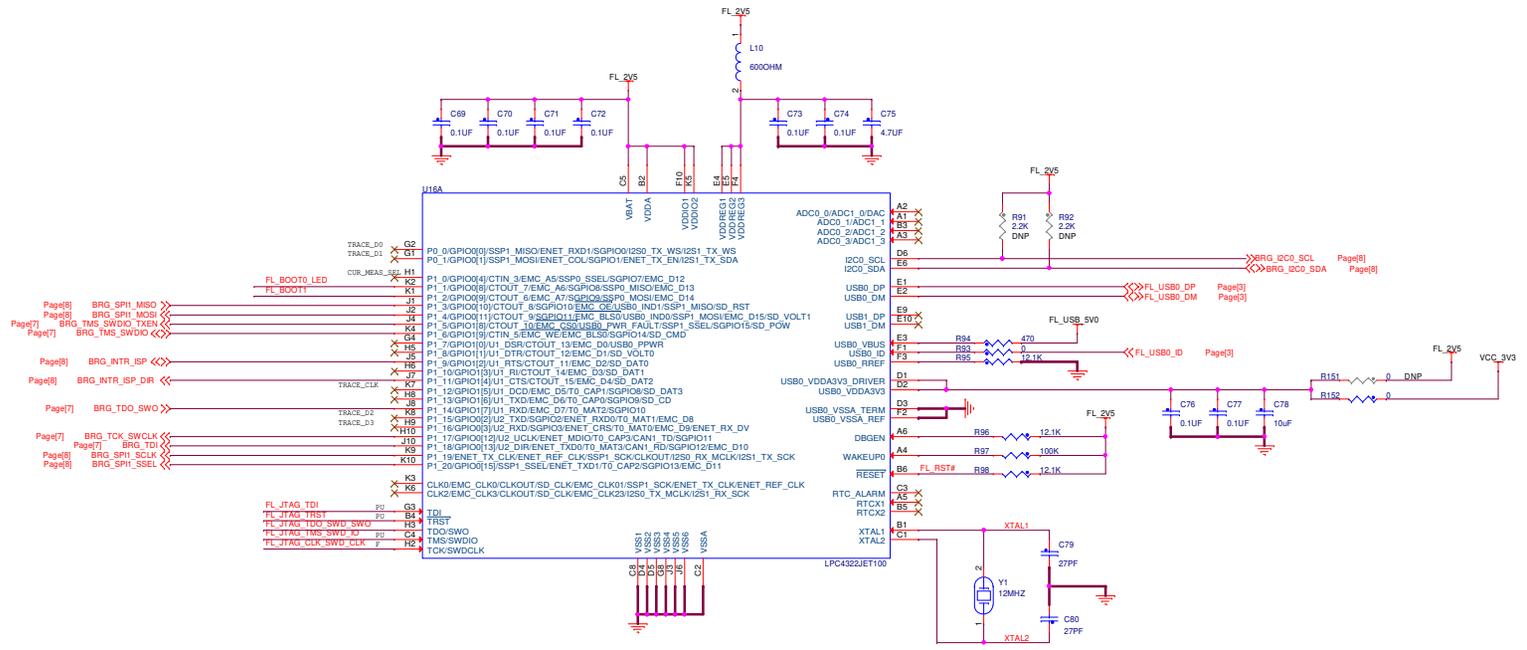
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Drawing Title: **LPC55S06-EVK**

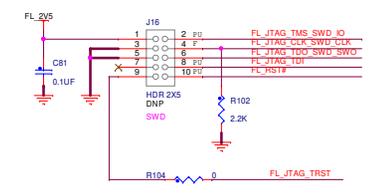
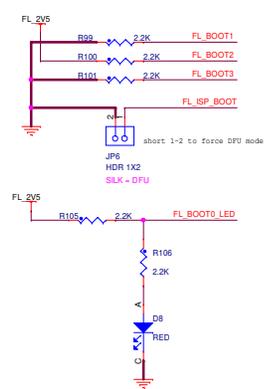
Page Title: **Interface**

|                                 |  |        |
|---------------------------------|--|--------|
| Size C                          | Document Number SCH-47516 PDF: SPF-47516 | Rev A1 |
| Date: Thursday, August 20, 2020 | Sheet 5 of 10                            |        |

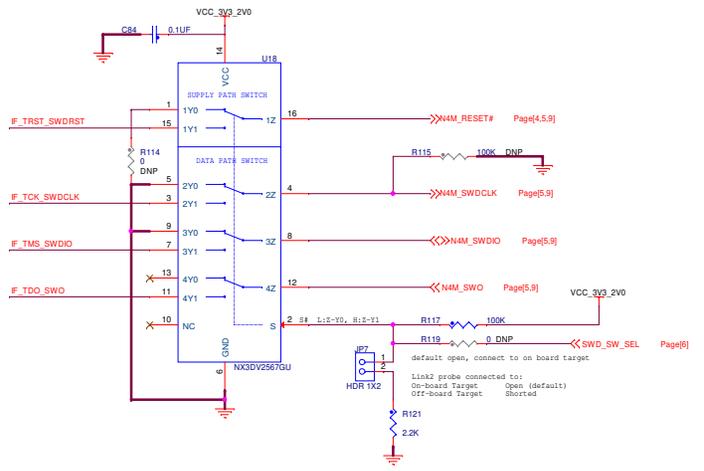
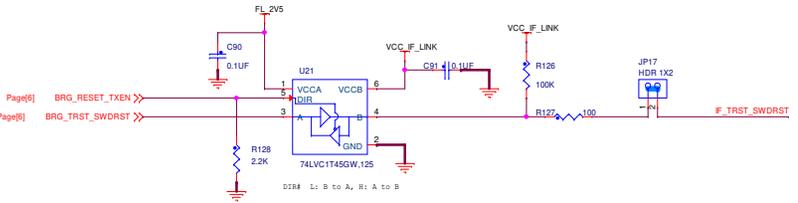
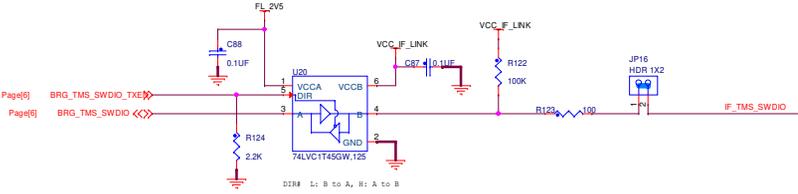
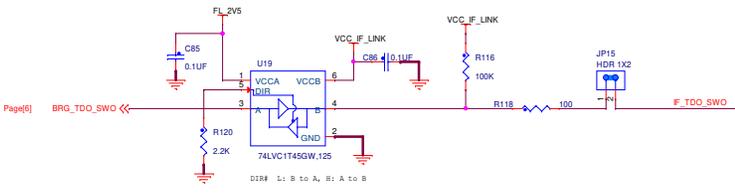
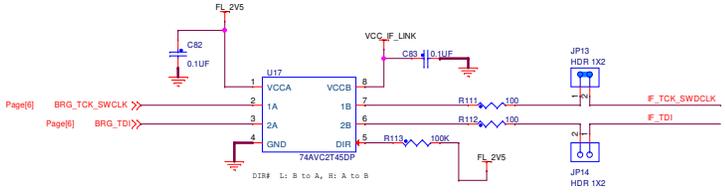
LINK2 MCU



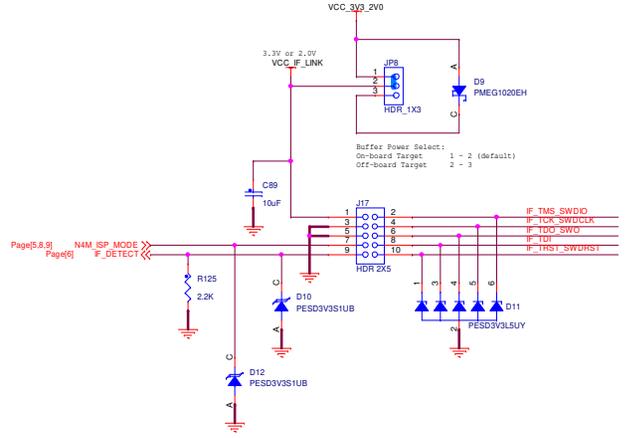
Link2  
LPC4322 Boot Mode  
DPU USB0 = B3:0 = 0101



|  |                           |                          |         |
|--|---------------------------|--------------------------|---------|
| <b>NXP</b>                                   |                           |                          |         |
| Classification: Company Internal/Proprietary |                           |                          |         |
| <b>LPC55S06-EVK</b>                          |                           |                          |         |
| Page Title: <b>Link2 MCU</b>                 |                           |                          |         |
| Size C                                       | Document Number           | SCH-47516 PDF: SPF-47516 | Rev A1  |
| Date:  | Thursday, August 20, 2020 | Sheet                    | 6 of 10 |

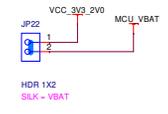
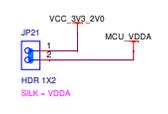
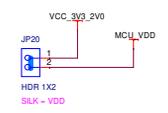


SWD CONN



|  |  |        |
|--|--|--------|
| Classification: Company Internal/Proprietary |  |        |
| Drawing Title: <b>LPC55S06-EVK</b>           |  |        |
| Page Title: <b>Link2 SWD</b>                 |  |        |
| Size C                                       | Document Number SCH-47516 PDF: SPF-47516 | Rev A1 |
| Date: Thursday, August 20, 2020              | Sheet 7 of 10                            |        |



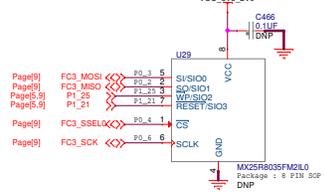


SKT 64 QFP SMT 0.5MM 680H AU

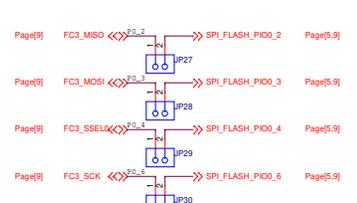
| Pin | Signal             | Pin | Signal |
|-----|--------------------|-----|--------|
| 1   | EXP_CMP0_OUT       | 101 | IO64   |
| 2   | ARD_BTN_USR_P1_9   | 102 | IO65   |
| 3   | PI_0               | 103 | IO66   |
| 4   | NAM_SWDDC          | 104 | IO67   |
| 5   | NAM_SWDDC          | 105 | IO68   |
| 6   | ARD_MK_ADC0_B_N    | 106 | IO69   |
| 7   | MCU_VDD            | 107 | IO68   |
| 8   | VREFP              | 108 | IO69   |
| 9   | MCU_VDDA           | 109 | IO68   |
| 10  | ARD_ADC0_P         | 110 | IO69   |
| 11  | NAM_SWDDC          | 111 | IO68   |
| 12  | ARD_MK_ADC0_B_N    | 112 | IO69   |
| 13  | MCU_VDD            | 113 | IO68   |
| 14  | MCU_VDDA           | 114 | IO69   |
| 15  | PO_8               | 115 | IO68   |
| 16  | MCU_VDD            | 116 | IO69   |
| 17  | PO_8               | 117 | IO68   |
| 18  | PO_8               | 118 | IO69   |
| 19  | XTAL32M_N          | 119 | IO68   |
| 20  | XTAL32M_P          | 120 | IO69   |
| 21  | ARD_MK_P1_5        | 121 | IO68   |
| 22  | NAM_RESET#         | 122 | IO69   |
| 23  | LPC_VDD_PMU        | 123 | IO68   |
| 24  | ARD_MK_FCI_USART10 | 124 | IO69   |
| 25  | SP_P1_7            | 125 | IO68   |
| 26  | ACC_INT_EXP_P1_23  | 126 | IO69   |
| 27  | MCU_VDD            | 127 | IO68   |
| 28  | FB                 | 128 | IO69   |
| 29  | IO30               | 129 | IO68   |
| 30  | IO30               | 130 | IO69   |
| 31  | LX                 | 131 | IO68   |
| 32  | MCU_VBAT           | 132 | IO69   |



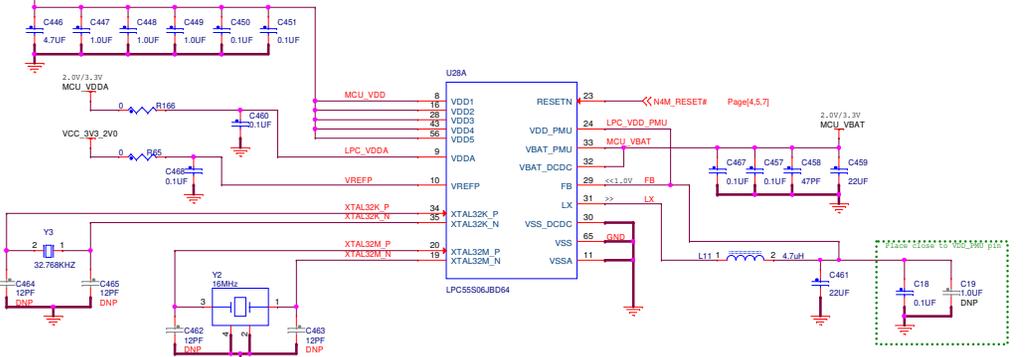
SPI NOR FLASH



SPI NOR FLASH Mode / SPI Mode



LPC55S06



| Page        | Signal                | Pin    | Signal |
|-------------|-----------------------|--------|--------|
| Page[5]     | ARD_CMP0_IN_A         | P0_3   | 36     |
| Page[5]     | EXP_CMP0_OUT          | P0_3   | 36     |
| Page[5.9]   | SPI_FLASH_PIO0_2      | P0_3-2 | 37     |
| Page[5.9]   | SPI_FLASH_PIO0_3      | P0_3-3 | 38     |
| Page[5.9]   | SPI_FLASH_PIO0_4      | P0_4-1 | 39     |
| Page[5.7,8] | NAM_ISP_MODE          | P0_4   | 39     |
| Page[5.9]   | SPI_FLASH_PIO0_6      | P0_4-6 | 40     |
| Page[5]     | ARD_MK_P1_5           | P0_4   | 39     |
| Page[5]     | EXP_CMP0_IN_B         | P0_8   | 17     |
| Page[5]     | NAM_SWDDC             | P0_8   | 17     |
| Page[5.7]   | NAM_SWDDC             | P0_8   | 17     |
| Page[5.7]   | NAM_SWDDC             | P0_8   | 17     |
| Page[5.9]   | FCI_IC_SDA            | P0_19  | 18     |
| Page[5.9]   | FCI_IC_SCL            | P0_20  | 19     |
| Page[5]     | ARD_INT_P0_15         | P0_15  | 7      |
| Page[5]     | ARD_MK_ADC0_B_N       | P0_19  | 18     |
| Page[5]     | ARD_LEDD_PWM          | P0_19  | 18     |
| Page[5]     | ARD_LEDD_PWM          | P0_20  | 19     |
| Page[4.5]   | ARD_LEDD_PWM          | P0_20  | 19     |
| Page[4.5]   | ARD_LEDD_PWM          | P0_20  | 19     |
| Page[5]     | ARD_ADC0_P            | P0_22  | 12     |
| Page[5]     | FCI_IC_SDA            | P0_22  | 12     |
| Page[5]     | FCI_IC_SCL            | P0_22  | 12     |
| Page[5]     | ARD_MK_P1_5           | P0_22  | 12     |
| Page[5]     | ARD_MK_P1_5           | P0_22  | 12     |
| Page[5.8,9] | CAN_HSSPI_ISP_PIO0_26 | P0_26  | 40     |
| Page[4.5]   | MK_EXP_BTN_WK         | P0_27  | 41     |
| Page[4.5]   | FCI_USART_RXD         | P0_28  | 42     |
| Page[4.5,8] | FCI_USART_TXD         | P0_28  | 42     |
| Page[5]     | ARD_MK_P1_5           | P0_31  | 15     |
| Page[5]     | P1_0                  | P1_0   | 4      |
| Page[5.8,9] | CAN_HSSPI_ISP_PIO1_1  | P1_1   | 38     |
| Page[5.9]   | CAN_HSSPI_ISP_PIO1_2  | P1_2   | 39     |
| Page[5.9]   | CAN_HSSPI_ISP_PIO1_3  | P1_3   | 40     |
| Page[5.9]   | CAN_HSSPI_ISP_PIO1_4  | P1_4   | 41     |
| Page[5]     | ARD_MK_P1_5           | P1_5   | 3      |
| Page[4.5]   | ARD_BTN_USR_P1_9      | P1_9   | 25     |
| Page[5]     | ARD_MK_FCI_USART_TXD  | P1_11  | 82     |
| Page[5]     | ARD_MK_FCI_USART_RXD  | P1_11  | 82     |
| Page[5.9]   | ISIP_P1_21            | P1_21  | 21     |
| Page[5.9]   | ISIP_P1_21            | P1_21  | 21     |
| Page[4.5]   | ACC_INT_EXP_P1_23     | P1_23  | 50     |
| Page[4.5]   | EXP_ISP_P1_29         | P1_23  | 53     |
| Page[5]     | P1_0                  | P1_0   | 4      |
| Page[5]     | P1_1                  | P1_1   | 38     |

### Jumper Table

| REF DES                 | JUMPER(DEFAULT) | PAGE NAME            |
|-------------------------|-----------------|----------------------|
| JP1                     | 1-2             | 03 - System Power    |
| JP24,JP23               | 1-2             | 04 - Peripheral      |
| J3,JP12                 | OPEN            | 04 - Peripheral      |
| J11                     | OPEN            | 05 - Other Interface |
| JP6                     | OPEN            | 06 - Link2 MCU       |
| JP16,JP17,JP15,JP8,JP13 | 1-2             | 07 - Link2 SWD       |
| JP14,JP7,J17            | OPEN            | 07 - Link2 SWD       |
| JS4,JS5                 | 1-2             | 08 - Link2 ISP       |
| J18,JS3,JP9             | OPEN            | 08 - Link2 ISP       |
| JP20,JP21,JP22          | 1-2             | 09 - LPC55S06        |
| JP25,JP26               | 2-3             | 09 - LPC55S06        |
| JP28,JP29,JP30,JP27     | OPEN            | 09 - LPC55S06        |

### DNP Table

| REF DES                                  | ASSY OPT | PAGE NAME            |
|--|----------|----------------------|
| JS1,SS4,SS3,SS2,SS1                      | DNP      | 03 - System Power    |
| R34,R35,R32,R33                          | DNP      | 04 - Peripheral      |
| JP4                                      | DNP      | 05 - Other Interface |
| R107,R108,R109,R91,R92,J16,<br>R151      | DNP      | 06 - Link2 MCU       |
| R115,R119,R114                           | DNP      | 07 - Link2 SWD       |
| R136                                     | DNP      | 08 - Link2 ISP       |
| C464,C462,C465,C19,C463,U29,<br>C466,J32 | DNP      | 09 - LPC55S06        |



|  |                           |                |
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| Classification: Company Internal/Proprietary |                           |                |
| Drawing Title: <b>LPC55S06-EVK</b>           |                           |                |
| Page Title: <b>Jumper &amp; DNP Tables</b>   |                           |                |
| Size   | Document Number           | Rev            |
| C  | SCH-47516 PDF: SPF-47516  | A1             |
| Date:  | Thursday, August 20, 2020 | Sheet 10 of 10 |