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Revisions			
Rev	Description	Date	Approved
X1	Prototype initial	2020/3/17	Philippe Rousseille
A	Prototype release	2020/3/26	Philippe Rousseille

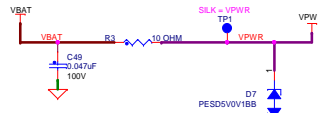
FRDM33771CSPIEBV

EVB MC33771C with SPI Interface

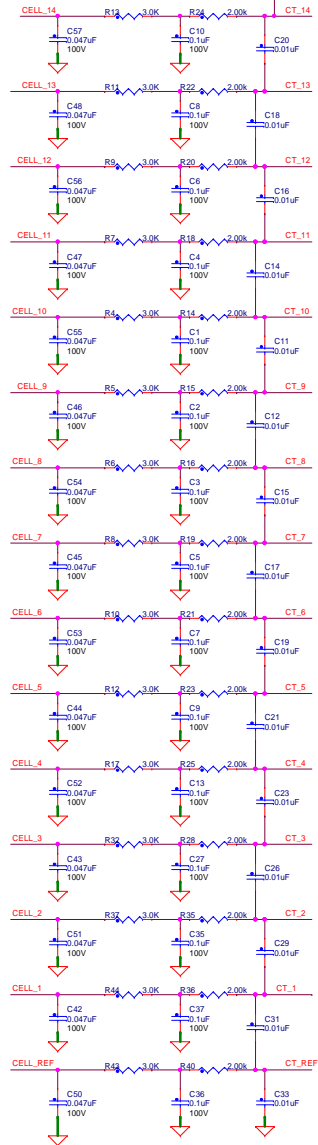
REF DES	ASSY_OPT	PAGE NAME
R41	DNP	02 - MC33771
U2,J1	DNP	03 - SPI interface

		Automotive Product Group 6501 William Cannon Drive West Austin, TX 78735-8598	
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Approved: Philippe Rousseille	Size C	Document Number SCH-47189 PDF: SPF-47189	Rev A
Date: Monday, March 30, 2020		Sheet 1 of 3	

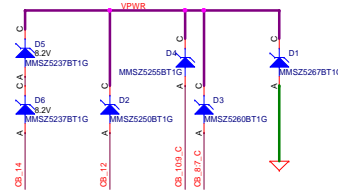
BCC Power supply



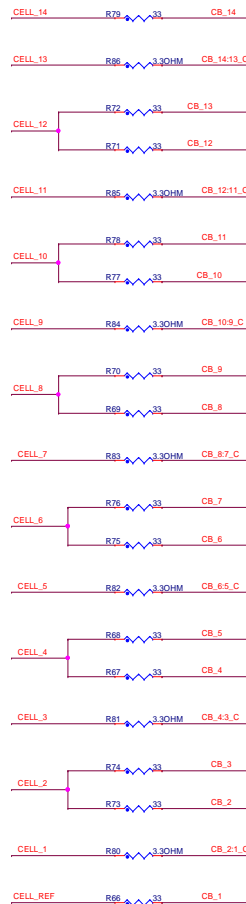
Cell Voltage Measurement



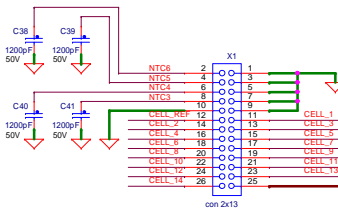
Hotplug Protection



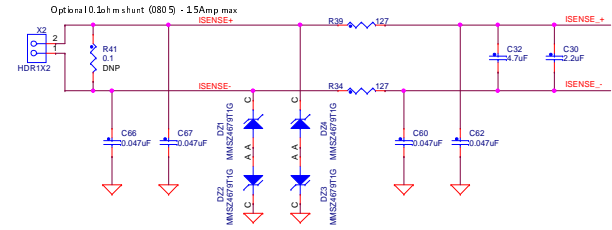
Cell Balancing Circuit



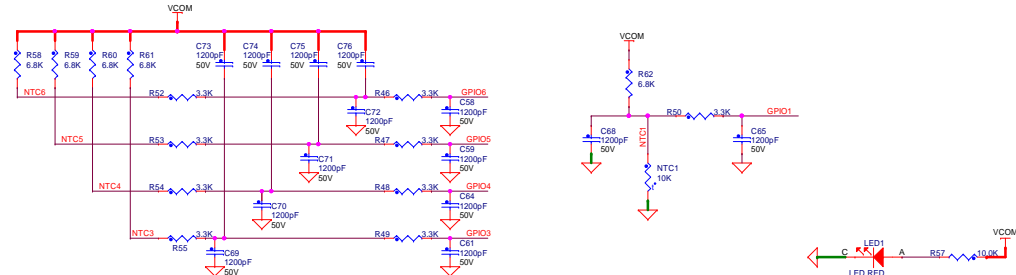
Cell Terminal connector



Isense filter



GPIO filters



BCC - Battery Cell Controller

