



TOP COMPONENTS

BOTTOM COMPONENTS

Date	Draw by	Verify	Modifications	IND.		
Date : 18/02/15 Draw by : ASCO_SR Checked by:J.GESLIN Project : PN7120		Material:	Marging : +/-0.1	Nbr of layers :		
		Finition : NI/Au chemical	Manufact.level :	Silkscreen TOP : BOTTOM:		
		Protection :	Scale : 2/1			
NXP Semiconductors 2,Esplanade Anton Philips Campus Effiscience Colombelles BP 2000 - 14906 CAEN CEDEX 9						
ASSEMBLY DRAWING PN7120 NFC Controller Board			SIZE A4	OM5577-A-400	REV. 00	PL. 1/1

