INTRODUCTION TO LAYERSCAPE ATUO & BLUEBOX 3.0 FOR HIGH PERFORMANCE COMPUTING

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SECURE CONNECTIONS FOR A SMARTER WORLD

PUBLIC

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AGENDA

Introduction

Automotive Layerscape

BlueBox 3.0 & SDK

LX2160A/BlueBox 3.0 Typical Use Case

Summary

ONE CAR, DIFFERENT MISSIONS & TECHNOLOGY EVOLUTIONS



THE AUTO-DRIVING CAR



THE ELECTRIC VEHICLE



THE SERVICE-ORIENTED CAR



Be conscious, decide, learn
Mega sensing + processing
New infrastructures



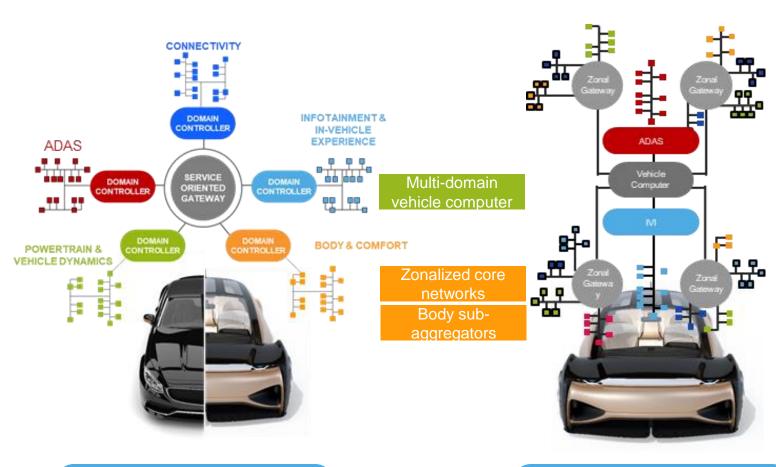
Transport at top efficiency
New infrastructures
Battery technology



Update, customize, reconfigure
Hardware virtualization
Safe hyper-connectivity



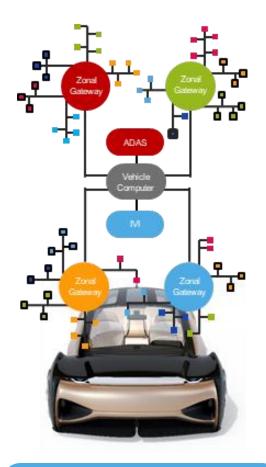
EVOLUTIONARY PATH: TWO WAVES, ON LOGICAL AND PHYSICAL PATHS



Distributing virtual tasks

IP-based service orientation

ADAS+IVI blended into zones Multi-domain edge aggregators



MY22 - Domain EE

Logical: domain Physical: dedicated

networks

MY26 - Hybrid Zonal EE
Logical: multi-domain
Physical: zonal + dedicated
networks

MY30 - Zonal EE
Logical: virtualized domain
Physical: zonal networks





LAYERSCAPE MARKETS AND TECHNOLOGIES

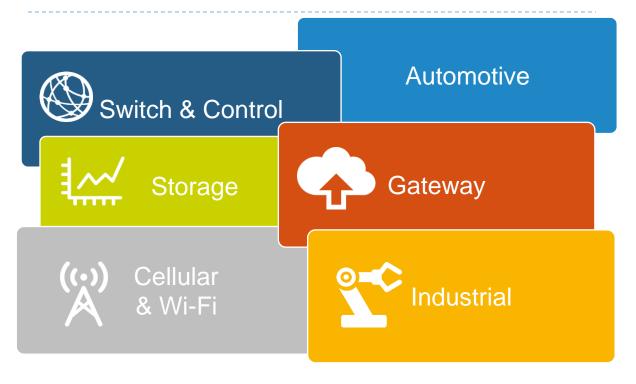
Enabling Secure Infrastructure
with Cost- and Power-Efficient Solutions
and Unique Expertise

Virtualization

Security (Trust & Protocol Acceleration)

Software Solutions and Services

Targeted Solution Segments





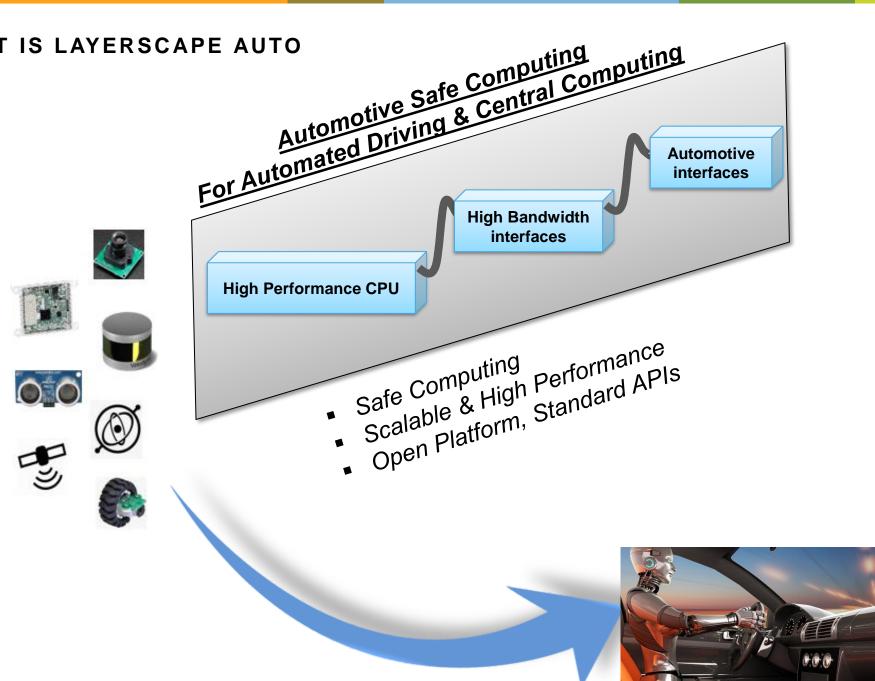


WHY NXP IS A KEY PARTNER



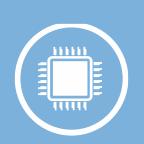


WHAT IS LAYERSCAPE AUTO





SUPPLY LONGEVITY & AUTOMOTIVE QUALIFICATION



Longevity

NXP Automotive & Industrial Application Processors

- 10 and 15-year supply longevity options
- Formal program with products listed at <u>www.nxp.com/productlongevity</u>







Automotive & Industrial applications require product longevity

- Long product lifecycles
- Special product certification required



Automotive of	
AUTOHIOTIVE	idaiiilGatiOff

- All LayerScape Automotive SoCs are AEC Q100 Grade 3 qualified (105 Tj max)
- LS1043A has an AEC Q100 Grade 2 version (125 Tj max)
- All LayerScape Auotmotive SoCs are part of the 15 year product longevity program

Safety	
Features	Collateral
 ZD-like approach to reduce risk of DPPM or Life failures Expected Operating Life fail rate <10 FIT Mission Profile: 10 years always on, 90C Tj-effective TMU, MBIST, ECC 	 Safety reference Manual FMEDA Safety for mil/aero DO-254



LAYERSCAPE SERIES: SCALABILITY AND FLEXIBILITY LEVERAGE ONE DESIGN INTO DIVERSE PRODUCT PORTFOLIO

LS1023A

2x A53



Scalable series of 64-bit ARM-based SoC Families

LS1012A 1x A53

64-bit Arm

2Gbps Packet

1Gbps Crypto

LS1012A LS1021A

> 32-bit Arm 2Gbps Pkt 1Gbps Crypto

LS1021A

2x A72

LS1024A

LS1024A

2x A9

32-bit Arm 2Gbps Pkt 2Gbps Crypto LS1028A

LS1028A

2x A72

64-bit Arm 5Gbps Crypto Integrated GPU

64-bit Arm 10Gbps Pkt 5Gbps Crypto

LS1023A

LS1026A

LS1026A

2x A72

64-bit Arm 20Gbps Pkt 10Gbps Crypto

LS1043A

LS1043A

4x A53

64-bit Arm 10Gbps Pkt 5Gbps Crypto LS1046A

LS1046A

4x A72

64-bit Arm 20Gbps Pkt LS1048A 64-bit Arm

LS1048A

4x A53

20Gbps Pkt

LS1088A

LS1088A

8x A53

64-bit Arm 20Gbps Pkt 10Gbps Crypto 10Gbps Crypto 10Gbps Crypto

LS2084A

LS2084A

8x A72

40G Pkt

64-bit Arm 20G Crypto

LS2088A 8x A72

LS2088A

64-bit Arm 40G Pkt 20G Crypto **LX2162A LX2160A**

64-bit Arm 88G Pkt 50G Crypto

LX2162A

16x A72

64-bit Arm 100G Pkt 50G Crypto

LX2160A

16x A72

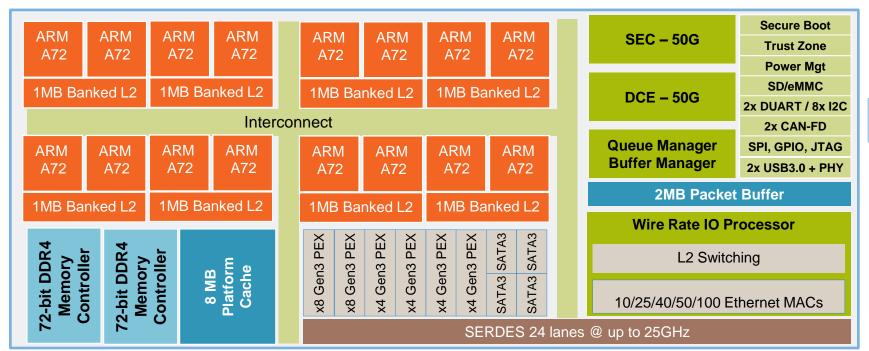
Pin-to-pin Compatible

Pin-to-pin Compatible

Software Compatible



LX2160A





Auto Qualified Parts: Now

Auto quality

- AEC Q100 Grade 3 (105 Tj)
- 15 years product longevity
- Profile: 10 years, 90C Tj-effective

Performance

- ARM A72 x 16 @ up to 2.2 GHz
- 2x72b (including ECC) DDR4 up to 3.2GT/s
- High Speed IO
- Multiple PCIe Gen3 controllers
- Multiple Ethernet MACs (up to 100G)

Functional Safety

- QM(B)
- Safety Manual, FMEDA
- ECC protected memories
- · Fault localization, containment and recovery
- Excellent support for virtualization, containerization

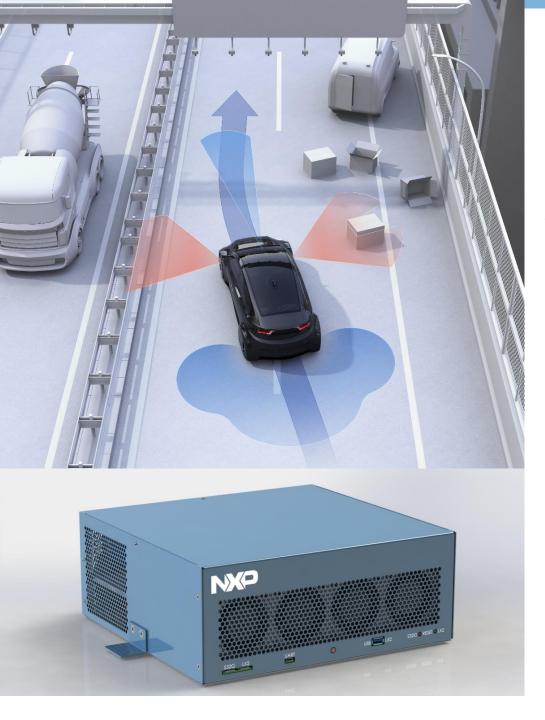
Process & Package

• 40x40mm, Lidded FCBGA, 1mm pitch (1517 pins)

Security

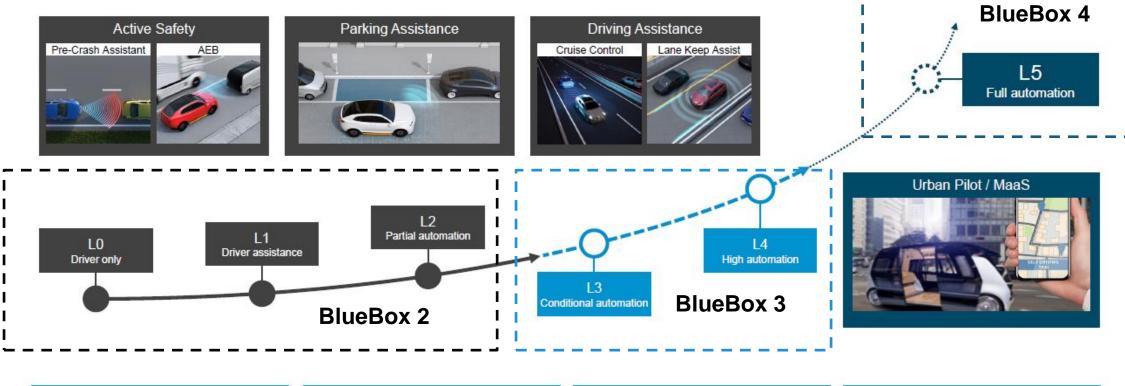
- 50Gbps Crypto Acceleration
- MACSEC, IPsec, SSL
- Trust Architecture
 - Secure Boot
 - Secure Debug
 - Secure Storage
 - Tamper Detection
 - HW Enforced Partitioning
 - ARM Trust Zone





A development platform for Automotive High Performance Compute applications such as Automated Drive and Central Computing with functional safety, Al acceleration and automotive interfaces based on existing Silicon

TARGET APPLICATIONS





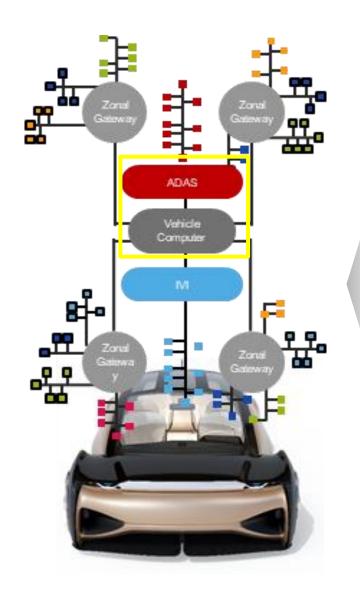






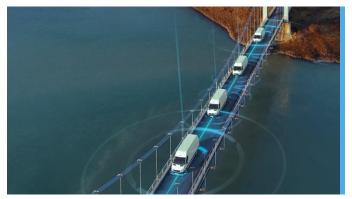


TARGET APPLICATIONS









HIGH PERFORMANCE COMPUTE

Intelligent Networking Solution
Sensor Data Intelligent Recorder
Telematics
Mobility as a Service

ADAS

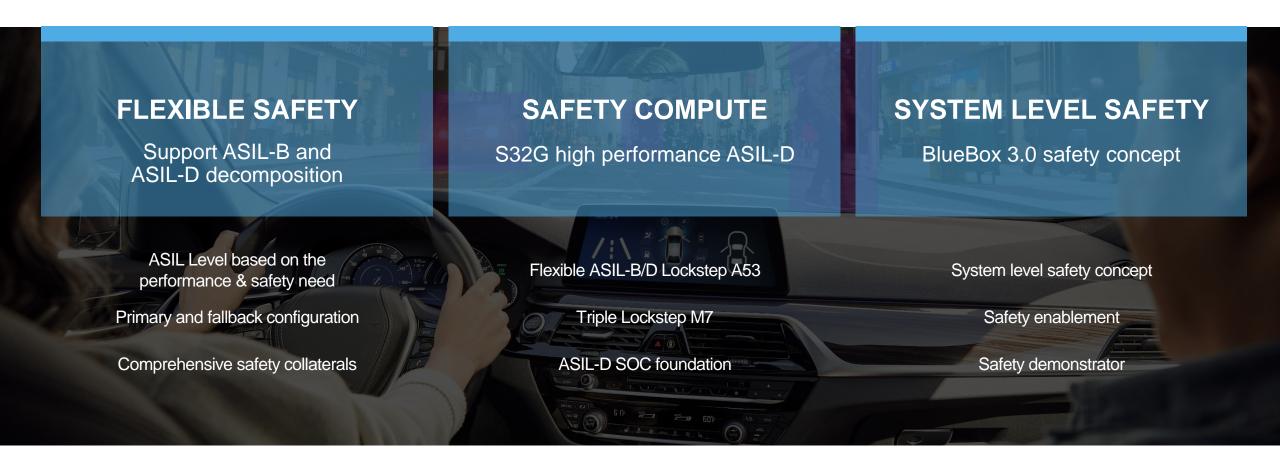
Active Safety
Parking Assistance
Driving Assistance

AD

Traffic Jam Pilot
Valet Parking
Highway Pilot
Autonomous Trucking



BLUEBOX 3.0 FUNCTIONAL SAFETY



System level safety concept with flexibility from QM, ASIL B to ASIL D



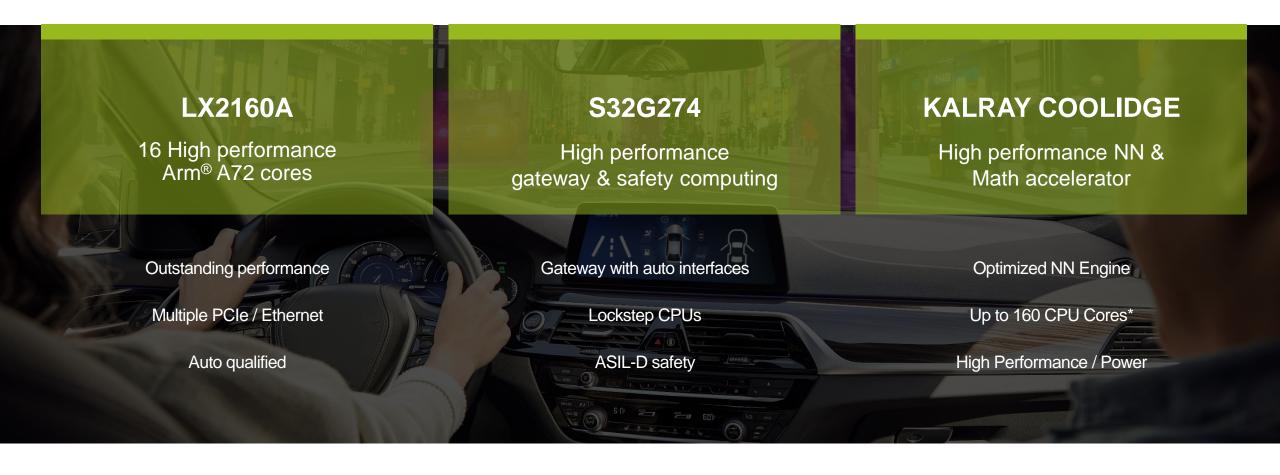
TRUE EMBEDDED SYSTEM SOLUTION



Tailored for Automotive Safe Compute with flexibility to allow customers to develop and prototype various AD and EE architectures use cases and software



ENHANCE PERFORMANCE WITH BLUEBOX 3.0



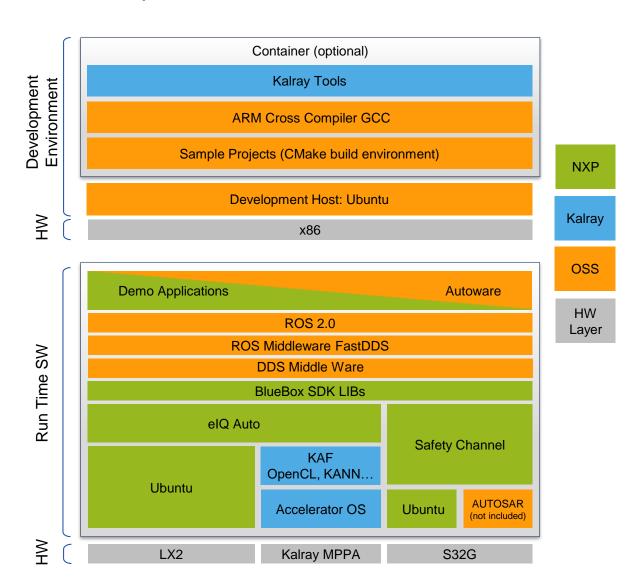
Outstanding overall performance for all Automotive Safe & High-Performance Compute workloads

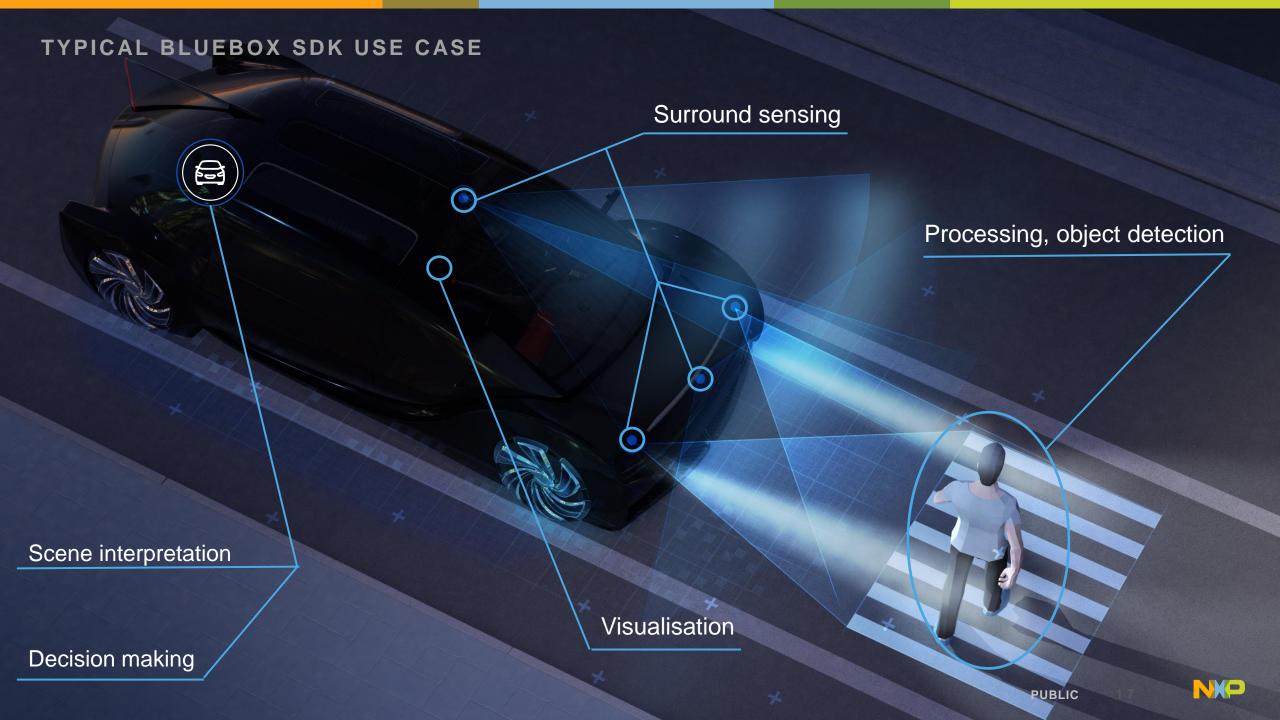


NXP

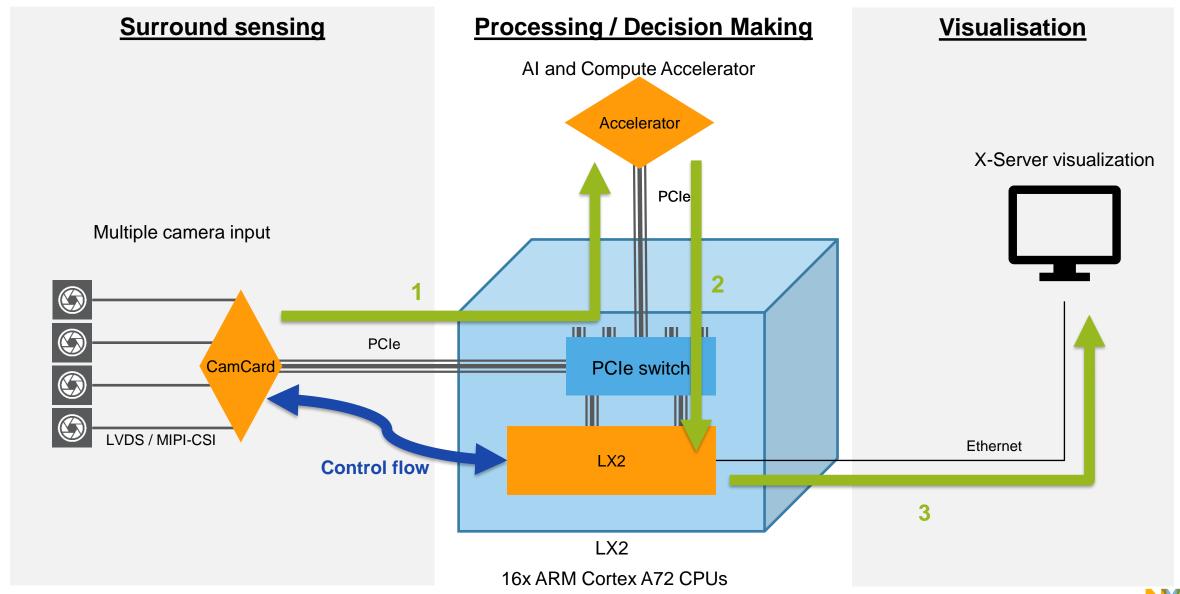
BLUEBOX SOFTWARE DEVELOPMENT KIT (BLUEBOX SDK)

- Comprehensive software development environment
 - Consistent Linux based development
 - CMake based build system
 - Integrated offline tools
- Integrated High Performance AI/ML and compute accelerator support
 - eIQ Auto, Kalray MPPA
- Support for highly efficient communication
 - FastDDS on PCIe
- Prototyping and application boilerplates
 - ROS 2, Autoware. Auto
- Automotive software grade
 - Key software components targeting Automotive SPICE



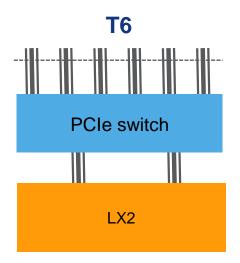


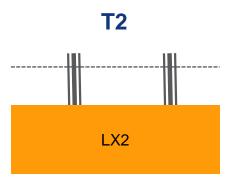
TYPICAL BLUEBOX SDK USE CASE (T6 CHASSIS)

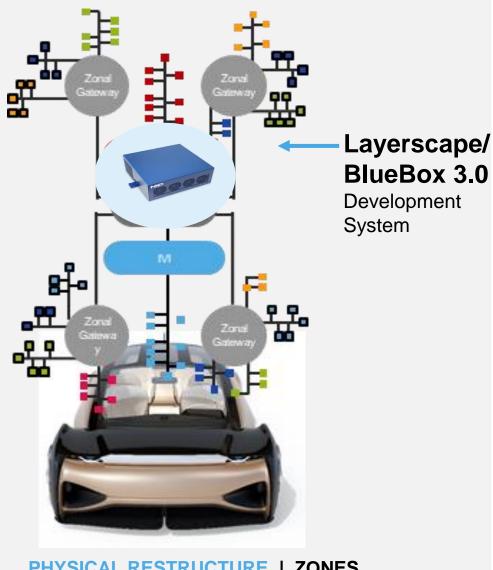


LX2 CPU

- Multi-purpose CPU
 - Post-processing
 - Data interpretation
 - Decision making
- All processes runs from LX2
 - CamCard API for image input
 - Compute acceleration via OpenCL for MPPA
 - eIQ Auto API for AI processing
- T6 Chassis has PCIe switch
 - PCIe P2P traffic through switch without LX overhead
- T2 Chassis doesn't have PCIe switch
 - PCIe P2P traffic routed through the two LX2 PCIe connections







PHYSICAL RESTRUCTURE | ZONES

ENABLING USER-DEFINED CAR

NXP LAYERSCAPE/BLUEBOX 3.0

Automotive High Performance Compute (AHPC) development platform, built with Layerscape Automotive



Protect your customers Safety



Embedded architecture Flexible and easy prototyping



Increase the speed of development Increased performance







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