

UM10392

UBA2024T SO14 13 W demo board

Rev. 3 — 25 January 2011

User manual

Document information

Info	Content
Keywords	UBA2024T, half-bridge CFL driver, non-dimmable
Abstract	This document describes the correct use of the UBA2024T half-bridge CFL driver demo boards for both 120 V and 230 V mains voltages and some circuit examples for up to 13 W



Revision history

Rev	Date	Description
v.3	20110125	third issue
v.2	20100407	second issue
v.1	20091002	first issue

Contact information

For more information, please visit: <http://www.nxp.com>

For sales office addresses, please send an email to: salesaddresses@nxp.com

1. Introduction

1.1 Safety warning

WARNING

Lethal voltage and fire ignition hazard



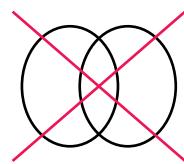
The non-insulated high voltages that are present when operating this product, constitute a risk of electric shock, personal injury, death and/or ignition of fire.

This product is intended for evaluation purposes only. It shall be operated in a designated test area by personnel qualified according to local requirements and labor laws to work with non-insulated mains voltages and high-voltage circuits. This product shall never be operated unattended.

The board needs to be connected to mains voltage. Touching the reference board during operation must be avoided at all times. An isolated housing is obligatory when used in uncontrolled, non-laboratory environments. Galvanic isolation of the mains phase using a variable transformer is always recommended. These devices can be recognized by the symbols shown in Figure 1:



019aaa690



019aaa691

a. Isolated

Not isolated

Fig 1. Variac isolation symbols

1.2 General description

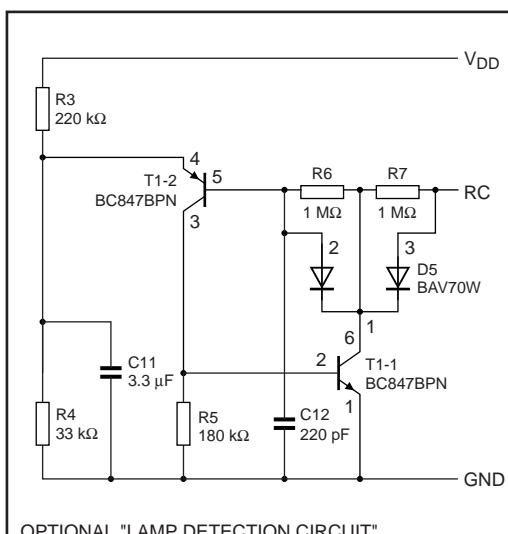
The UBA2024T circuit is a half-bridge driver IC, which has been set-up to drive a standard PLC-13 W, G24q-2 socket based lamp or similar lamp types with a nominal lamp power of 12.5 W. The total power drawn from the mains is about 13 W at a nominal mains voltage of 230 V (RMS); 50 Hz or 120 V (RMS); 60 Hz set-up. The board can easily be configured to drive different Compact Fluorescent Lamps (CFL) of different power ratings as some design examples will show by changing the inductor tap and applying a different lamp capacitor. The UBA2024T demo board is not recommended for driving lower voltage linear lighting lamps like the T5 or the T8. The UBA2021 is the optimal option for these type of lamps. The IC is able to drive lamps up to 15 W provided the maximum junction temperature of the IC is not exceeded. There are no THD requirements for mains powers lower than 25 W so that a preconditioning function is obsolete.

The circuit is set-up to do a quasi preheat, so the lamp will turn on approximately 0.7 s after the mains voltage has been applied to the board. For detailed design steps on how to set-up lamps with other power ratings please consult the application note *AN10713*.

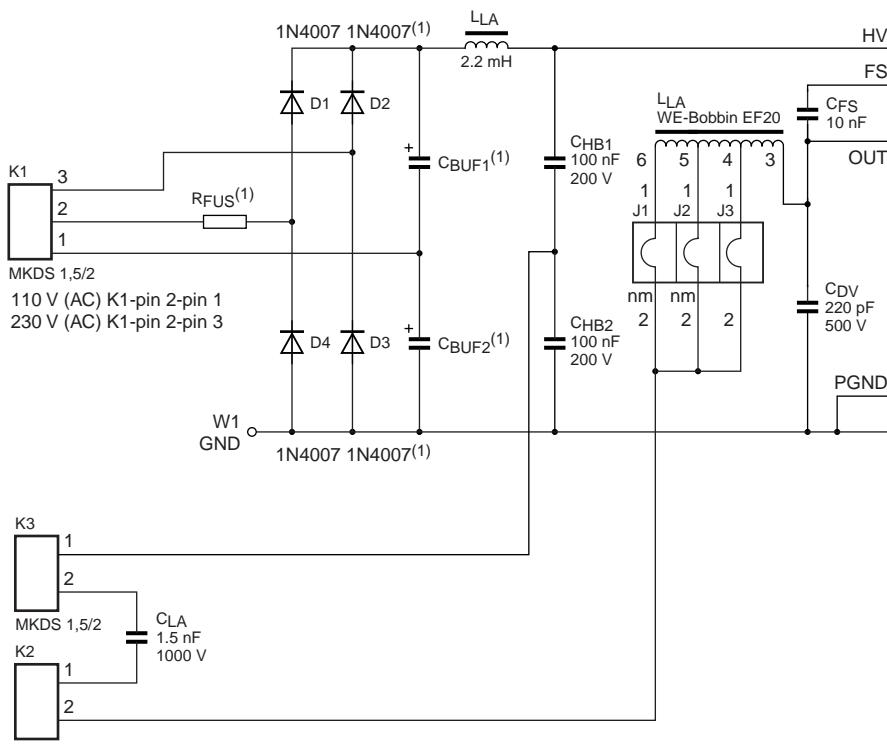
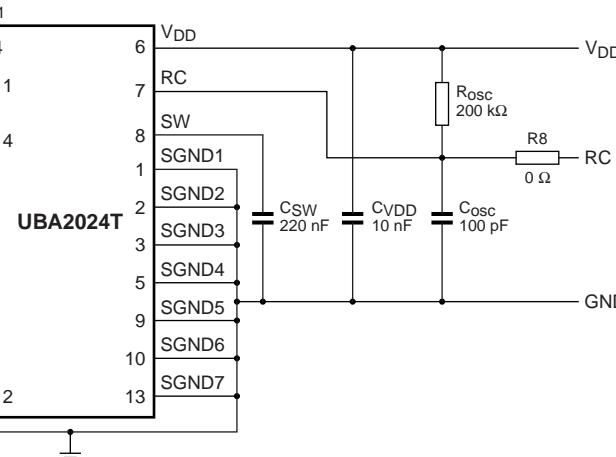
Depending on the ordered board the mains voltage operating range is either set for 90 V to 130 V (RMS) or 200 V to 250 V (RMS). Both voltage range strappings have been incorporated in one layout of the board. This makes easier to set-up the same board with a different voltage range. Since the IC was basically intended as a cost-effective solution to drive CFLs with an integrated ballast (CFLi), the IC is not equipped with a thermal protection or open lamp detection. As the demo board has been set-up around a detachable lamp, a protection circuit has been added to it to set the IC to a safe mode of operation when no lamp is attached to the circuit. This circuit is not needed in a typical CFL application.

Remark: If the UBA2024AT is used in a non-integrated ballast or a 'matchbox' type of ballast, the protection circuit is a requirement.

2. Schematic diagram



019aab390



(1)NOTE! design combines 110V (AC) and 230V (AC)

120 V (AC):
RFUS = 6.8 Ω/1 W
CBUF1, CBUF2 = 10 μF/200 V
D2 and D3 NOT mounted
K1 mounted on position 1, 2

230 V (AC):
RFUS = 10 Ω/1 W
CBUF1 = 6.8 μF/400 V
CBUF2 = wire bridge
D1 to D4 are all mounted 1N4007
K1 mounted on position 2, 3

lamp inductor selection

J1, J2, J3 are 0 Ω resistor jumpers
J1 = 2.1 mH
J2 = 2.7 mH
J3 = 3.1 mH, default set for 13 W

DO NOT short more than one jumper at the same time.

Fig 2. Schematic diagram

3. Specification

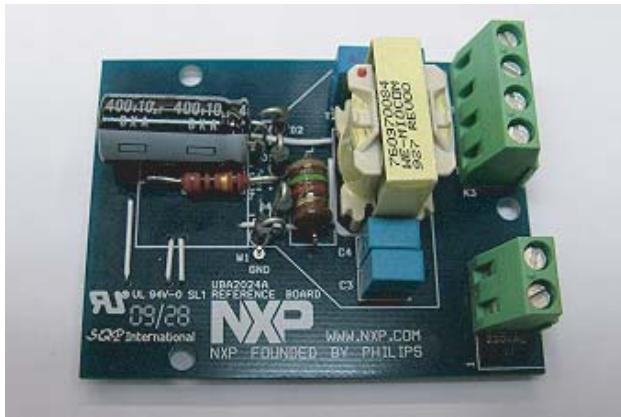


Fig 3. UBA2024T 230 V (AC) mains demo board

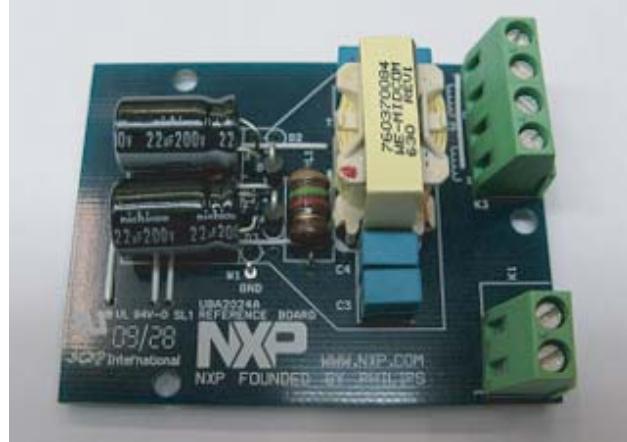


Fig 4. UBA2024T 120 V (AC) mains demo board

The UBA2024T demo board is set-up to drive an 13 W burner with a G24q-1 type of socket. The specifications for this setup are:

230 V (AC):

- Input voltage range: 230 V (AC); ± 15 %; 50 Hz
- Input power: 13 W at 230 V (AC)
- Input current: 105 mA at 230 V (AC)
- Power factor: 0.55
- Running frequency 44 kHz; start frequency 110 kHz
- 700 ms quasi-preheat

120 V (AC):

- Input voltage range: 120 V (AC); ± 15 %; 60 Hz
- Input power: 13 W at 120 V (AC)
- Input current: 180 mA at 120 V (AC)
- Power factor: 0.58
- Running frequency 44 kHz; start frequency 110 kHz
- 700 ms quasi-preheat

Protections:

- No load and lamp removal protection by means of external protection circuit

Burners:

- Osram Dulux D/E 13 W; 4-pin; G24q-1
- Philips PL-C 13 W; 4-pin; G24q-1

- General Electric F13DBX ECO 4P; G24q-1

Other burners that are electrically possible and safe to use:

- Osram Dulux T/E 13 W; 4-pin; Gx24q-1
- Philips PL-T 13 W; 4-pin; Gx24q-1
- General Electric F13TBX ECO 4P; GX24q-1
- All T2 or T3 12.5 W burners with 80 V lamp voltage and 165 mA lamp current

3.1 Board connections

The connection to the lamp is very straight forward as the [Figure 5](#) and [Figure 6](#) show. The board has been designed to accommodate layouts for 120 V (AC) or 230 V (AC) line voltages. An ordered board is preset for a certain line voltage. The labeling on the board for the mains voltage connector has been designed in such a way that the correct line voltage label becomes visible when the two way screw terminal block for the mains voltage is soldered to the proper position.

When a board for a specific line voltage is ordered, the customer is free to set it up for a different line voltage. Ensure that the position of the two way screw terminal block is changed accordingly, so the correct mains voltage label is visible.

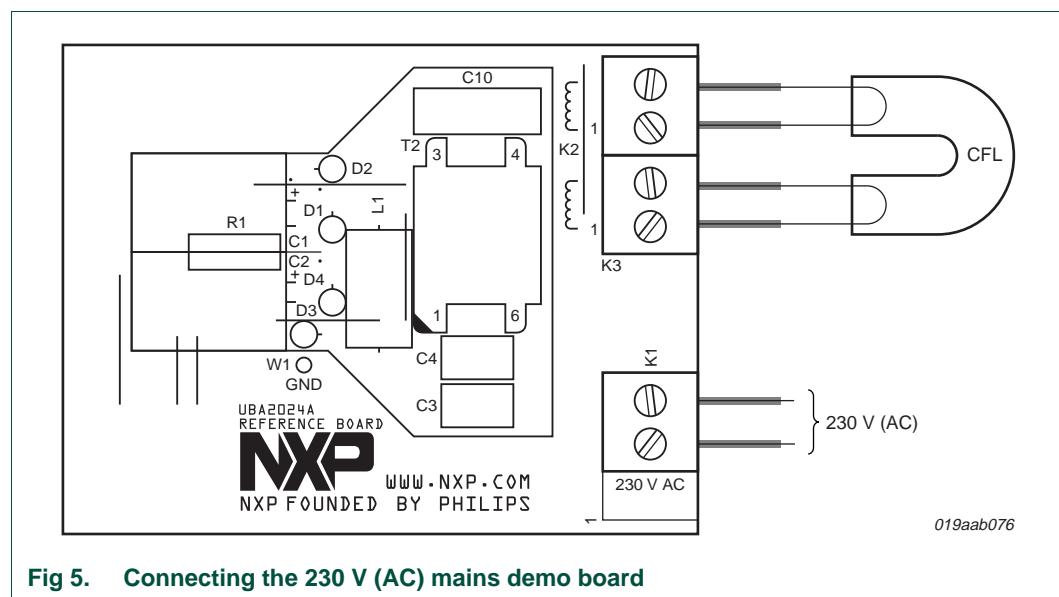


Fig 5. Connecting the 230 V (AC) mains demo board

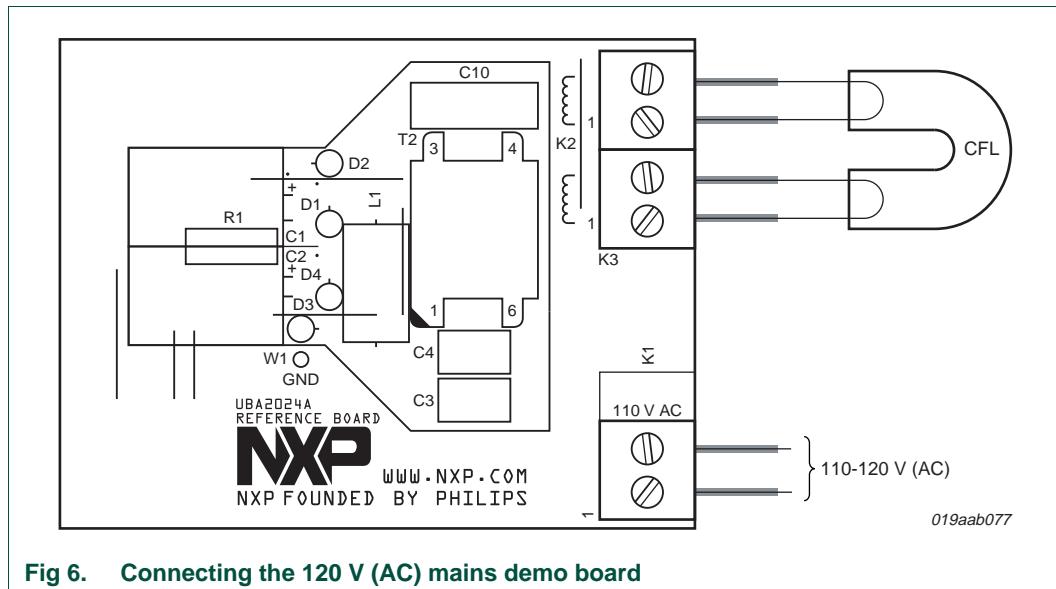


Fig 6. Connecting the 120 V (AC) mains demo board

3.2 Lamp inductor selection

The inductor supplied with this board has been made to accommodate three inductors in one. This makes setting up the board for different lamp powers easier, since it is much easier to change the lamp capacitor than the lamp inductor. It also speeds up the design time (see [Section 5](#) and the application note [AN10713](#)).

[Figure 5](#) shows how to select a different lamp inductor. The inductor can be set for 3.1 mH (default setting on delivery for the 13 W lamp), 2.7 mH, and 2.1 mH. The saturation current for the 2.1 mH inductor setting is 1.1 A at 125 °C ambient.

Remark: Only short one jumper, otherwise the inductor windings become shorted.

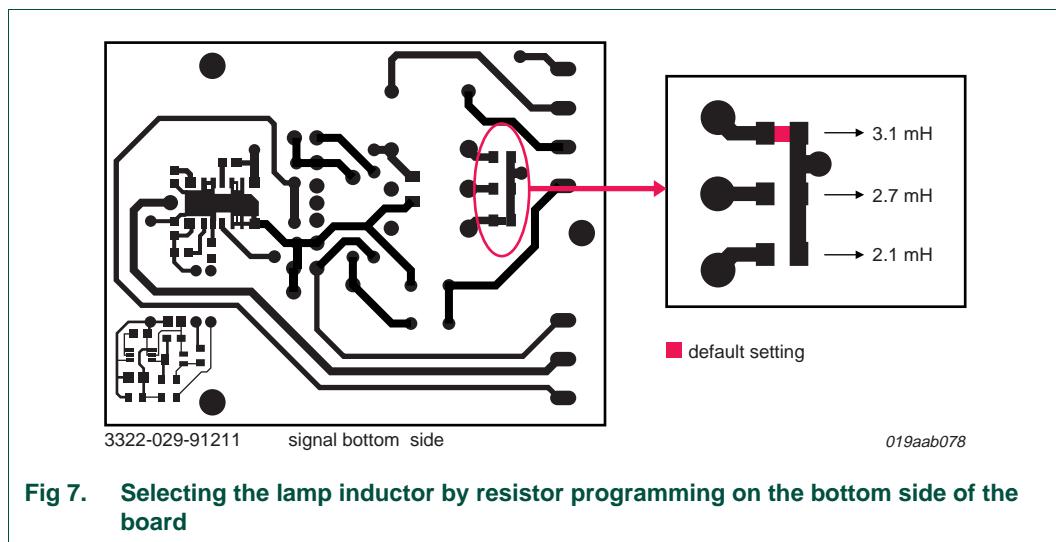


Fig 7. Selecting the lamp inductor by resistor programming on the bottom side of the board

3.3 Bill of material 13 W lamp

Table 1. 13 W lamp (12 W; 150 mA burner; requiring warm ignition; $f_O = 44$ kHz)

Reference	Description	Remarks	115 V; 60 Hz	230 V; 50 Hz
R _{FUS}	fusible inrush current limiter resistor	special type, fusible, high peak power	6.8 Ω	10 Ω
D1, D2	voltage doubler diodes		1N4007	-
D1, D4	bridge rectifier diodes		-	1N4007
C _{BUF1} , C _{BUF2}	buffer capacitors	high temperature electrolytic type	10 μF; 200 V	-
C _{BUF1}	buffer capacitor	high temperature electrolytic type	-	6.8 μF; 400 V
L _{FILT}	filter inductor	axial type	2.2 mH; 200 mA	2.2 mH; 200 mA
C _{HB1} , C _{HB1}	half-bridge capacitors		100 nF; 400 V	100 nF; 400 V
C _{LA}	lamp capacitor	high voltage polypropylene film type capable of withstanding peak voltages	1.5 nF; 800 V	1.5 nF; 800 V
L _{LA}	lamp inductor	E20 core for lamp powers up to 23 W; Würth electronic type: 760370084 (see Section 6); J1 = open; J2 = open; J3 = closed	3.1 mH	3.1 mH
C _{DV}	dV/dt limiting capacitor		220 pF; 500 V	220 pF; 500 V
C _{FS}	floating supply buffer capacitor	SMD: X7R type; leaded: PET type, high temperature	10 nF; 50 V	10 nF; 50 V
C _{VDD}	low voltage supply buffer capacitor	SMD: X7R type; leaded: PET type, high temperature	10 nF; 50 V	10 nF; 50 V
C _{osc}	oscillator capacitor	SMD: NP0 type; leaded: C0G type, preferably high accuracy value type	100 pF; 50 V; 2 %	100 pF; 50 V; 2 %
R _{osc}	oscillator resistor	preferably E96 series high accuracy value type	200 kΩ; 1/8 W; 1 %	200 kΩ; 1/8 W; 1 %
C _{sw}	sweep time capacitor	SMD: X7R type; leaded: PET type, high temperature	220 nF; 50 V	220 nF; 50 V
U1	CFL half-bridge driver IC	NXP ordering code: 9352 703 84518	UBA2024T	UBA2024T

Table 2. Components values for the optional lamp detection circuit

Reference	Description	Remarks	Value
R3	resistor	preferably E24 series high accuracy value type	220 kΩ; 0.125 W; 1 %
R4	resistor	preferably E24 high accuracy value type	33 kΩ; 0.125 W; 1 %
R5	resistor		180 kΩ; 0.125 W
R6, R7	resistor		1 MΩ; 0.125 W
C11	ignition time-out capacitor	MLCC X7R type with a voltage rating \geq 10 V	3.3 μF; 10 V
C12	capacitor	ceramic or NP0; leaded type	220 pF; 16 V
D5	double diode common cathode		
Q1-1, Q2-2	PNP/NPN transistor in one package or use separate transistors.	$h_{fe} > 100$ at 10 μA	BC847BNP
Q1-1		$h_{fe} > 100$ at 10 μA	BC847B
Q2-2		$h_{fe} > 100$ at 10 μA	BC857B

4. Thermal considerations SO package

As can be seen on [Figure 3](#) and [Figure 4](#) the board outline for the UBA2024AT in the SO14 package drawn on the demo board is T shaped. The reason for this shape is that an actual board with a similar shape is intended to be mounted vertically into a CFL lamp base. In this way distance is created between lamp filaments and the IC.

In most situations the PCB is mounted horizontally into a lamp base, but since the UBA2024AT is a SMD component this would mean the IC is very close to the lamp filaments. The lamp filaments would directly radiate heat onto the IC. This would limit the IC drive capabilities. It is recommended to either mount some form of heat shield in between the lamp filaments and the PCB or to mount the PCB in vertically into the lamp socket. This increases the distance between IC and lamp filaments.

Another solution is to mount the SMD components: C_{FS}, C_{VDD}, C_{osc}, R_{osc}, C_{sw} and the UBA2024AT onto a separate PCB and mount this PCB perpendicular onto a horizontal PCB under the lamp that contains all the leaded components. In this way the horizontal PCB serves as a shield between the lamp filaments and the heat sensitive components on the vertical PCB.



019aab162

Fig 8. Proposed board placement in a lamp for the UBA2024AT to reduce radiated heat to the IC

5. Examples of different lamp powers

5.1 8 W lamp

Table 3. 8 W lamp (7 W; 150 mA burner; suited for cold ignition; $f_O = 46 \text{ kHz}$)

Reference	Description	Remarks	115 V; 60 Hz	230 V; 50 Hz
R _{FUS}	fusible inrush current limiter resistor	special type, fusible, high peak power	10 Ω	39 Ω
D1, D2	voltage doubler diodes		1N4007	-
D1, D4	bridge rectifier diodes		-	1N4007
C _{BUF1} , C _{BUF2}	buffer capacitors	high temperature electrolytic type	10 μF; 200 V	-
C _{BUF1}	buffer capacitor	high temperature electrolytic type	-	3.3 μF; 400 V
L _{FILT}	filter inductor	axial type	2.7 mH; 200 mA	2.7 mH; 200 mA
C _{HB1} , C _{HB1}	half-bridge capacitors		47 nF; 400 V	47 nF; 400 V
C _{LA}	lamp capacitor	high voltage polypropylene film type capable of withstanding peak voltages	1.5 nF; 800 V	1.5 nF; 800 V
L _{LA}	lamp inductor	E20 core for lamp powers up to 23 W; Würth electronic type: 760370084 (see Section 6); J1 = open; J2 = open; J3 = short	3.1 mH	3.1 mH
C _{DV}	dV/dt limiting capacitor		220 pF; 500 V	220 pF; 500 V
C _{FS}	floating supply buffer capacitor	SMD: X7R type; leaded: PET type, high temperature	10 nF; 50 V	10 nF; 50 V
C _{VDD}	low voltage supply buffer capacitor	SMD: X7R type; leaded: PET type, high temperature	10 nF; 50 V	10 nF; 50 V
C _{osc}	oscillator capacitor	SMD: NP0 type; Leaded: C0G type, preferably high accuracy value type	180 pF; 50 V; 2 %	180 pF; 50 V; 2 %
R _{osc}	oscillator resistor	Preferably E24 series high accuracy value type	110 kΩ; 1/8 W; 1 %	110 kΩ; 1/8 W; 1 %
C _{SW}	sweep time capacitor	SMD: X7R type; leaded: PET type, high temperature	68 nF; 50 V	68 nF; 50 V

5.2 11 W lamp

Table 4. 11 W lamp (9.5 W; 150 mA burner; suited for cold ignition; $f_O = 42.5 \text{ kHz}$)

Reference	Description	Remarks	115 V; 60 Hz	230 V; 50 Hz
R _{FUS}	fusible inrush current limiter resistor	special type, fusible, high peak power	8.2 Ω	33 Ω
D1, D2	voltage doubler diodes		1N4007	-
D1, D4	bridge rectifier diodes		-	1N4007
C _{BUF1} , C _{BUF2}	buffer capacitors	high temperature electrolytic type	15 μF; 200 V	-
C _{BUF1}	buffer capacitor	high temperature electrolytic type	-	4.7 μF; 400 V
L _{FILT}	filter inductor	axial type	2.7 mH; 200 mA	2.7 mH; 200 mA
C _{HB1} , C _{HB1}	half-bridge capacitors		47 nF; 400 V	47 nF; 400 V

Table 4. 11 W lamp (9.5 W; 150 mA burner; suited for cold ignition; $f_O = 42.5$ kHz) ...continued

Reference	Description	Remarks	115 V; 60 Hz	230 V; 50 Hz
C _{LA}	lamp capacitor	high voltage polypropylene film type capable of withstanding peak voltages	1.5 nF; 800 V	1.5 nF; 800 V
L _{LA}	lamp inductor	E20 core for lamp powers up to 23 W; Würth electronic type: 760370084 (see Section 6); J1 = open; J2 = open; J3 = short	3.1 mH	3.1 mH
C _{DV}	dV/dt limiting capacitor		220 pF; 500 V	220 pF; 500 V
C _{FS}	floating supply buffer capacitor	SMD: X7R type; leaded: PET type, high temperature	10 nF; 50 V	10 nF; 50 V
C _{VDD}	low voltage supply buffer capacitor	SMD: X7R type; leaded: PET type, high temperature	10 nF; 50 V	10 nF; 50 V
C _{osc}	oscillator capacitor	SMD: NP0 type; leaded: C0G type, preferably high accuracy value type	180 pF; 50 V; 2 %	180 pF; 50 V; 2 %
R _{osc}	oscillator resistor	preferably E24 series high accuracy value type	120 kΩ; 1/8 W; 1 %	120 kΩ; 1/8 W; 1 %
C _{sw}	sweep time capacitor	SMD: X7R type; leaded: PET type, high temperature	68 nF; 50 V	68 nF; 50 V

6. Inductor specification

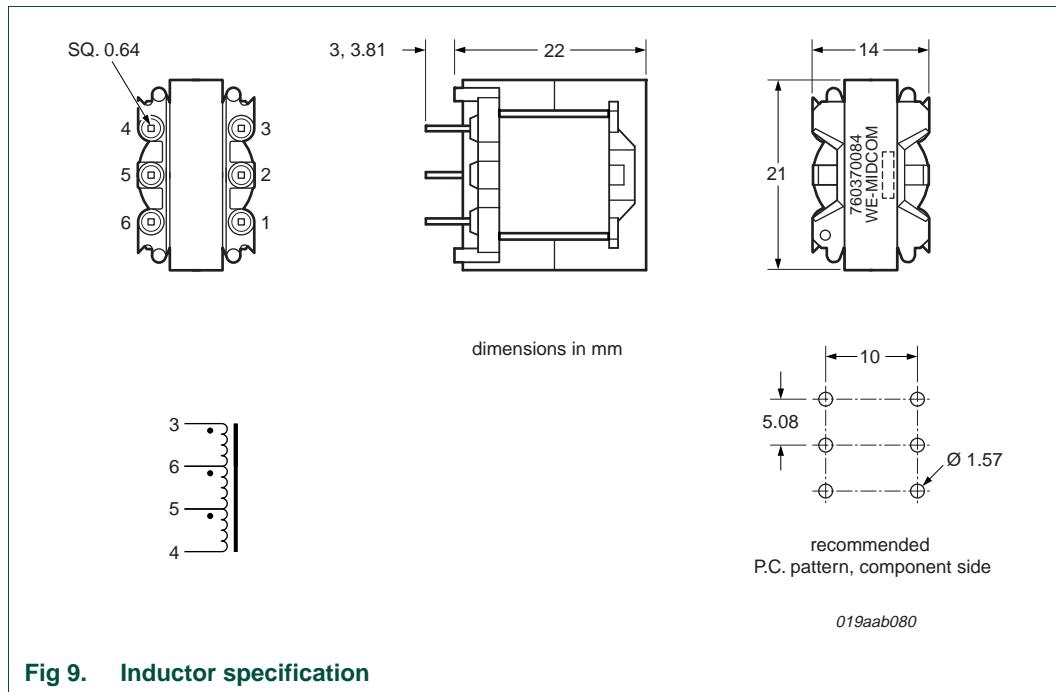


Fig 9. Inductor specification

Remark: The following electrical specifications are at 25 °C unless otherwise specified.

6.1 D.C. RESISTANCE (at 20 °C)

- 3 to 6: $4.75 \Omega \pm 20\%$
- 6 to 5: $0.630 \Omega \pm 20\%$
- 5 to 4: $0.465 \Omega \pm 20\%$

6.2 INDUCTANCE

- $2.20 \text{ mH} \pm 10\%$, 10 kHz, 100 m V (AC), 0 mA DC, 3 to 6, L_s
- $2.70 \text{ mH} \pm 15\%$, 10 kHz, 100 m V (AC), 0 mA DC, 3 to 5, L_s
- $3.10 \text{ mH} \pm 15\%$, 10 kHz, 100 m V (AC), 0 mA DC, 3 to 4, L_s

6.3 OPERATING TEMPERATURE RANGE

- -40°C to $+125^\circ\text{C}$ including temp rise

7. Legal information

7.1 Definitions

Draft — The document is a draft version only. The content is still under internal review and subject to formal approval, which may result in modifications or additions. NXP Semiconductors does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

7.2 Disclaimers

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information.

In no event shall NXP Semiconductors be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, NXP Semiconductors' aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the *Terms and conditions of commercial sale* of NXP Semiconductors.

Right to make changes — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

Suitability for use — NXP Semiconductors products are not designed, authorized or warranted to be suitable for use in life support, life-critical or safety-critical systems or equipment, nor in applications where failure or malfunction of an NXP Semiconductors product can reasonably be expected to result in personal injury, death or severe property or environmental damage. NXP Semiconductors accepts no liability for inclusion and/or use of NXP Semiconductors products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

Applications — Applications that are described herein for any of these products are for illustrative purposes only. NXP Semiconductors makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Customers are responsible for the design and operation of their applications and products using NXP Semiconductors products, and NXP Semiconductors accepts no liability for any assistance with applications or customer product design. It is customer's sole responsibility to determine whether the NXP Semiconductors product is suitable and fit for the customer's applications and products planned, as well as for the planned application and use of customer's third party customer(s). Customers should provide appropriate design and operating safeguards to minimize the risks associated with their applications and products.

NXP Semiconductors does not accept any liability related to any default, damage, costs or problem which is based on any weakness or default in the customer's applications or products, or the application or use by customer's third party customer(s). Customer is responsible for doing all necessary testing for the customer's applications and products using NXP Semiconductors products in order to avoid a default of the applications and the products or of the application or use by customer's third party customer(s). NXP does not accept any liability in this respect.

Safety of high-voltage evaluation products — The non-insulated high voltages that are present when operating this product, constitute a risk of electric shock, personal injury, death and/or ignition of fire. This product is intended for evaluation purposes only. It shall be operated in a designated test area by personnel that is qualified according to local requirements and labor laws to work with non-insulated mains voltages and high-voltage circuits.

The product does not comply with IEC 60950 based national or regional safety standards. NXP Semiconductors does not accept any liability for damages incurred due to inappropriate use of this product or related to non-insulated high voltages. Any use of this product is at customer's own risk and liability. The customer shall fully indemnify and hold harmless NXP Semiconductors from any liability, damages and claims resulting from the use of the product.

Export control — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from national authorities.

Evaluation products — This product is provided on an "as is" and "with all faults" basis for evaluation purposes only. NXP Semiconductors, its affiliates and their suppliers expressly disclaim all warranties, whether express, implied or statutory, including but not limited to the implied warranties of non-infringement, merchantability and fitness for a particular purpose. The entire risk as to the quality, or arising out of the use or performance, of this product remains with customer.

In no event shall NXP Semiconductors, its affiliates or their suppliers be liable to customer for any special, indirect, consequential, punitive or incidental damages (including without limitation damages for loss of business, business interruption, loss of use, loss of data or information, and the like) arising out of the use of or inability to use the product, whether or not based on tort (including negligence), strict liability, breach of contract, breach of warranty or any other theory, even if advised of the possibility of such damages.

Notwithstanding any damages that customer might incur for any reason whatsoever (including without limitation, all damages referenced above and all direct or general damages), the entire liability of NXP Semiconductors, its affiliates and their suppliers and customer's exclusive remedy for all of the foregoing shall be limited to actual damages incurred by customer based on reasonable reliance up to the greater of the amount actually paid by customer for the product or five dollars (US\$5.00). The foregoing limitations, exclusions and disclaimers shall apply to the maximum extent permitted by applicable law, even if any remedy fails of its essential purpose.

7.3 Trademarks

Notice: All referenced brands, product names, service names and trademarks are the property of their respective owners.

8. Contents

1	Introduction	3
1.1	Safety warning	3
1.2	General description	3
2	Schematic diagram	5
3	Specification	6
3.1	Board connections	7
3.2	Lamp inductor selection	8
3.3	Bill of material 13 W lamp	9
4	Thermal considerations SO package	10
5	Examples of different lamp powers	11
5.1	8 W lamp	11
5.2	11 W lamp	11
6	Inductor specification	13
6.1	D.C. RESISTANCE (at 20 °C)	13
6.2	INDUCTANCE	13
6.3	OPERATING TEMPERATURE RANGE	13
7	Legal information	14
7.1	Definitions	14
7.2	Disclaimers	14
7.3	Trademarks	14
8	Contents	15

Please be aware that important notices concerning this document and the product(s) described herein, have been included in section 'Legal information'.

© NXP B.V. 2011.

All rights reserved.

For more information, please visit: <http://www.nxp.com>

For sales office addresses, please send an email to: salesaddresses@nxp.com

Date of release: 25 January 2011

Document identifier: UM10392