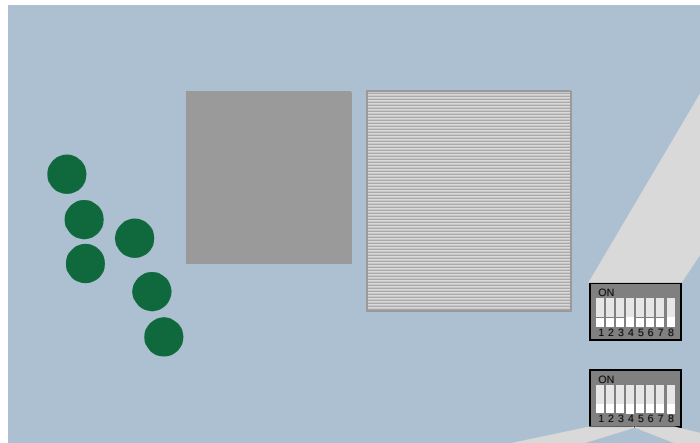


Gyrus X2 (MPMC744x) Configuration Guide

BOARD OPTIONS

NAME	SET	PCI
ROMLOC	↑=0	Boot from motherboard flash
	↓=1	Boot from local flash
MAPSEL	↑=0	Map "A" / PReP
	↓=1	Map "B" / CHRP
PMCTYPE	↑=0	Mot. MPMC (MPC107 arbiter)
	↓=1	VITA PrPMC (SPX3 arbiter)
AGENT	↑=0	Free agent
	↓=1	Wait for host
PROGSEL	↑=0	Local flash is programmable
	↓=1	Local flash is bootable
ROMSEL	↑=0	Swap flash devices
	↓=1	Standard flash selection
M66EN	↑=0	33 MHz PCI only
	↓=1	66 MHz PCI allowed
SYSRST	↑=0	COP may reset system
	↓=1	COP resets MPMC only



MPC744x PLL

0	1	2	3	Mult.	66	83	100	133
↓	↑	↑	↑	3X	200	250	300	400
↓	↓	↓	↑	3.5X	233	292	350	466
↓	↑	↓	↑	4X	266	333	400	532
↑	↓	↓	↓	4.5X	300	375	450	600
↓	↑	↓	↓	5X	333	416	500	666
↓	↑	↑	↓	5.5X	366	458	550	733
↓	↓	↑	↓	6X	400	500	600	800
↑	↓	↑	↓	6.5X	433	542	650	866
↑	↑	↓	↑	7X	466	584	700	933
↑	↑	↑	↓	7.5X	500	625	750	1000
↓	↓	↑	↑	8X	533	666	800	1066
↑	↓	↓	↓	9X	600	750	900	1200
↓	↑	↓	↑	10X	666	833	1000	1333
↓	↑	↑	↓	11X	733	913	1100	1466
↓	↑	↓	↓	12X	800	1000	1200	1600
↓	↑	↓	↑	13X	1080	1083	1300	1733
↓	↓	↑	↑	14X	1166	1166	1400	1866

These settings require removal of R13 on board.

MPC107 PLL

0	1	2	3	PCI	BUS
↑	↑	↑	↑	33	33
↑	↑	↓	↑	20	40
↑	↓	↑	↓	33	66
↓	↑	↑	↑	33	100
↓	↑	↑	↓	66	133
↓	↑	↓	↑	33	133
↓	↓	↑	↑	33	83
↓	↓	↑	↓	66	66
↑	↑	↓	↓	bypass	

↑ = 0, ↓ = 1

NOTE: Frequencies in these tables are for informational purposes only; there is no implication that components are offered which support any of the above settings.