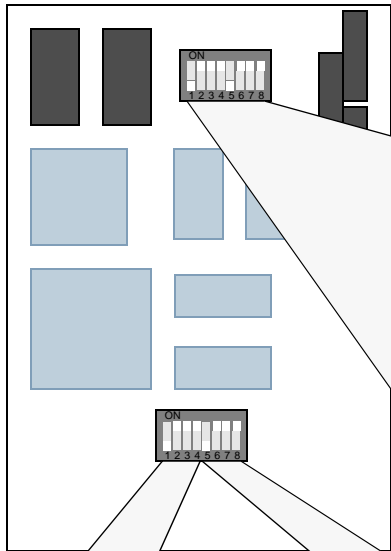


Altimus X3B (MPMC750/755/7400/7410) Configuration Guide



BOARD OPTIONS

NAME	SET	PCI
SYSRST	↑=0	COP resets MPPMC & system
	↓=1	COP resets MPPMC only
M66EN	↑=0	33 MHz PCI only
	↓=1	66 MHz PCI allowed
ROMSEL	↑=0	Alternate Flash selected
	↓=1	Standard Flash selected
PROGSEL	↑=0	Local flash is programmable
	↓=1	Local flash is bootable
AGENT	↑=0	Free agent
	↓=1	Wait for host
PMCTYPE	↑=0	MOTSPS MPPMC (arbiter)
	↓=1	VITA PrPMC (no arbiter)
MAPSEL	↑=0	Map "A" / PReP
	↓=1	Map "B" / CHRP
ROMLOC	↑=0	RCS0 on PCI
	↓=1	RCS0 on local bus

PROCESSOR PLL

3	2	1	0	Mult.	33	66	83	100	133
↓	↓	↑	↑	byp.	33	66	83	100	133
↑	↑	↑	↓	3X	100	200	250	300	400
↑	↓	↓	↓	3.5X	116	233	292	350	466
↑	↓	↑	↓	4X	133	266	333	400	533
↓	↓	↓	↑	4.5X	150	300	372	450	600
↓	↓	↑	↓	5X	166	330	415	500	666
↓	↑	↑	↓	5.5X	183	363	456	550	733
↓	↑	↓	↓	6X	200	400	500	600	800
↑	↓	↑	↓	6.5X	216	433	540	650	866
↑	↓	↑	↑	7X	233	466	580	700	933
↓	↑	↑	↑	7.5X	250	500	622	750	1000
↑	↑	↓	↓	8X	266	528	664	800	1066
↑	↑	↑	↑	9X	300	600	750	900	1200

MPC107 PLL

3	2	1	0	Mult	PCI	BUS
↓	↑	↑	↑	1X	33	33
↓	↑	↑	↓	1.5X	33	50
↓	↑	↓	↑	2X	33	66
↑	↑	↓	↓	2.5X	33	83
↑	↑	↑	↓	3X	33	100
↑	↓	↑	↓	4X	33	133
↓	↑	↓	↓	1X	66	66
↓	↑	↑	↓	1.5X	66	100
↓	↑	↓	↑	2X	66	133
↓	↓	↑	↑	1X	bypass	

NOTE: PLL tables are for reference only; components are not guaranteed operational over the ranges shown on this page..