

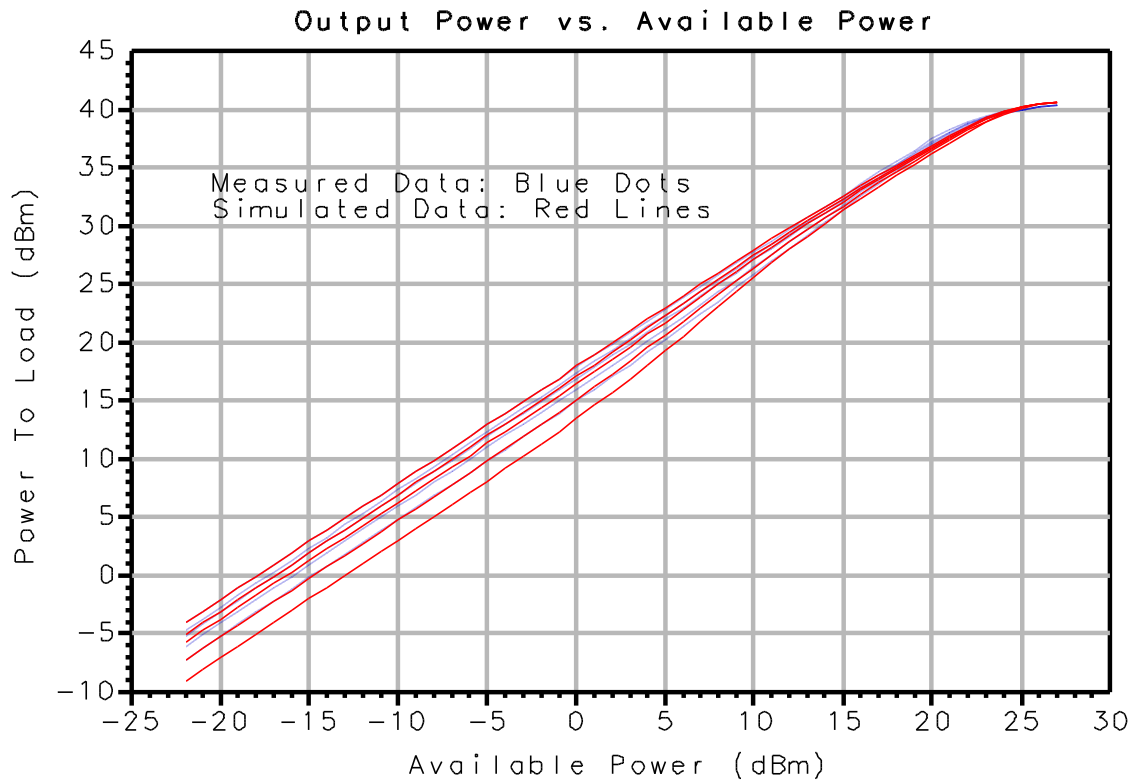
MET LDMOS Model vs. Measured Data

Under single and two tone excitation for 5 different bias conditions of a 19.8 mm device used in the MRF18085A, MRF18085AS, MRF18085B, MRF18085BS, MRF19085, MRF19085S, MRF19045, MRF19045S, MRF21045, MRF21045S, MRF21125, and MRF21125S products.

Devices were tuned for best compromise between output power, efficiency and linearity.

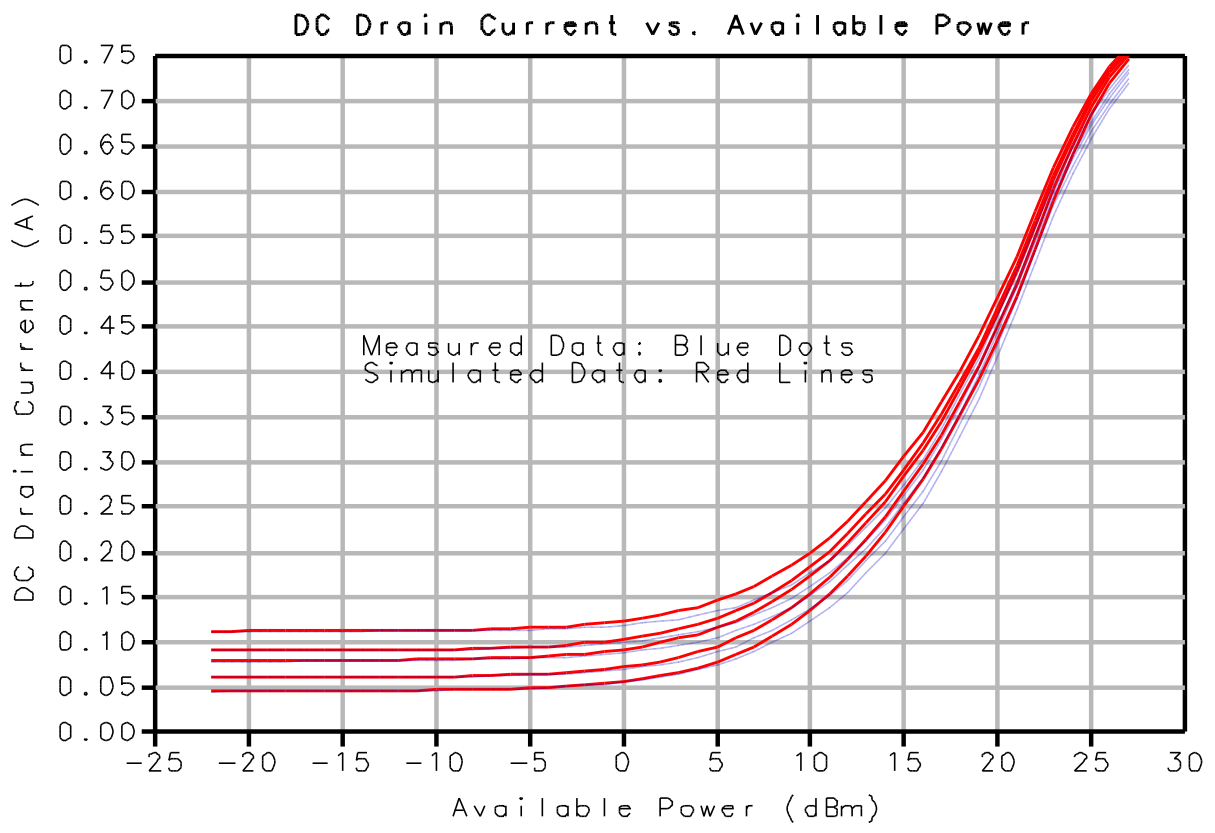


Single Tone (1.96 GHz) Simulated LDMOS MET Model versus Measured Data for 5 Different Bias Conditions of a 19.8 mm Device Used in the MRF18085A, MRF18085AS, MRF18085B, MRF18085BS, MRF19085, MRF19085S, MRF19045, MRF19045S, MRF21045, MRF21045S, MRF21125 and MRF21125S. Tuned for Best Compromise Between Output Power and Efficiency



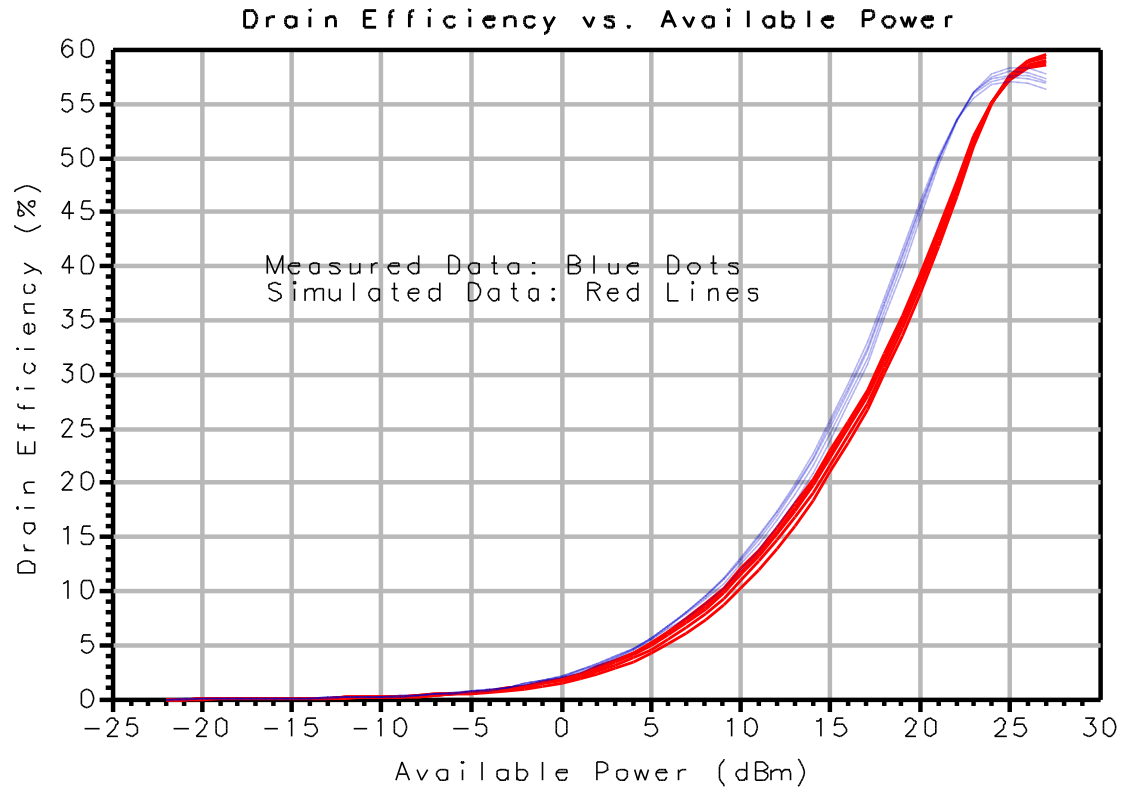


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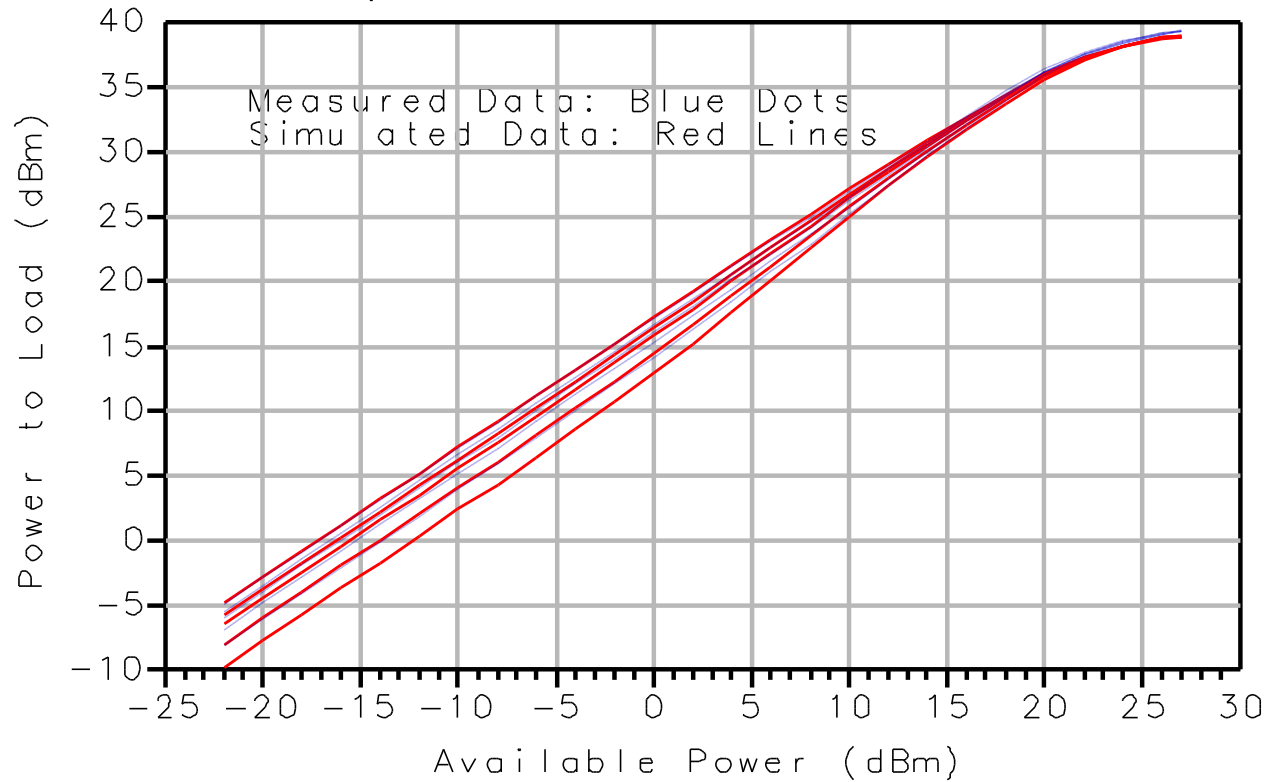


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Two Tone (1.96 and 1.97 GHz) Simulated LDMOS MET Model versus Measured Data for 5 Different Bias Conditions of a 19.8 mm Device Used in the MRF18085A, MRF18085AS, MRF18085B, MRF18085BS, MRF19085, MRF19085S, MRF19045, MRF19045S, MRF21045, MRF21045S, MRF21125 and MRF21125S. Tuned for Best Compromise Between Output Power, Efficiency and Linearity

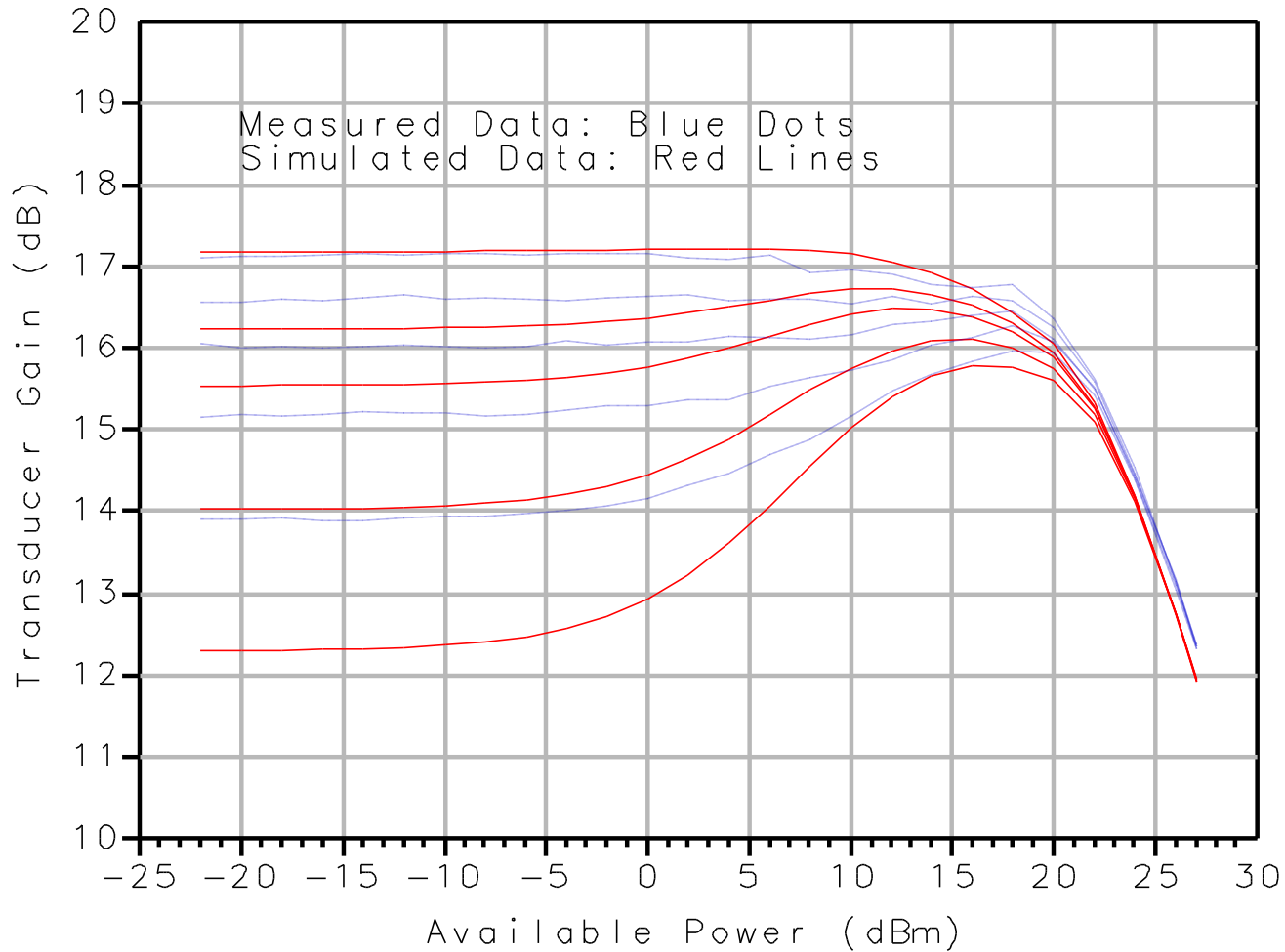
Output Power vs. Available Power





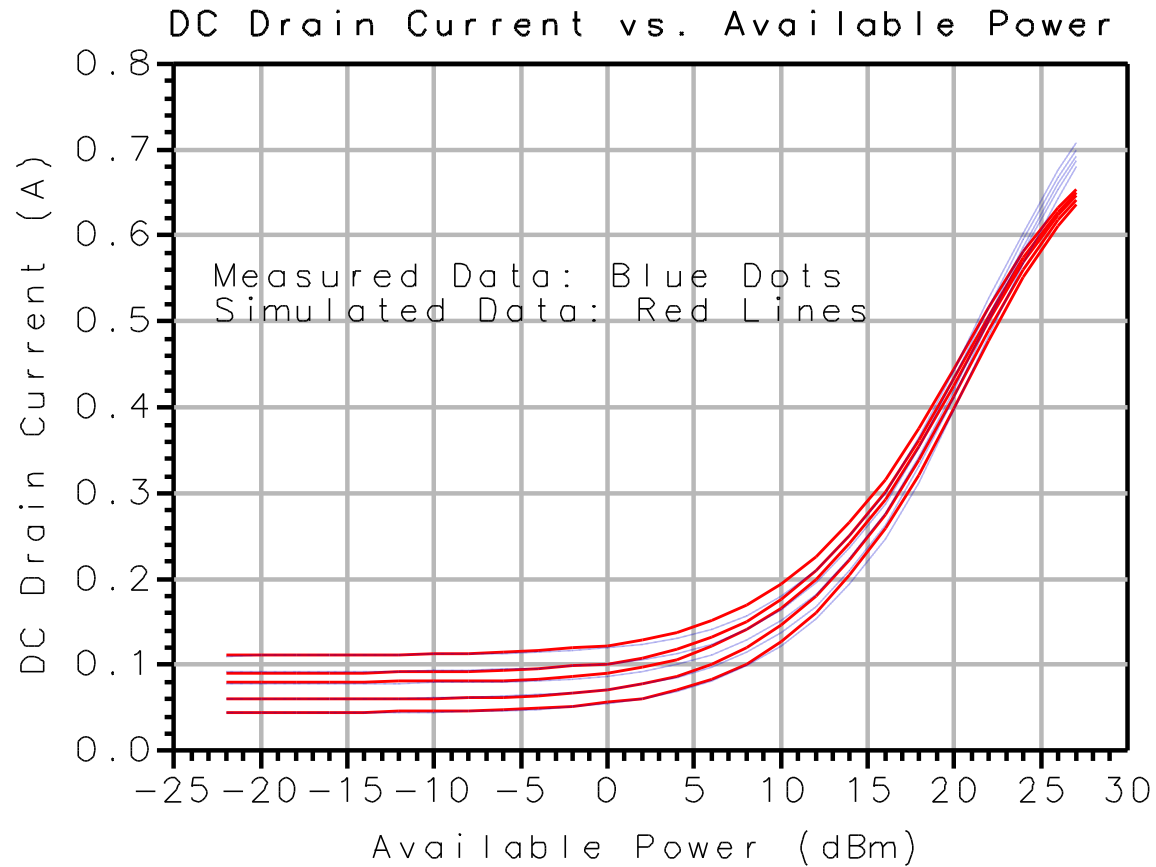
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Transducer Gain vs. Available Power



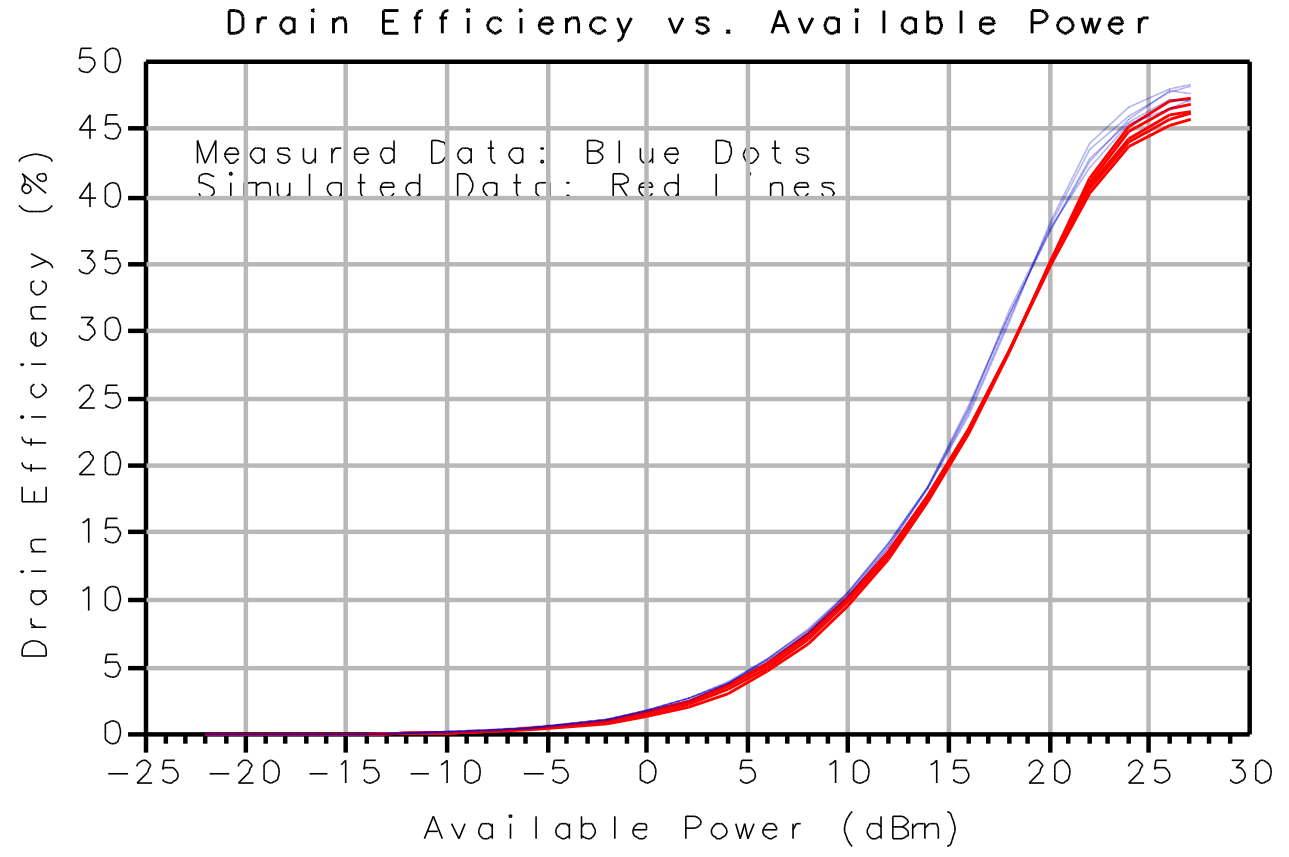


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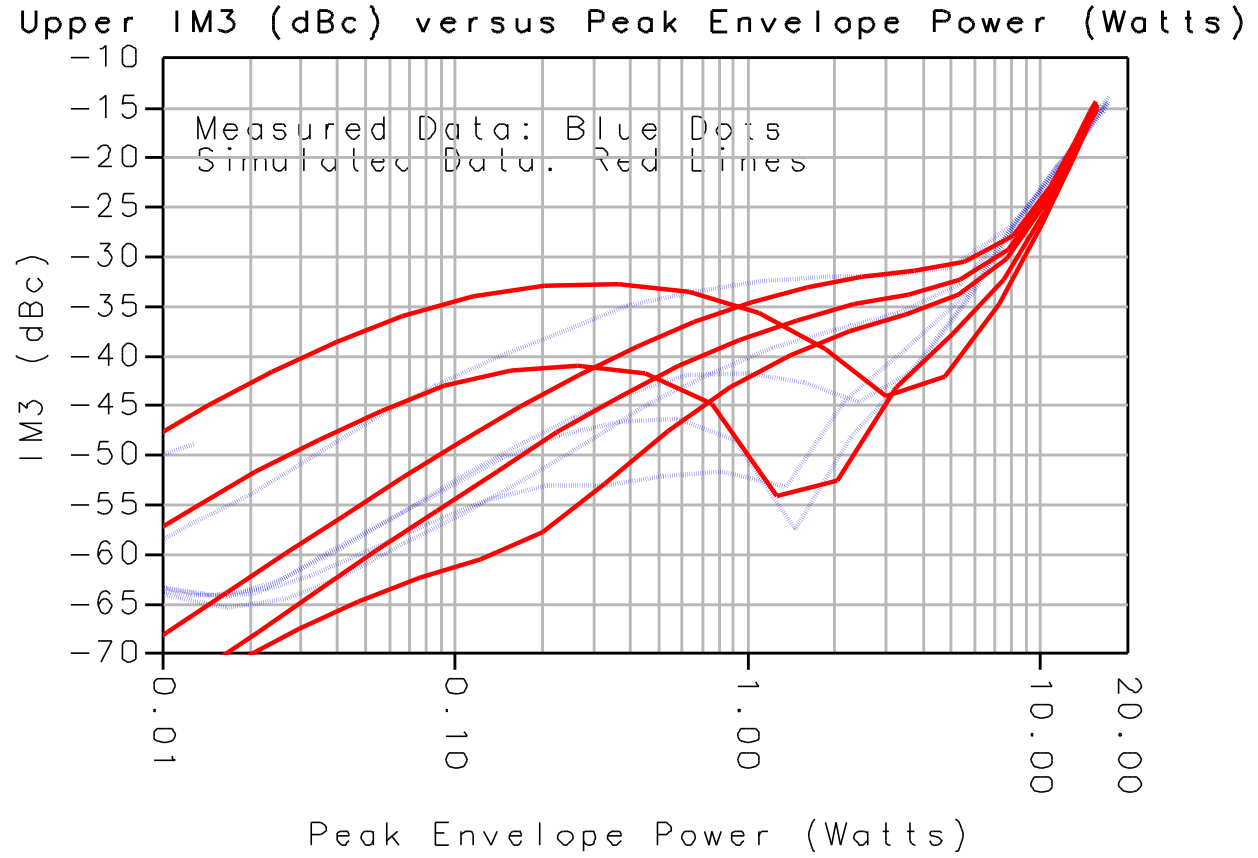


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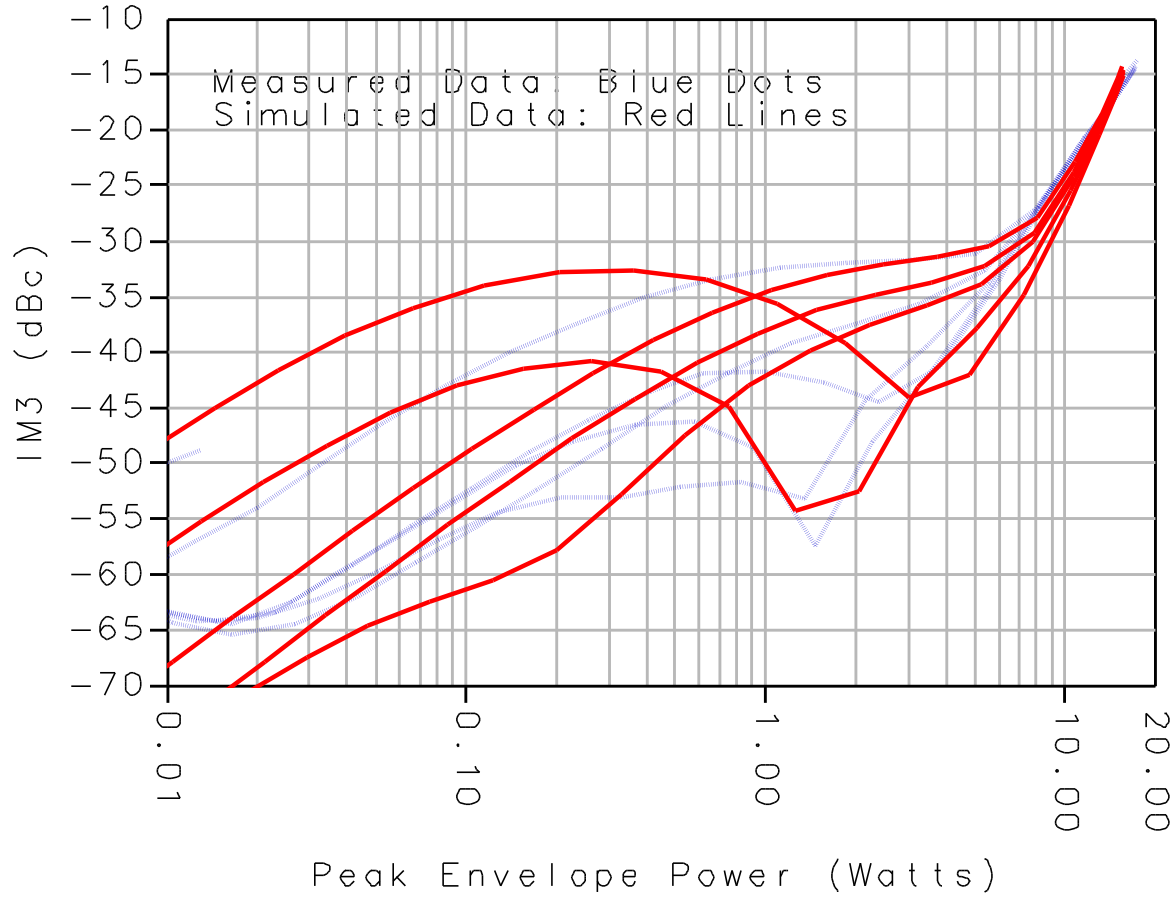
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Lower IM3 (dBc) versus Peak Envelope Power (Watts)



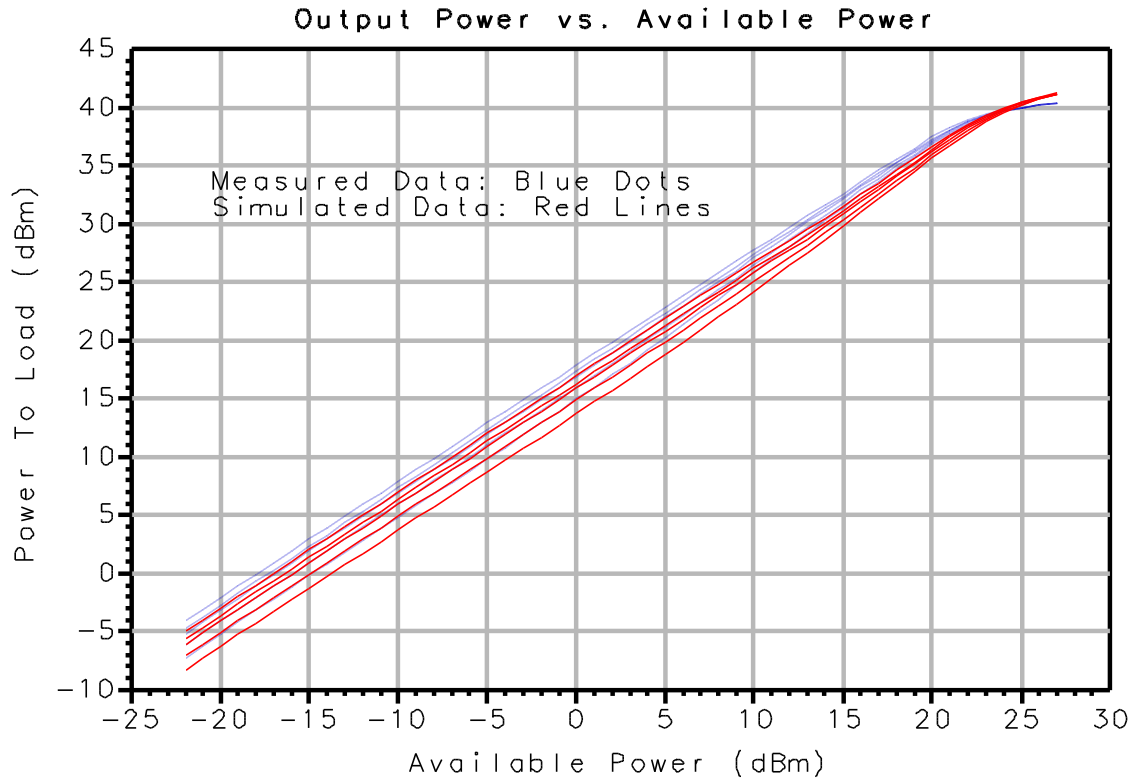
Root LDMOS Model vs. Measured Data

Under single and two tone excitation for 5 different bias conditions of a 19.8 mm device used in the MRF18085A, MRF18085AS, MRF18085B, MRF18085BS, MRF19085, MRF19085S, MRF19045, MRF19045S, MRF21045, MRF21045S, MRF21125, and MRF21125S products.

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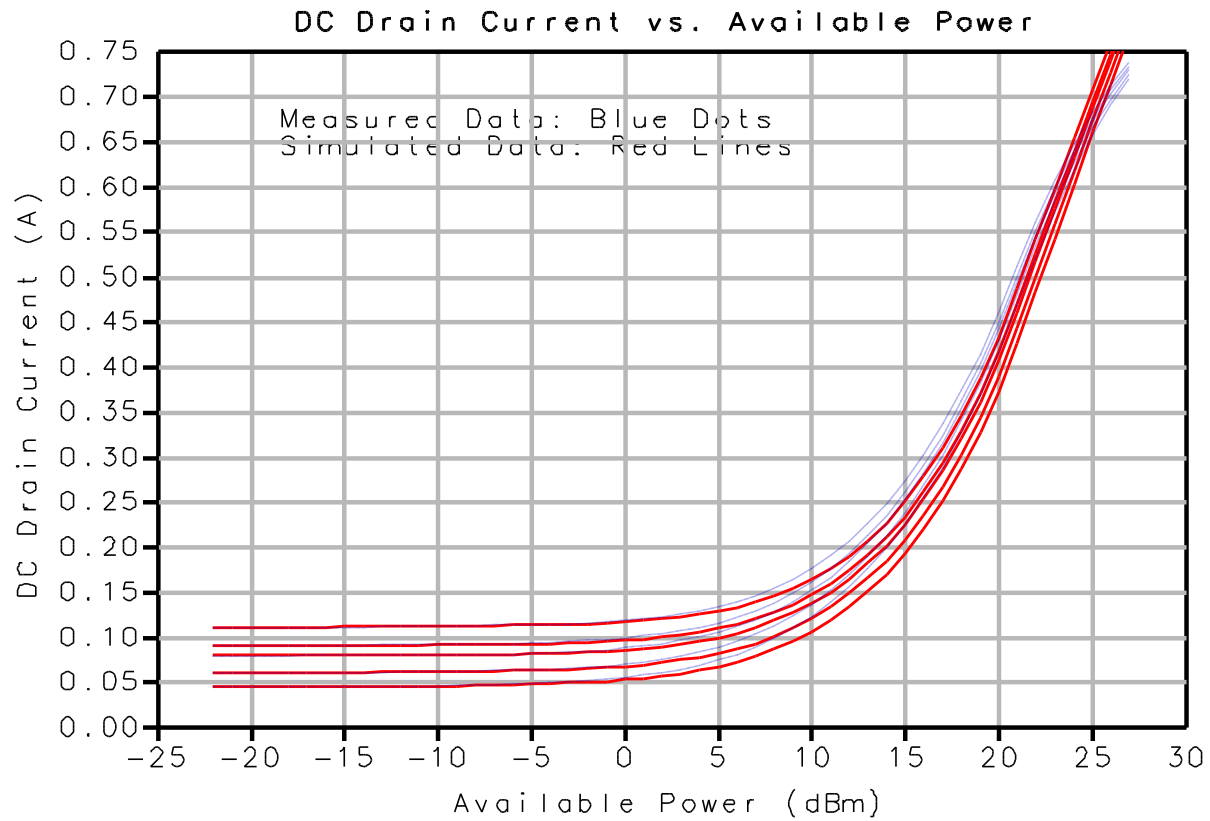


Single Tone (1.96 GHz) Simulated LDMOS Root Model versus Measured Data for 5 Different Bias Conditions of a 19.8 mm Device Used in the MRF18085A, MRF18085AS, MRF18085B, MRF18085BS, MRF19085, MRF19085S, MRF19045, MRF19045S, MRF21045, MRF21045S, MRF21125 and MRF21125S. Tuned for Best Compromise Between Output Power and Efficiency



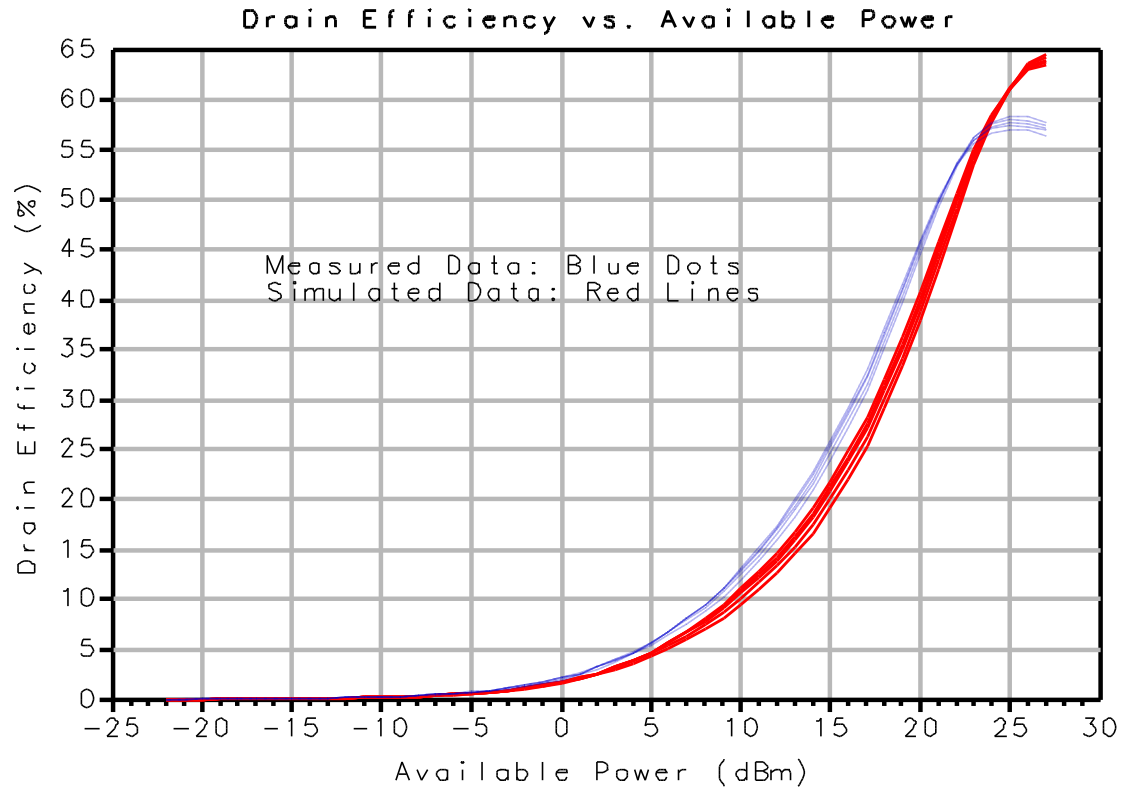


Single Tone (1.96 GHz) Simulated LDMOS Root Model versus Measured Data for 5 Different Bias Conditions of a 19.8 mm Device Used in the MRF18085A, MRF18085AS, MRF18085B, MRF18085BS, MRF19085, MRF19085S, MRF19045, MRF19045S, MRF21045, MRF21045S, MRF21125 and MRF21125S. Tuned for Best Compromise Between Output Power and Efficiency





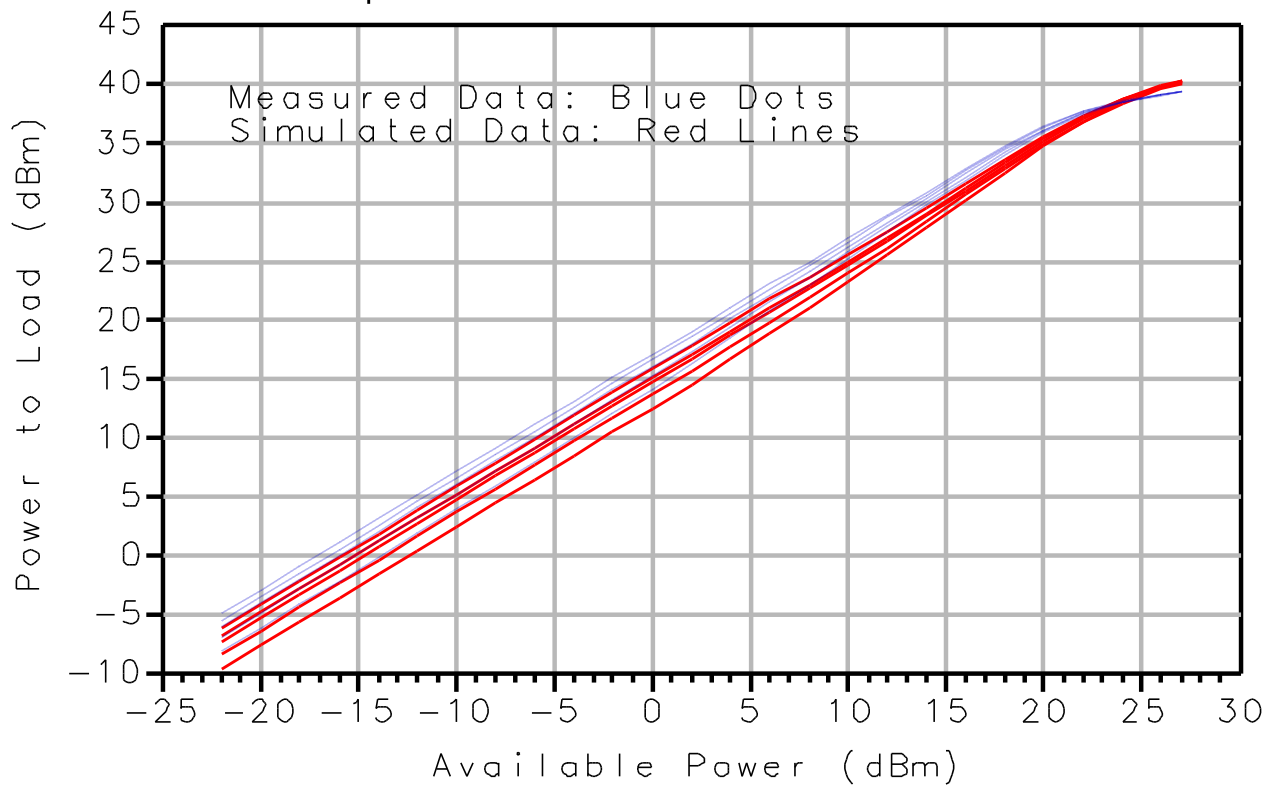
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Two Tone (1.96 and 1.97 GHz) Simulated LDMOS Root Model versus Measured Data for 5 Different Bias Conditions of a 19.8 mm Device Used in the MRF18085A, MRF18085AS, MRF18085B, MRF18085BS, MRF19085, MRF19085S, MRF19045, MRF19045S, MRF21045, MRF21045S, MRF21125 and MRF21125S. Tuned for Best Compromise Between Output Power, Efficiency and Linearity

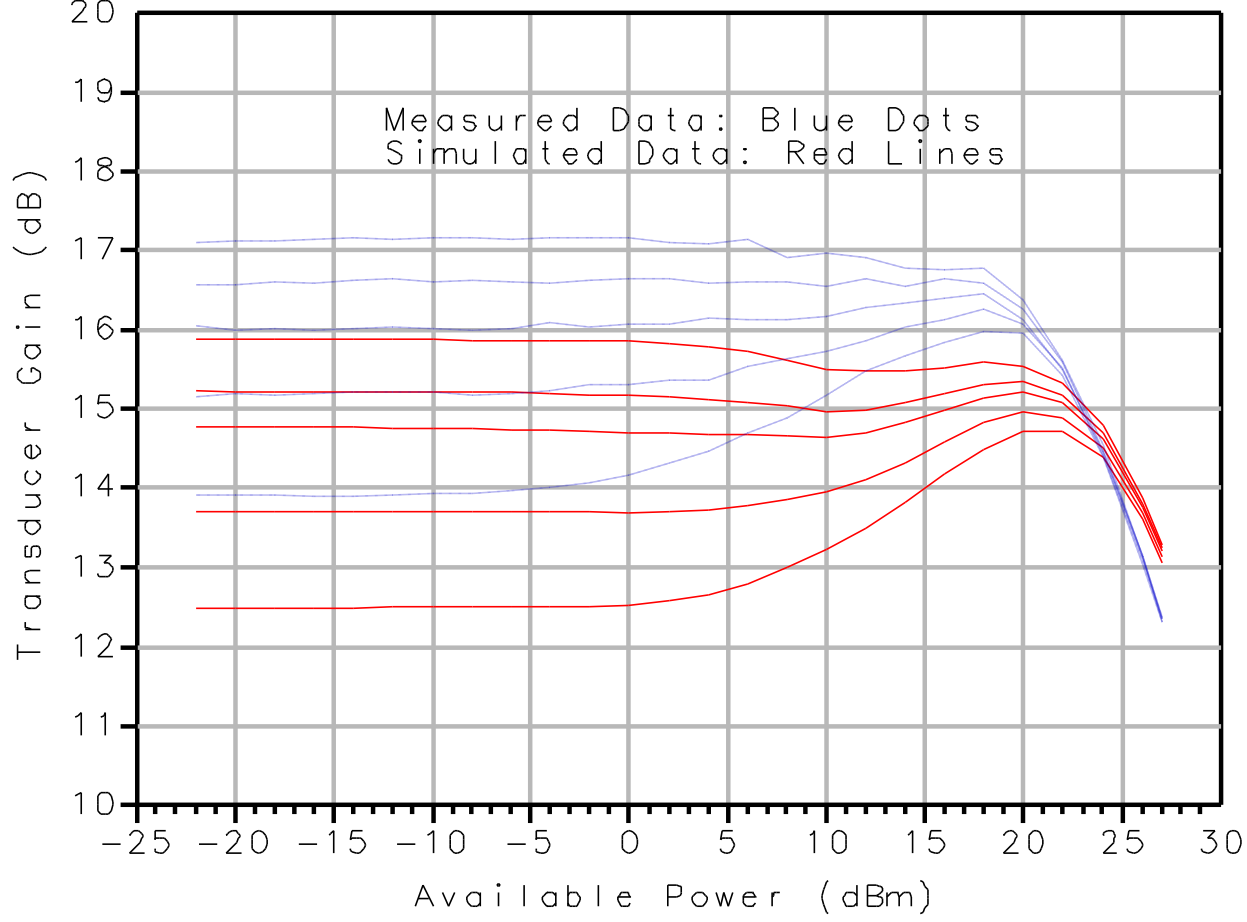
Output Power vs. Available Power





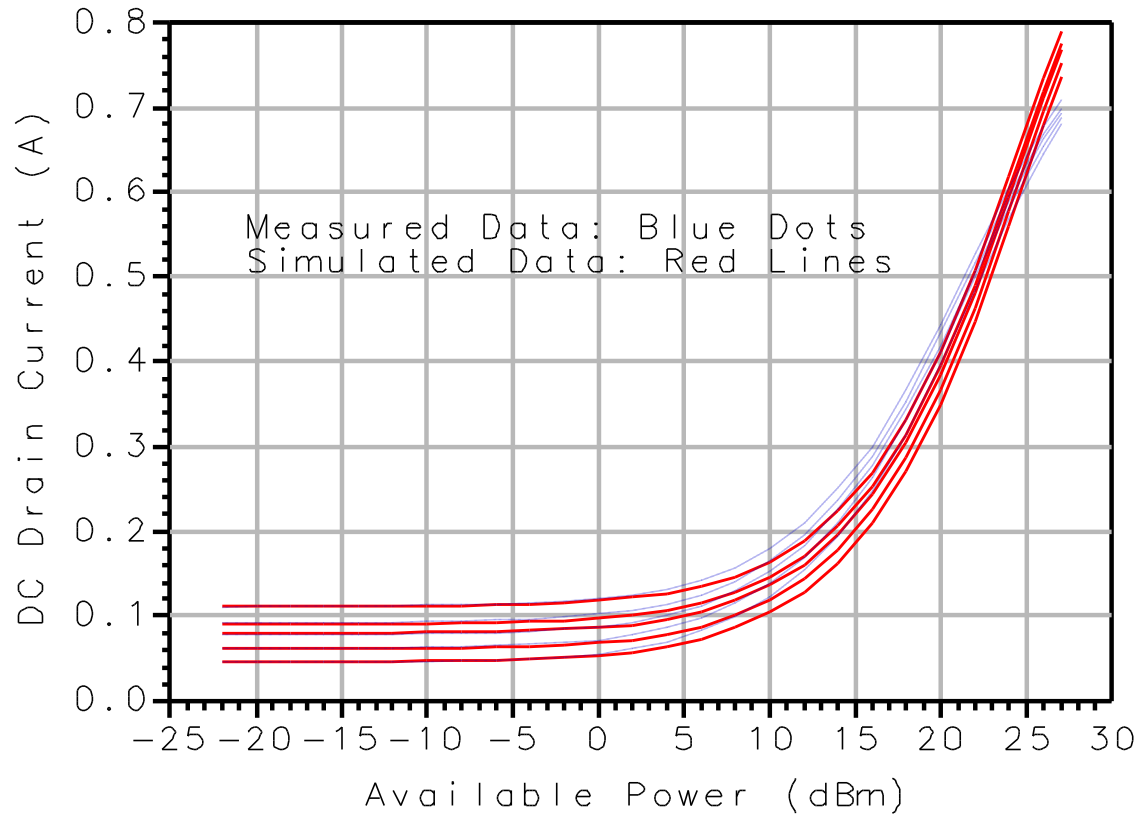
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Transducer Gain vs. Available Power



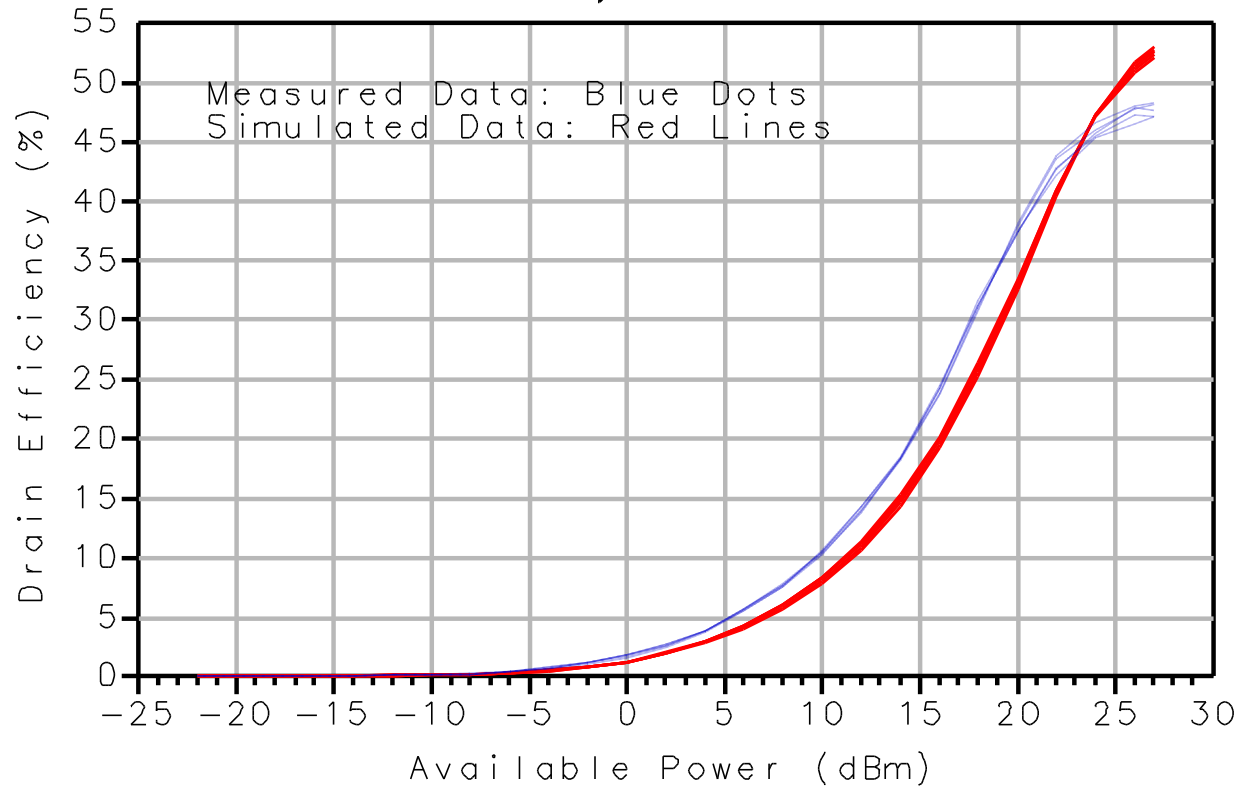
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DC Drain Current vs. Available Power



Two Tone (1.96 and 1.97 GHz) Simulated LDMOS Root Model versus Measured Data for 5 Different Bias Conditions of a 19.8 mm Device Used in the MRF18085A, MRF18085AS, MRF18085B, MRF18085BS, MRF19085, MRF19085S, MRF19045, MRF19045S, MRF21045, MRF21045S, MRF21125 and MRF21125S. Tuned for Best Compromise Between Output Power, Efficiency and Linearity

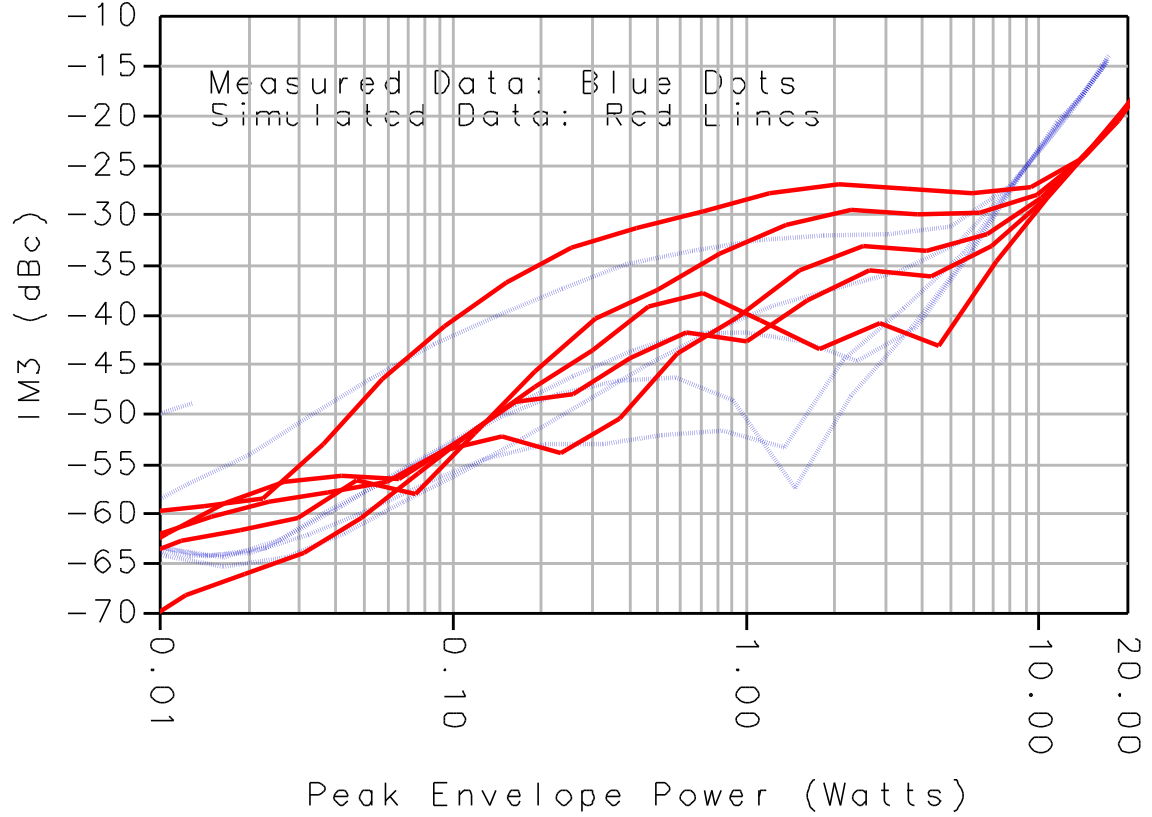
Drain Efficiency vs. Available Power





Two Tone (1.96 and 1.97 GHz) Simulated LDMOS Root Model versus Measured Data for 5 Different Bias Conditions of a 19.8 mm Device Used in the MRF18085A, MRF18085AS, MRF18085B, MRF18085BS, MRF19085, MRF19085S, MRF19045, MRF19045S, MRF21045, MRF21045S, MRF21125 and MRF21125S. Tuned for Best Compromise Between Output Power, Efficiency and Linearity

Upper IM3 (dBc) versus Peak Envelope Power (Watts)





Two Tone (1.96 and 1.97 GHz) Simulated LDMOS Root Model versus Measured Data for 5 Different Bias Conditions of a 19.8 mm Device Used in the MRF18085A, MRF18085AS, MRF18085B, MRF18085BS, MRF19085, MRF19085S, MRF19045, MRF19045S, MRF21045, MRF21045S, MRF21125 and MRF21125S. Tuned for Best Compromise Between Output Power, Efficiency and Linearity

Lower IM3 (dBc) versus Peak Envelope Power (Watts)

