

**Package information** 

# **Package summary**

**Terminal position code** Q (quad) HUQFN18 Package type descriptive code

Package style descriptive code HUQFN (thermal enhanced ultra thin quad

flatpack; no leads)

Mounting method type S (surface mount)

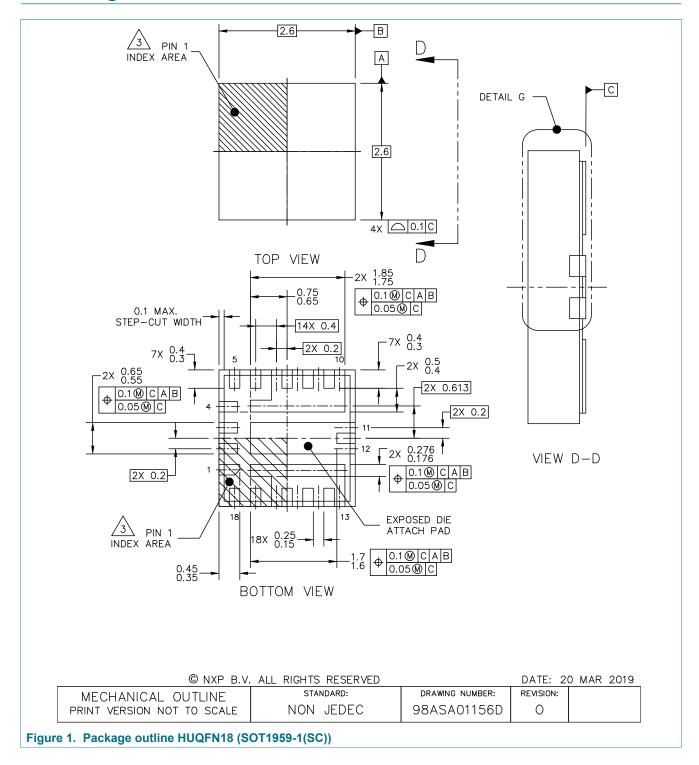
Issue date 20-03-2019 98ASA01156D Manufacturer package code

Table 1. Package summary

Parameter	Min	Nom	Max	Unit
package length	-	2.6	-	mm
package width	-	2.6	-	mm
seated height	-	0.55	-	mm
nominal pitch	-	0.4	-	mm
actual quantity of termination	-	18	-	



# 2 Package outline



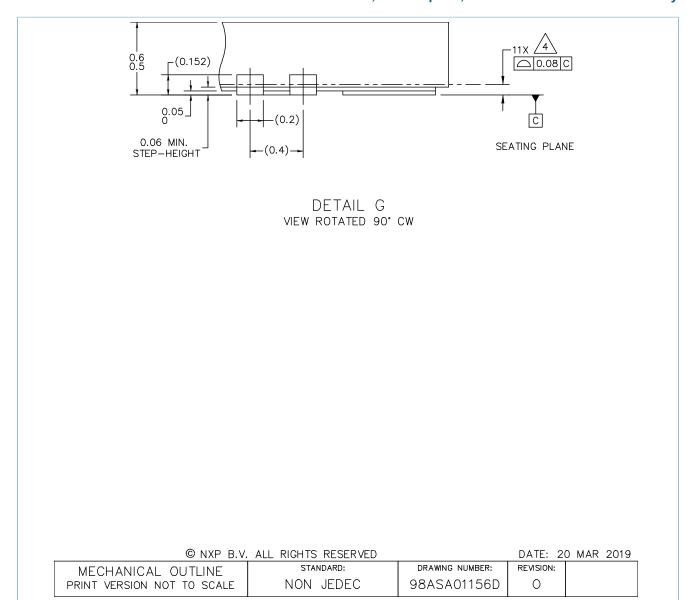
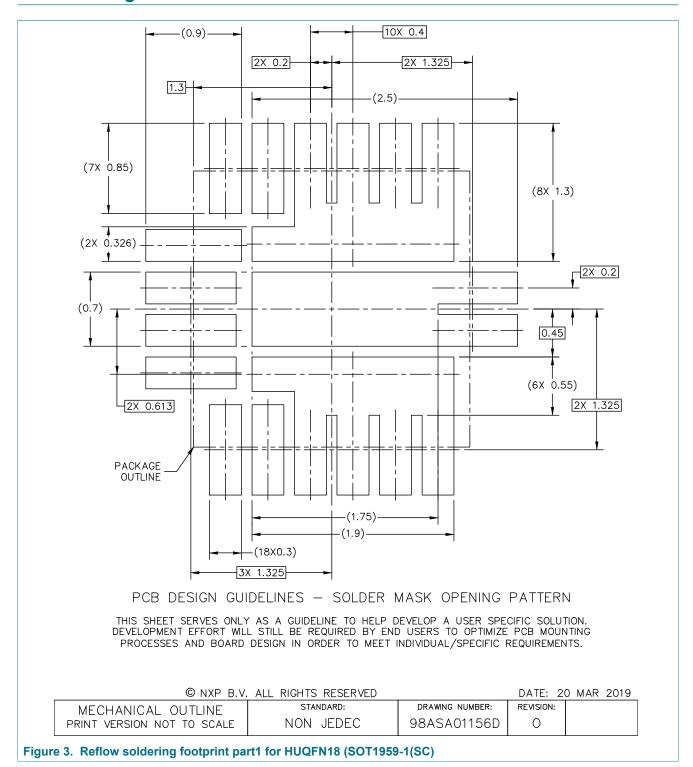
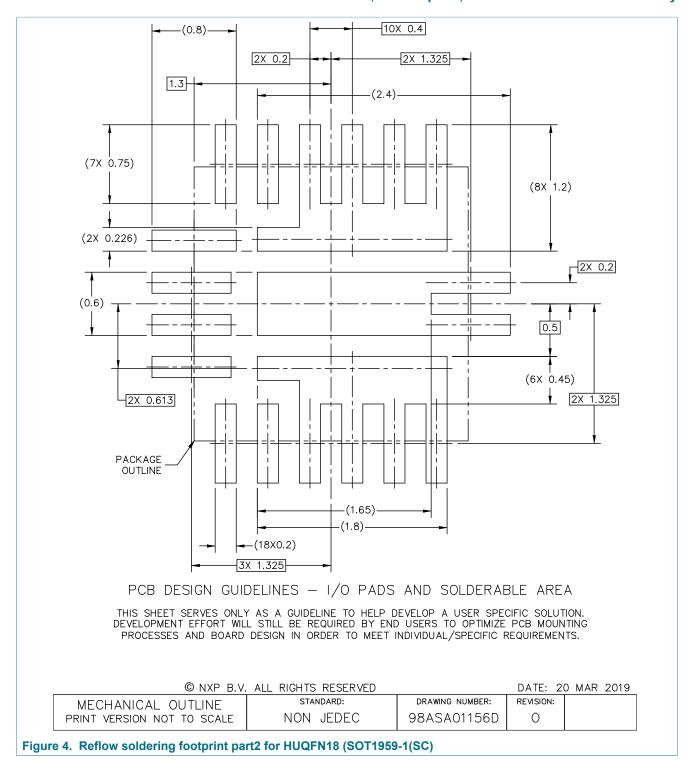


Figure 2. Package outline detail HUQFN18 (SOT1959-1(SC))

## 3 Soldering





SOT1959-1(SC)

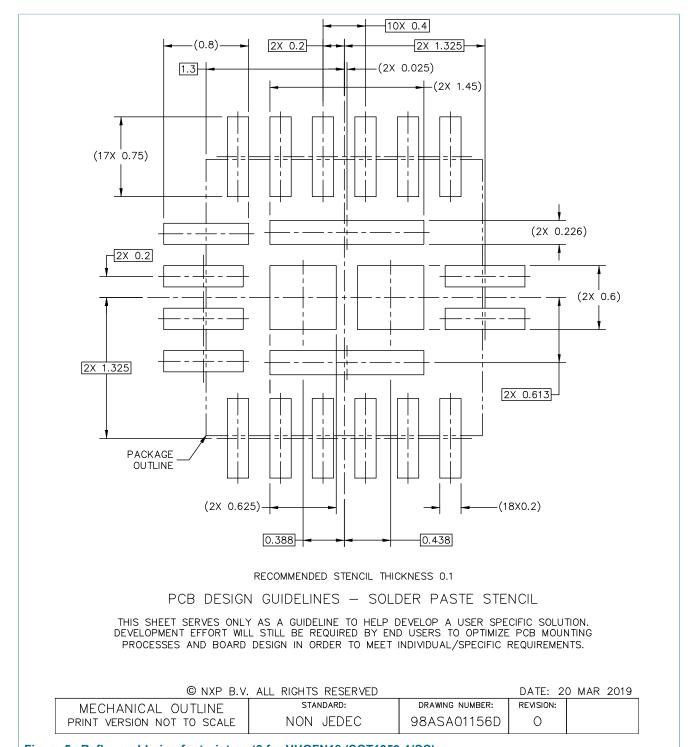


Figure 5. Reflow soldering footprint part3 for HUQFN18 (SOT1959-1(SC)

#### NOTES:

- 1. ALL DIMENSIONS ARE IN MILLIMETERS.
- 2. DIMENSIONING AND TOLERANCING PER ASME Y14.5M-1994.

3. PIN ONE CONFIGURATION MAY VARY.

 $\sqrt{4}$  COPLANARITY APPLIES TO ALL LEADS, FUSED LEADS AND EXPOSED PAD ON BOTTOM SIDE.

5. MIN. METAL GAP SHOULD BE 0.15 MM.

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DATE: 20 MAR 2019

MECHANICAL OUTLINE	STANDARD:	DRAWING NUMBER:	REVISION:	
PRINT VERSION NOT TO SCALE	NON JEDEC	98ASA01156D	0	

Figure 6. Package outline note HUQFN18 (SOT1959-1(SC))

# 4 Legal information

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### **NXP Semiconductors**

SOT1959-1(SC)

HUQFN18, thermal enhanced ultra thin quad flat package; no leads; step-cut wettable flank, 18 terminals, 0.4 mm pitch, 2.6 mm x 2.6 mm x 0.55 mm body

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