

WLCSP40, wafer level chip-scale package; 40 bumps; 0.4 mm pitch; 2.25 mm x 3.45 mm x 0.525 mm body (backside coating included)

19 March 2018

Package information

1. Package summary

Terminal position code B (bottom)

Package type descriptive code WLCSP40

Package type industry code WLCSP40

Package style descriptive code WLCSP (wafer level chip-size package)

Mounting method type S (surface mount)

Issue date 13-9-2017

Manufacturer package code SOT1942-1

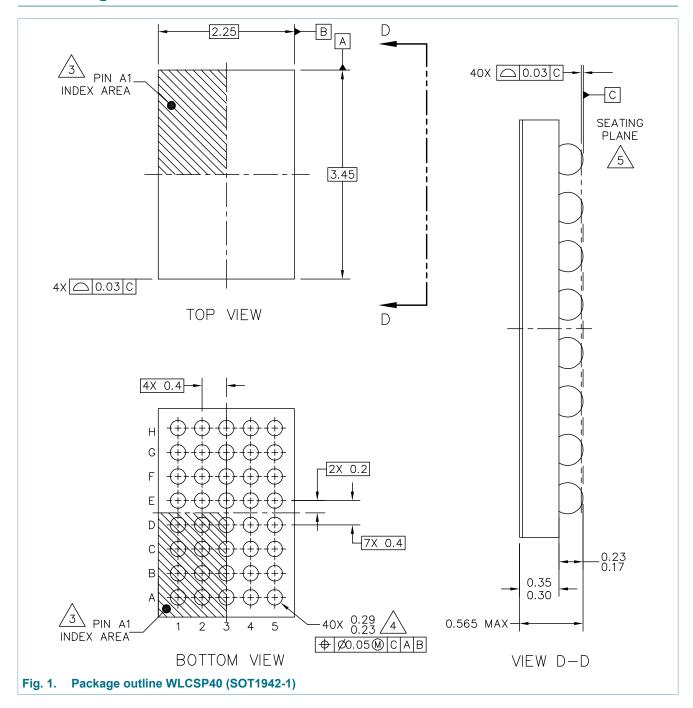
Table 1. Package summary

Symbol	Parameter	Min	Тур	Nom	Max	Unit
D	package length	2.22	-	2.25	2.28	mm
E	package width	3.42	-	3.45	3.48	mm
Α	seated height	0.485	-	0.525	0.565	mm
е	nominal pitch	_	-	0.4	-	mm
n ₂	actual quantity of termination	-	-	40	-	A/A



WLCSP40, wafer level chip-scale package; 40 bumps; 0.4 mm pitch; 2.25 mm x 3.45 mm x 0.525 mm body (backside coating included)

2. Package outline



WLCSP40, wafer level chip-scale package; 40 bumps; 0.4 mm pitch; 2.25 mm x 3.45 mm x 0.525 mm body (backside coating included)

NOTES:

- 1. ALL DIMENSIONS IN MILLIMETERS.
- 2. DIMENSIONING AND TOLERANCING PER ASME Y14.5M-1994.

3. PIN A1 FEATURE SHAPE, SIZE AND LOCATION MAY VARY.
4. MAXIMUM SOLDER BALL DIAMETER MEASURED PARALLEL TO DATUM C.

DATUM C, THE SEATING PLANE, IS DETERMINED BY THE SPHERICAL CROWNS OF THE SOLDER BALLS.

6. THIS PACKAGE HAS A BACK SIDE COATING THICKNESS OF 0.025.

Fig. 2. Package outline note WLCSP40 (SOT1942-1)

WLCSP40, wafer level chip-scale package; 40 bumps; 0.4 mm pitch; 2.25 mm x 3.45 mm x 0.525 mm body (backside coating included)

3. Soldering

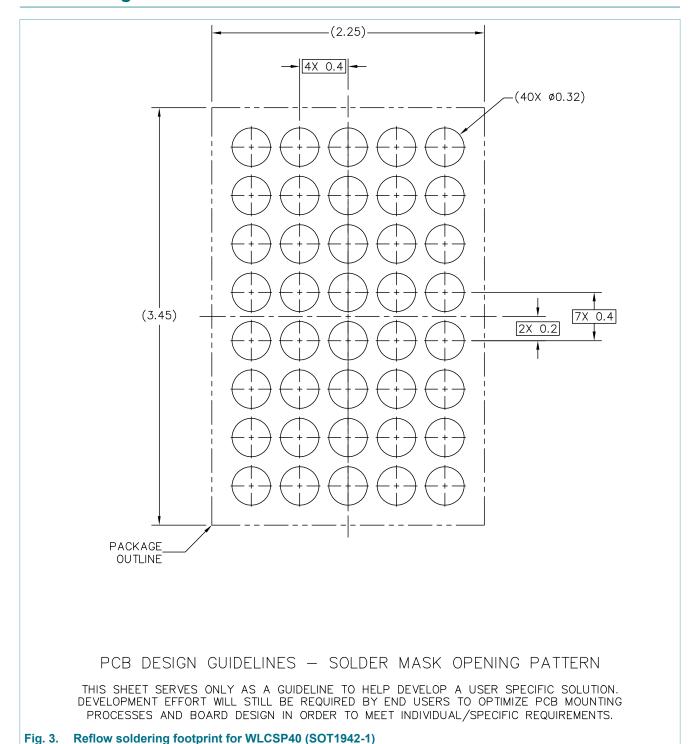


Fig. 3.

WLCSP40, wafer level chip-scale package; 40 bumps; 0.4 mm pitch; 2.25 mm x 3.45 mm x 0.525 mm body (backside coating included)

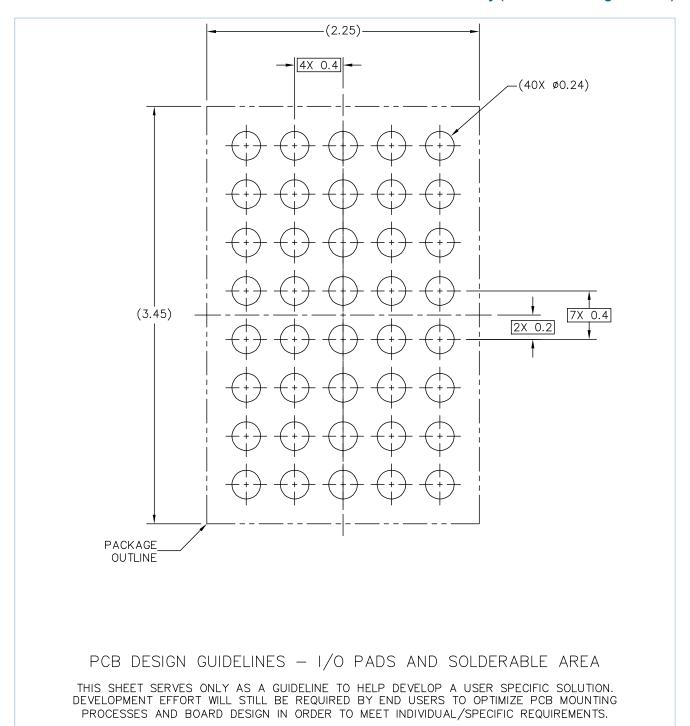


Fig. 4. Reflow soldering footprint part2 for WLCSP40 (SOT1942-1)

WLCSP40, wafer level chip-scale package; 40 bumps; 0.4 mm pitch; 2.25 mm x 3.45 mm x 0.525 mm body (backside coating included)

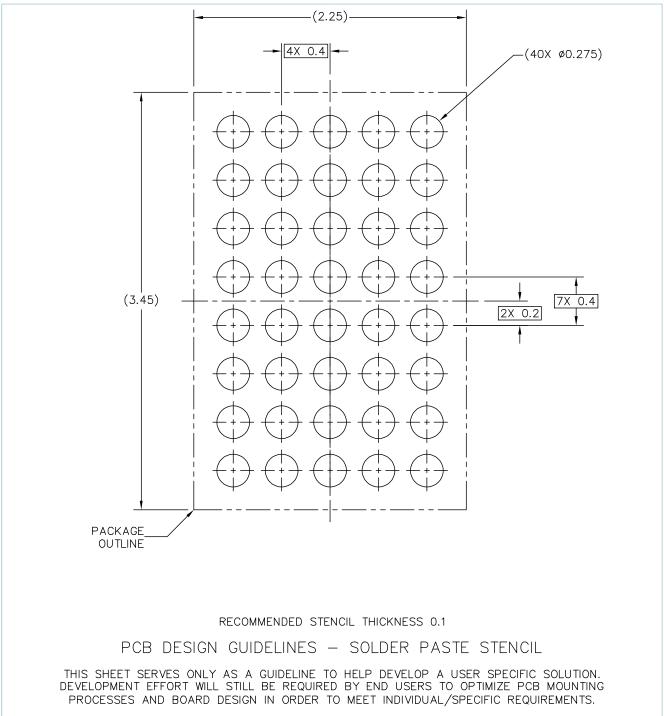


Fig. 5. Reflow soldering footprint part3 for WLCSP40 (SOT1942-1)

WLCSP40, wafer level chip-scale package; 40 bumps; 0.4 mm pitch; 2.25 mm x 3.45 mm x 0.525 mm body (backside coating included)

4. Legal information

Disclaimers

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. NXP Semiconductors takes no responsibility for the content in this document if provided by an information source outside of NXP Semiconductors.

In no event shall NXP Semiconductors be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, NXP Semiconductors' aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the *Terms and conditions of commercial sale* of NXP Semiconductors.

Right to make changes — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

WLCSP40, wafer level chip-scale package; 40 bumps; 0.4 mm pitch; 2.25 mm x 3.45 mm x 0.525 mm body (backside coating included)

5. Contents

1.	Package summary	1
2.	Package outline	2
3.	Soldering	4
4.	Legal information	7
© N	NXP B.V. 2018. All rights reserved	
For	more information, please visit: http://www.nxp.com sales office addresses, please send an email to: salesaddresses@nxp.com te of release: 19 March 2018	