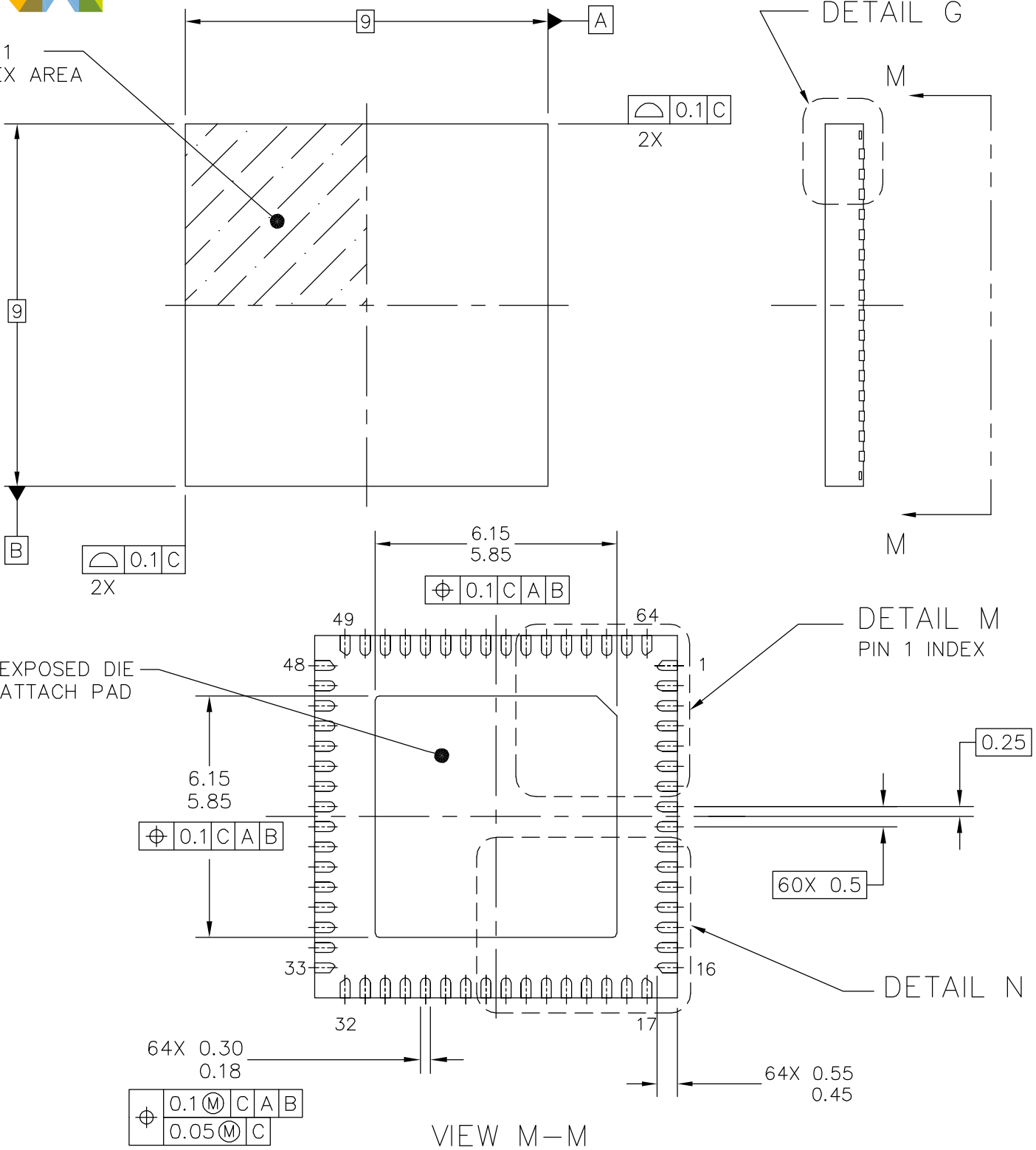


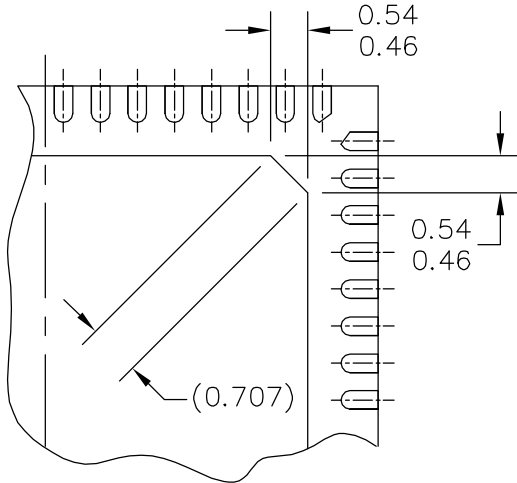


PIN 1 INDEX AREA

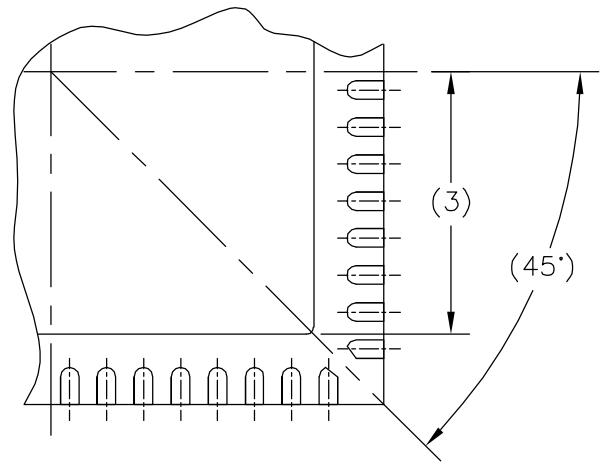


VIEW M-M

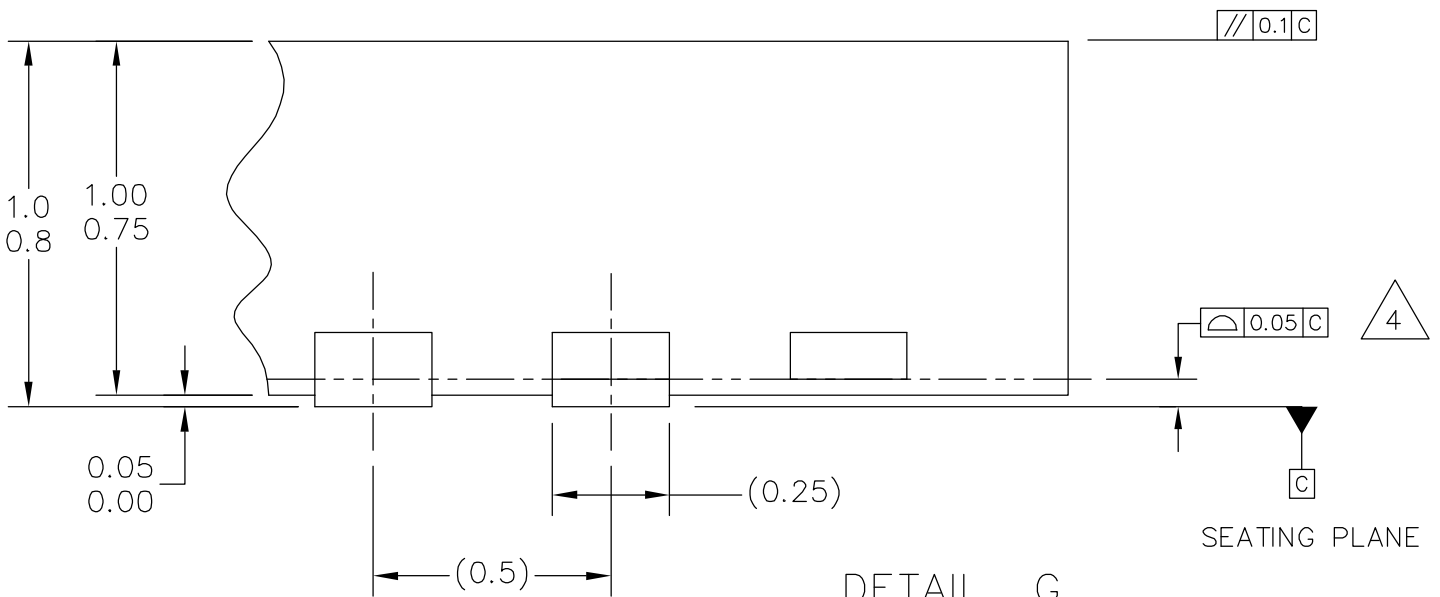
<p>© NXP SEMICONDUCTORS N.V. ALL RIGHTS RESERVED</p>	<p><b>MECHANICAL OUTLINE</b></p>	<p>PRINT VERSION NOT TO SCALE</p>
<p>TITLE: THERMALLY ENHANCED QUAD FLAT NON-LEADED PACKAGE (QFN) 64 TERMINAL, 0.5 PITCH (9 X 9 X 1)</p>	<p>DOCUMENT NO: 98ASA10690D</p>	<p>REV: A</p>
	<p>STANDARD: JEDEC MO-220 VMMD-3</p>	
	<p>SOT804-7</p>	<p>10 MAR 2016</p>



DETAIL M  
PREFERRED PIN1 BACKSIDE IDENTIFIER



DETAIL N  
PREFERRED CORNER CONFIGURATION




DETAIL G  
VIEW ROTATED 90° CW

© NXP SEMICONDUCTORS N.V. ALL RIGHTS RESERVED	<b>MECHANICAL OUTLINE</b>	PRINT VERSION NOT TO SCALE	
TITLE: THERMALLY ENHANCED QUAD FLAT NON-LEADED PACKAGE (QFN) 64 TERMINAL, 0.5 PITCH (9 X 9 X 1)		DOCUMENT NO: 98ASA10690D	REV: A
		STANDARD: JEDEC MO-220 VMMD-3	
		SOT804-7	10 MAR 2016



NOTES:

1. ALL DIMENSIONS ARE IN MILLIMETERS.
2. INTERPRET DIMENSIONS AND TOLERANCES PER ASME Y14.5M-1994.
3. THE COMPLETE JEDEC DESIGNATOR FOR THIS PACKAGE IS: HF-PQFN.
4.  COPLANARITY APPLIES TO LEADS, CORNER LEADS AND DIE ATTACH PAD.
5. MIN METAL GAP SHOULD BE 0.2MM.

© NXP SEMICONDUCTORS N.V. ALL RIGHTS RESERVED	<b>MECHANICAL OUTLINE</b>	PRINT VERSION NOT TO SCALE
TITLE: THERMALLY ENHANCED QUAD FLAT NON-LEADED PACKAGE (QFN) 64 TERMINAL, 0.5 PITCH (9 X 9 X 1)	DOCUMENT NO: 98ASA10690D	REV: A
	STANDARD: JEDEC MO-220 VMMD-3	
	SOT804-7	10 MAR 2016