

SOT1591-3

HVQFN16, plastic, thermal enhanced very thin quad flatpack; no leads; 16 terminals; 0.5 mm pitch; 3 mm x 3 mm x 0.85 mm body

Package information

Package summary

Terminal position code Q (quad) Package type descriptive code HVQFN16 Package type industry code HVQFN16

Package style descriptive code HVQFN (thermal enhanced very thin quad

flatpack; no leads)

Package body material type P (plastic)

Mounting method type S (surface mount)

Issue date 01-08-2018 Manufacturer package code 98ASA00741D

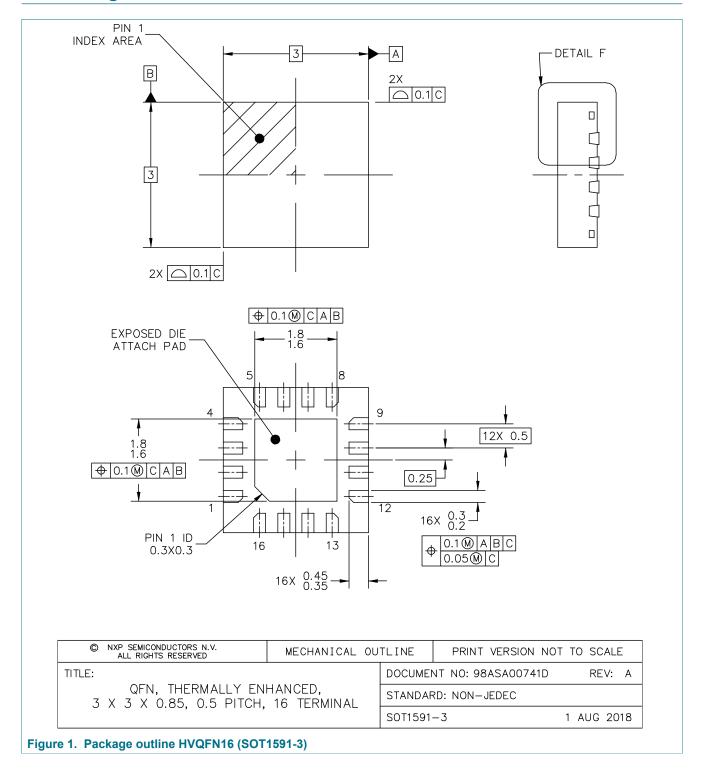
Table 1. Package summary

- table in a deliage cannot y				
Parameter	Min	Nom	Max	Unit
package length	-	3	-	mm
package width	-	3	-	mm
package height	-	0.85	-	mm
nominal pitch	-	0.5	-	mm
actual quantity of termination	-	16	-	

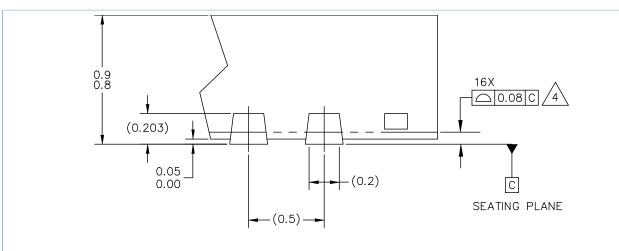


HVQFN16, plastic, thermal enhanced very thin quad flatpack; no leads; 16 terminals; 0.5 mm pitch; 3 mm x 3 mm x 0.85 mm body

2 Package outline



HVQFN16, plastic, thermal enhanced very thin quad flatpack; no leads; 16 terminals; 0.5 mm pitch; 3 mm x 3 mm x 0.85 mm body



DETAIL F VIEW ROTATED 90°CW

NXP SEMICONDUCTORS N.V. ALL RIGHTS RESERVED	MECHANICAL OUTLINE F		PRINT VERSION NO	T TO SCALE
QFN, THERMALLY ENHANCED, 3 X 3 X 0.85, 0.5 PITCH, 16 TERMINAL		DOCUMEN	NT NO: 98ASA00741D	REV: A
		STANDARD: NON-JEDEC		
		SOT1591-	-3	1 AUG 2018

Figure 2. Package outline detail of HVQFN16 (SOT1591-3)

HVQFN16, plastic, thermal enhanced very thin quad flatpack; no leads; 16 terminals; 0.5 mm pitch; 3 mm x 3 mm x 0.85 mm body

NOTES:

- 1. ALL DIMENSIONS ARE IN MILLIMETERS.
- 2. DIMENSIONING AND TOLERANCING PER ASME Y14.5M-1994.
- 3. THIS IS A NON-JEDEC REGISTERED PACKAGE.
- 4. COPLANARITY APPLIES TO LEADS AND DIE ATTACH FLAG.
- 5. MIN. METAL GAP SHOULD BE 0.2 MM.

© NXP SEMICONDUCTORS N.V. ALL RIGHTS RESERVED	MECHANICAL OUTLINE		PRINT VERSION NO	T TO SCALE
OFN THERMALLY ENHANCED		DOCUMEN	NT NO: 98ASA00741D	REV: A
		STANDARD: NON-JEDEC		
0 % 0 % 0.00, 0.0 i i i o i ,	TO TEINIMALE	SOT1591	-3	1 AUG 2018

Figure 3. Package outline note HVQFN16 (SOT1591-3)

HVQFN16, plastic, thermal enhanced very thin quad flatpack; no leads; 16 terminals; 0.5 mm pitch; 3 mm x 3 mm x 0.85 mm body

3 Legal information

Disclaimers

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. NXP Semiconductors takes no responsibility for the content in this document if provided by an information source outside of NXP Semiconductors.

In no event shall NXP Semiconductors be liable for any indirect, incidental, punitive, special or consequential damages (including -without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, NXP Semiconductors' aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the Terms and conditions of commercial sale of NXP Semiconductors.

Right to make changes — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

HVQFN16, plastic, thermal enhanced very thin quad flatpack; no leads; 16 terminals; 0.5 mm pitch; 3 mm x 3 mm x 0.85 mm body

Contents

1	Package summary1
2	Package outline2
3	Legal information5