MC92500

December 10, 1996
Rev. 1.0

Datapath Switching

Errata to

MC92500 ATM Cell Processor

This document describes errata of the MC92500 ATM Cell Processor.

1.1 Summary Table

Table 1-1 provides a summary of MC92500 errata.

Table 1-1Summary of MC92500 Errata

Errata #	Problem	Impact	Work Around
1	The TDO I/O is not driven correctly.	Boundary scanning is impaired.	The MC92500 should be placed as the last device on the JTAG chain. This method will enable all the devices on JTAG chain to be configured, but TDO will not be driven correctly when JTAG's instruction register is written.
2	Latched parallel outputs do not retain their state.	Boundary scanning is impaired.	After the JTAG HIGHZ instruction is finished, the latched parallel outputs of control boundary-scan register cells should be reloaded (enscan1-enscan5: scan bit # 0-4).

1.2 Errata Details

Following are the details of the errata that apply to the MC92500 ATM Cell Processor:

Errata #1: The TDO I/O is not driven correctly

Description: SCAN through MC92500 JTAG's instruction register configures the MC92500, but doesn't drive correctly the test data out I/O.

Projected Impact: Boundary scanning is impaired.

Solution: No fix is planned for the MC92500.

Work Around: The MC92500 should be placed as the last device on the JTAG chain. This method will enable all the devices on the JTAG chain to be configured, but TDO will not be driven correctly when JTAG's instruction register is written.

Errata Applies To: MC92500 ATM Cell Processor.

Errata #2: Latched parallel outputs do not retain their state

Description: When the JTAG HIGHZ instruction is selected, the latched parallel outputs of control boundary-scan register cells that enable the bi-directional pins that observe output enables from the on-chip system logic do not retain their state (enscan1–enscan5 : Scan Bit# 0–4).

Projected Impact: Boundary scanning is impaired.

Solution: No fix is planned for the MC92500.

Work Around: After the JTAG HIGHZ instruction is finished, the latched parallel outputs of control boundary-scan register cells should be reloaded (enscan1–enscan5: Scan Bit# 0–4).

Errata Applies To: MC92500 ATM Cell Processor.

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