68848 CAMEL A (0E76N) ERRATA

(October 6, 1994)

The CAMEL rev A mask 0E76N conforms with the full specification of the 68848, with the following exceptions.

1 .   Asynchronous Interface and Clear-on Read Registers

When using the asynchronous interface, it is possible that reading events in Clear on Read registers may result in the event being cleared before the event is read out of the ELM. This errata applies to all clear-on-read registers, including the interrupt event register. In the case of the interrupt event register, this may cause failure of the PCM software, preventing the node from entering the ring.

Workaround:
• Poll the other status registers to assure no events are missed. This will allow the node to enter the ring successfully.