Freescale Semiconductor, Inc.



Chip Errata DSP56007 Digital Signal Processor Mask: 1F91R

ERRATA

	Er	rata Description	Applies <u>to Mask</u>
1. Specification specified; e.g		ecification timings #83, #84, and #87 in the data sheet provide 1 x T $_{\rm C}$ less than ecified; e.g.,	15010
	a.	specification #83 measures 8 x T_C (12 x T_C) instead of 9 x Tc (13 x T_C) in the EMI fast (slow) DRAM timing mode, respectively;	IF9IK
	b.	specification #84 measures 4 x T _C - 9 (6 x T _C - 9) instead of 5 x T _C - 9 (7 x T _C - 9) in the EMI fast (slow) DRAM timing mode, respectively; and	
	c.	specification #87 measures 4 x T _C - 15 (6 x T _C - 15) instead of 5 x T _C - 15 (7 x T _C - 15) in the EMI fast (slow) DRAM timing mode, respectively.	
	We gra spe 60 DF me	orkaround: The refresh t _{RAS} timing requirement in a 60 ns DRAM device is not et when the DSP56007FJ66 runs at full speed (66 MHz) and the EMI is pro- ammed in the "fast" DRAM timing mode (EDTM = 0 in ECSR). In this case, only ec timing #84 does not meet the requirement. To ensure correct refresh of a ns DRAM connected to DSP56007FJ66 running at 66 MHz and accessing the RAM using the EMI fast timing mode, one of the following methods is recom- ended:	
	a.	Access the DRAM device without using the internal refresh timer, as de- scribed in Section 3.4.1 of the user's manual. This method is possible if the data transfer rate is high.	
	b.	Refresh the DRAM device in a burst manner, as described in Section 3.4.2.2 of the user's manual. This method divides the real time audio processing window into two portions.	
		During the first portion, DRAM data transfers without refresh (EREF = 0) are done using the EMI fast timing mode (EDTM = 0).	

During the second portion, when EMI is not doing data transfers, a burst of refresh cycles is enabled (EREF = 1) using the EMI slow timing mode (EDTM = 1).



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NOTES

- 1. An over-bar (for example, \overline{xxxx}) indicates an active-low signal.
- 2. The letters seen to the right of the errata tell which DSP56007 mask numbers apply.

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