



SUMMARY OF CHANGES

This is a complete revision, with complete reprint. All known errors in the publication have been corrected. The following summary lists significant changes.

Section 2 Registers

Page 2-5	Added Table 2-1 FPSCR Control, Status, and Sticky Bits.
Page 2-14	Added to the description of the BE bit in Table 2-8 Machine State Register Bit Settings.

Section 4

Page 4-62	Updated Table 4-30 Supervisor-Level SPR Encodings.
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Section 6 Exceptions

Page 6-7	Modified Figure 6-1.
Page 6-14	Added to the description of the BE bit in Table 6-7 Machine State Register Bit Settings.
Page 6-19	Changed MSR[RI] to SRR1[RI] in second paragraph under 6.11.2.

Section 7 Instruction Timing

7-16	Corrected the syntax in the programming example in Section 7.7.1.
7-17	Corrected the syntax in the programming example in Section 7.7.2.
7-18	Corrected the syntax in the programming example in Section 7.7.3.
7-20	Corrected the syntax in the programming example in Section 7.7.4.
7-21	Corrected the syntax in the programming example in Section 7.7.5.
7-22	Corrected the syntax in the programming example in Section 7.7.6.
7-23	Corrected the syntax in the programming example in Section 7.7.7.



Section 8 Development Support

Page 8-37	Revised the second paragraph of section 8.4.2.
Page 8-51	Updated Table 8-30 ICTRL Bit Settings to include the SER bit.
Page 8-57	Corrected the reset value for CHSTPE in Table 8-36 DER Bit Settings.

Section 9 Instruction Set

Page 9-17	Corrected the RTL.
Page 9-18	Corrected the RTL.
Page 9-21	Made RTL consistent with RTL on pages 9 -25 and 9-27.
Page 9-22 to 9-24	Corrected Table 9-8 Simplified Mnemonics for bc, bca, bcl, and bcla Instructions.
Page 9-25 to 9-26	Corrected the RTL. Corrected Table 9-9 Simplified Mnemonics for bcctr and bcctrl Instructions.
Page 9-27	Corrected the RTL.
Page 9-28	Corrected Table 9-10 Simplified Mnemonics for bclr and bclrl Instructions.
Page 9-30	Revised the second paragraph of text. Updated Table 9-11 Simplified Mnemonics for cmp Instruction.
Page 9-31	Updated Table 9-12 Simplified Mnemonics for cmpi Instruction.
Page 9-32	Corrected the RTL. Updated Table 9-13 Simplified Mnemonics for cmpl Instruction.
Page 9-33	Corrected the RTL. Revised the second paragraph of text. Updated Table 9-14 Simplified Mnemonics for cmpli Instruction.
Page 9-34	Corrected the RTL.
Page 9-43	Updated text regarding setting OV bit.
Page 9-44	Updated text regarding division by 0.
Page 9-45	Updated text regarding setting LT, GT, and EQ bits.
Page 9-47	Corrected the RTL.
Page 9-53	Revised the second paragraph of text. Corrected Other Reg-

isters Altered. Added RTL.



Page 9-54	Revised the second text paragraph of text. Corrected Other Registers Altered.
Page 9-78	Corrected the RTL. Revised the third paragraph of text.
Page 9-79	Corrected the RTL. Revised the third paragraph of text.
Page 9-82	Corrected the RTL. Revised the fourth paragraph of text.
Page 9-83	Corrected the RTL. Revised the fourth paragraph of text.
Page 9-86	Corrected the RTL. Revised the fourth paragraph of text.
Page 9-87	Corrected the RTL. Revised the fourth paragraph of text.
Page 9-90	Corrected the RTL. Revised the fourth paragraph of text.
Page 9-91	Corrected the RTL. Revised the third paragraph of text.
Page 9-94	Corrected the RTL.
Page 9-95	Corrected the RTL. Revised the third paragraph of text.
Page 9-96	Corrected the RTL. Revised the third paragraph of text.
Page 9-97	Corrected the RTL.
Page 9-98	Corrected the RTL. Revised the fourth paragraph of text.
Page 9-99	Corrected the RTL. Revised the fourth paragraph of text.
Page 9-100	Corrected the RTL.
Page 9-101	Corrected the RTL.
Page 9-102	Corrected the RTL.
Page 9-103	Corrected the RTL.
Page 9-104	Corrected the RTL. Revised the third paragraph of text.
Page 9-105	Corrected the RTL. Revised the third paragraph of text.
Page 9-106	Corrected the RTL.
Page 9-108	Revised the first paragraph of text.
Page 9-110	Corrected the RTL.



Page 9-121	Revised the first paragraph of text.
Page 9-123	Corrected the RTL and instruction encoding.
Page 9-125	Corrected the RTL.
Page 9-126	Corrected the RTL.
Page 9-127	Corrected the RTL.
Page 9-128	Corrected the RTL. Revised the first paragraph of text.
Page 9-130	Revised the first paragraph of text.
Page 9-142	Corrected the RTL. Revised the first paragraph of text.
Page 9-143	Corrected the RTL.
Page 9-144	Corrected the RTL.
Page 9-145	Corrected the RTL.
Page 9-146	Corrected the RTL.
Page 9-147	Corrected the RTL. Revised the third paragraph of text.
Page 9-148	Corrected the RTL. Revised the third paragraph of text.
Page 9-151	Corrected the RTL. Revised the third paragraph of text.
Page 9-152	Corrected the RTL. Revised the third paragraph of text.
Page 9-154	Corrected the RTL. Revised the second paragraph of text.
Page 9-156	Corrected the RTL. Revised the third paragraph of text.
Page 9-157	Corrected the RTL. Revised the fourth paragraph of text.
Page 9-161	Corrected the RTL. Revised the fourth paragraph of text.
Page 9-162	Corrected the RTL. Revised the fourth paragraph of text.
Page 9-165	Corrected the RTL. Revised the third paragraph of text.
Page 9-166	Corrected the RTL. Revised the third and fourth paragraphs of text.
Page 9-170	Corrected the RTL. Revised the fourth paragraph of text.
Page 9-171	Corrected the RTL. Revised the fourth paragraph of text.

Appendix A Instruction Set Listings



A-1 to A-6	Corrected Table A-1 Complete Instruction List Sorted by Mnemonic.
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Appendix E Simplified Mnemonics

E-2	Added caution note to section E.3.
E-9	Corrected Table E-8 Operands for Simplified Branch Mnemonics with Comparison Conditions.
E-14	Corrected encoding for no-op instruction.

