

## Freescale Semiconductor

MC68360 Autobaud Operation:

To sucessfully use autobaud on the 360

1. The SCC performing the autobaud must be connected to the BRG of the same number.

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i.e. SCC1 must connect to BRG1 SCC2" "BRG2 etc....
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- 2. To succesfully interrupt on an ATB lock:
  - Set BRGCx = 1001A (115K rate) Do not set AB bit
  - Perform other initialization (BD's, interrupt vectors, etc)
  - Enable receiver on SCCx
  - Set BRGCx bit #13 to one (AB bit = 1)
  - Allow the transmitter to transmit, and the autobaud will both lock and generate an interrupt.

In the case of setting the AB bit in BRGCx before the SCC receiver has had clocking from its BRG, the SCC has not "woken up" and therefore does not see the AB interrupt. The SCC must have a minimum of three clocks from the BRG. Using the fastest rate available for the BRG assures that the three clocks are generated in the minimum amount of time.

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