AN14238

Getting started with EdgeLock A30 secure authenticator support package Rev. 2.0 — 2 September 2025 Application note

Document information

Information	Content
Keywords	EdgeLock A30 secure authenticator, NX Middleware
Abstract	This document is the entry point for getting familiar with EdgeLock A30 support package contents and how to get started with them.



Getting started with EdgeLock A30 secure authenticator support package

1 About EdgeLock A30 secure authenticator

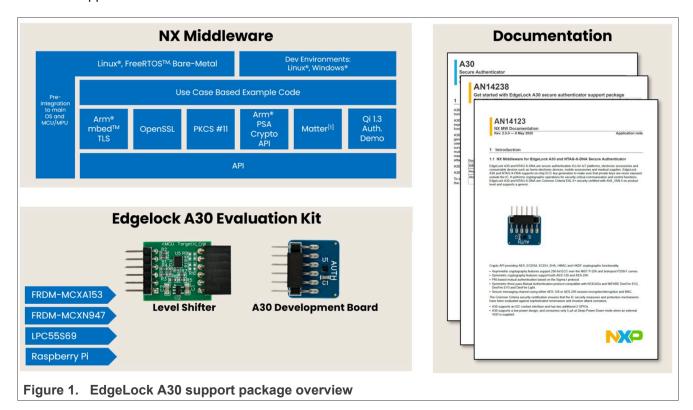
EdgeLock A30 is a secure authentication IC for IoT platforms, electronic accessories and consumable devices such as home electronic devices, mobile accessories and medical supplies.

EdgeLock A30 supports on-chip ECC key generation to make sure that private keys are never exposed outside the IC. It performs cryptographic operations for security critical communication and control functions. EdgeLock A30 is Common Criteria EAL 6+ security certified with AVA_VAN.5 on product level and supports a generic Crypto API providing AES, ECDSA, ECDH, SHA, HMAC and HKDF cryptographic functionality.

- Asymmetric cryptography features support 256-bit ECC over the NIST P-256 and brainpool P256r1 curves.
- Symmetric cryptography features support both AES-128 and AES-256.
- PKI-based mutual authentication based on the Sigma-I protocol.
- · Symmetric three-pass Mutual Authentication protocol.
- Secure messaging channel using either AES-128 or AES-256 session encryption/decryption and MAC.
- Number of supported persistent key entries:
 - Up to five persistent EC key entries for Sigma-I and crypto primitives.
 - Up to five persistent AES key entriesfor mutual authentication, Secure Dynamic Messaging and key update.
 These five AES key entries are not consuming any user memory.
 - Up to eight persistent AES key entriesfor crypto operations.

The Common Criteria security certification ensures that the IC security measures and protection mechanisms have been evaluated against sophisticated noninvasive and invasive attack scenarios.

- A30 supports an I²C contact interface and has two additional GPIOs.
- A30 supports a low-power design, and consumes only 5 μA at Deep-Power-Down mode when an external VDD is supplied.



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Delivered as a ready-to-use solution, the EdgeLock A30 includes a complete product support package that simplifies design-in and reduces time to market. The EdgeLock A30 support package offers:

- · EdgeLock A30 development kit
- NX Middleware
 - Software enablement for MCUs and MPUs.
 - Integration with the most common cryptographic libraries like OpenSSL, Mbed TLS and PKCS #11.
 - Multi-platform software enablement targeting freeRTOS and Linux as well as Windows as evaluation platform.
 - Sample code for major IoT and secure authentication use cases.
- Documentation

This document lists the existing material within EdgeLock A30 support package, organized in the following sections:

- Section 2 "EdgeLock A30 development kit"
- Section 3 "MCU/MPU reference boards"
- Section 4 "NX Middleware"
 - Section 4.4 "Evaluation using a Windows PC"
 - Section 4.5 "Evaluation using a MCU board"
 - Section 4.6 "Evaluation using Raspberry Pi"
 - Section 4.7 "Device provisioning"
 - Section 4.8 "Device configuration"
- Section 5 "EdgeLock 2Go service for EdgeLock A30"
- Section 6 "Supported EdgeLock A30 documentation"
- Section 7 "Appendix"
 - Section 7.1 "EdgeLock A30 product identification"
 - Section 7.2 "Available free memory of EdgeLock A30 ICs"
 - Section 7.3 "EdgeLock A30 application circuit diagram"
 - Section 7.4 "EdgeLock A30 WLCSP16 pin description"
 - Section 7.5 "EdgeLock A30 HVQFN20 pin description "
 - Section 7.6 "EdgeLock A30 development board schematic"
 - Section 7.7 "Level shifter board schematic"

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2 EdgeLock A30 development kit

The A30-EVAL is a flexible and easy-to-use development kit for the EdgeLock A30 secure authenticator. It can be used in various MCU and MPU platforms. This kit allows the evaluation of the EdgeLock A30 secure authenticator features and simplifies the development of secure IoT and authentication applications.

The A30 development kit including:

- · EdgeLock A30 development boards
- · Level shifter board
- · Jumper wires

Table 1 summarizes the contents of the evaluation kit.

Table 1. EdgeLock A30 development kit A30-EVAL

lable 1. EdgeLock A30 development kit A30-EVAL					
Part number	12NC	Number of pieces	Content	Picture	
A30-EVAL	935505 094598	3	A30 evaluation board	MCU/MPU SCL 1.0 2.0V SDA GND VCC	
		1	Level shifter board	MCU/MPU SCL 3.3 5.0V SDA GND VCC A30	
		6	Jumper wires		

The EdgeLock A30 is optimized for battery-powered applications and systems utilizing MCUs or MPUs with a 1.8 V supply voltage. To ensure compatibility and power efficient operation in such environments, the EdgeLock A30 supports a supply voltage range from 1.0 V to 2.0 V.

While most modern MCU and MPU families support operation at 1.8 V, many evaluation kits continue to use higher voltages such as 3.3 V or even 5 V. To facilitate seamless and rapid prototyping, the EdgeLock A30 development kit integrates a level shifter that translates voltage levels as required, ensuring compatibility across different platforms.

The corresponding board schematics can be found in Section 7.6 and Section 7.7.

Figure 2 shows how to connect the Level Shifter and A30 board.

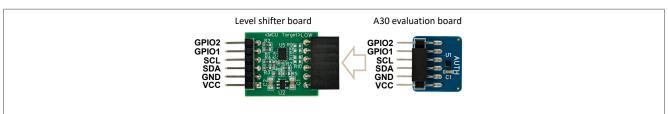


Figure 2. Connecting A30 development board with level shifter board

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3 MCU/MPU reference boards

The EdgeLock A30 secure authenticator IC is designed to be used as a part of an IoT system. It works as an auxiliary security authenticator device attached to a host controller (MCU or MPU board). The host controller communicates with EdgeLock A30 through an I²C interface with the host controller being the controller and the EdgeLock A30 being the target.

The EdgeLock A30 can be connected to any MCU/MPU on the market supporting the I²C interface. To enable easy evaluation of the EdgeLock A30, the NX Middleware supports the NXP FRDM-MCXA153, FRDM-MCXN947 and the LPCXpresso 55S69 demo board as an MCU reference platform.

Table 2. MCU reference boards

Part number	12NC	Content	Picture
FRDM-MCXA153	935455513598	FRDM development board for MCX A14x/A15x MCUs	
FRDM-MCXN947	935455837598	FRDM development board for MCX N94/N54 MCUs	
LPC55S69-EVK	935377412598	LPCXpresso55S69 development board	

The Raspberry Pi is used to demonstrate the embedded Linux enablement of A30. The NX Middleware is compatible with all Raspberry Pi board versions and has been validated on the Raspberry Pi 4 Model B.

Table 3. MPU reference boards

Part number	Content	Picture
Raspberry Pi	Raspberry Pi (any model)	

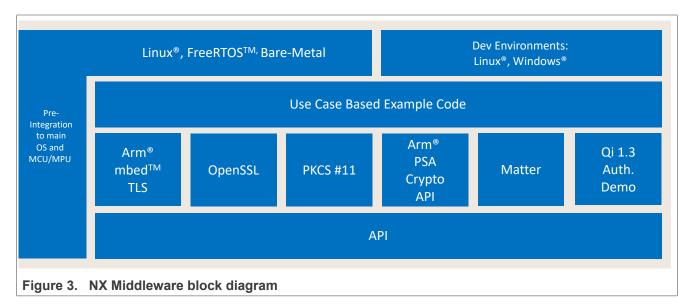
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4 NX Middleware

4.1 Overview

The NX Middleware is a unified software stack that simplifies the integration of the EdgeLock A30 secure authenticator IC into MCU or MPU-based software platforms.is a single software stack designed to facilitate the integration of EdgeLock A30 secure authenticator IC into your MCU or MPU software. The NX Middleware abstracts the commands and communication interface exposed by EdgeLock A30. It is directly accessible from stacks like mbedTLS, OpenSSL and PKCS #11. In addition, it includes code examples for quick integration of features and use cases such as TLS and AWS cloud service onboarding. It also comes with support for reference MCU/MPU platforms and can be ported to multiple host platforms and host operating systems.

Figure 3 is a simplified representation of the layers and components of NX Middleware:



The NX Middleware is delivered with CMake files that include the set of directives and instructions describing the project's source files and targets. The CMake files allow developers to build NX Middleware in their target platform, enable or disable features or change setting flags, among others.

The CMake based compilation option is provided as a convenient way for developers to run a project example on different target platforms:

- · Windows/Linux PC for evaluation purpose
- MCU boards
- MPU boards

The NX Middleware has built-in cryptographic and abstracts the commands as well the communication interface exposed by NXP EdgeLock A30 secure authenticator IC. The NX Middleware is directly accessible form the following cryptographic libraries:

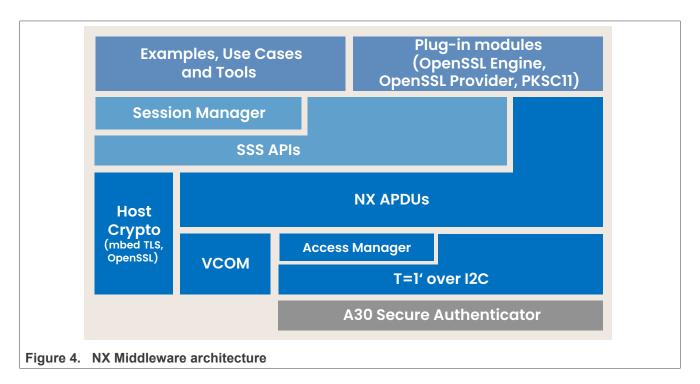
- ARM mbedTLS and PSA (Platform Security Architecture)
- OpenSSL
- PKCS #11

The NX Middleware can be downloaded via GitHub https://github.com/NXP/nxmw.

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4.2 Architecture overview

Figure 4 gives a brief overview of the NX Middleware.



- Session Manager
 - APIs to open a session. The following sessions are supported:
 - Plain session
 - PKI based asymmetric Mutual Authentication (Sigma-I-Verifier or Sigma-I-Prover)
 - AES-based Symmetric Mutual Authentication
 - Note: Both mutual authentication methods initiate a MIFARE DESFire compatible EV2 secure messaging channel (authenticated session).
- SSS APIs
 - Provides abstraction APIs for EdgeLock A30, OpenSSL and mbedTLS host crypto.
 - SSS APIs are supporting common crypto operations.
- NX APDUs
 - Implements the EdgeLock A30 authenticator commands (APDUs)
- · Access Manager
 - Manage access from multiple Linux processes to EdgeLock A30. Client processes connect over the JRCPv1 protocol to the Access Manager.

T=1' over I²C communication protocol according to Global Platform.

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4.3 Code documentation

The code documentation provided as part of NX Middleware package in PDF format and as a part of the GitHub release. The primary audience are programmers, developers, system architects and system designers.

The PDF version of the NX Middleware documenation (AN14123) is located in the nxmw\doc folder as shown in Figure 5.

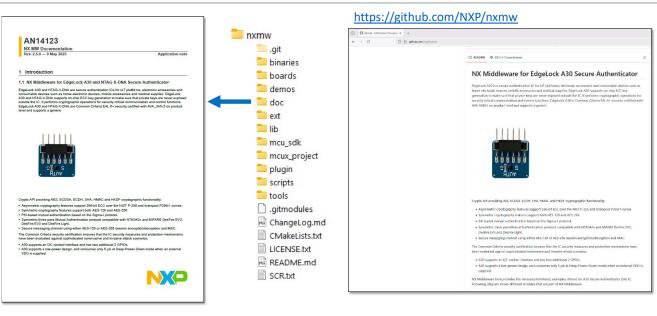


Figure 5. NX Middleware PDF documentation

4.4 Evaluation using a Windows PC

To accelerate evaluation and prototyping, the NX Middleware enables the protocol stack to be built and executed directly on a Windows PC, eliminating the need for immediate porting to the target MCU or MPU platform.

To connect the EdgeLock A30 evaluation board to a Windows PC, one of the following supported MCU development boards is required:

- FRDM-MCXA153
- FRDM-MCXN947
- LPC55S69-EVK

These development boards connect to a Windows PC via USB and must be running the T=1 over I²C firmware. This firmware serves as a communication bridge between the PC's virtual COM (VCOM) port and the EdgeLock A30.

The EdgeLock A30 and the level shifter board must be connected as illustrated in <u>Figure 7</u>, <u>Figure 8</u> and <u>Figure 9</u>.

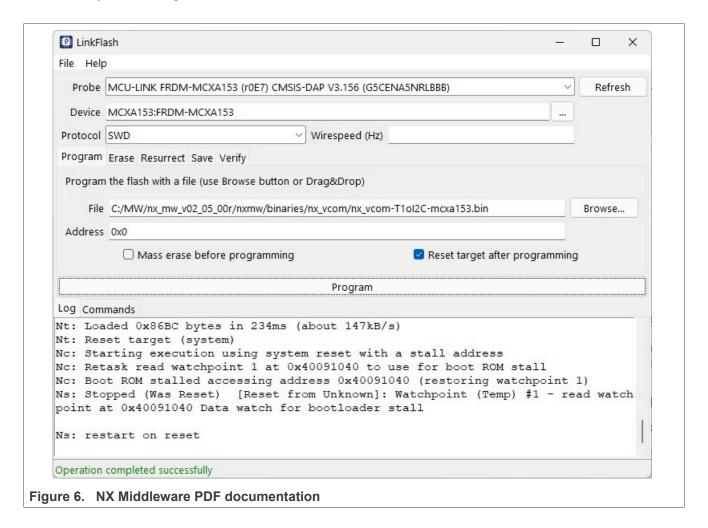
When the NX Middleware is compiled for the Windows platform, all low-level APDU commands and responses intended for the EdgeLock A30 are transmitted over the VCOM interface, rather than directly using the T=1 over I²C protocol. T=1 over I²C protocol is handled by the MCU firmware.

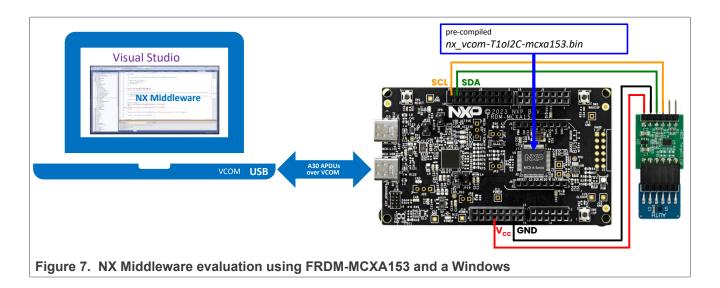
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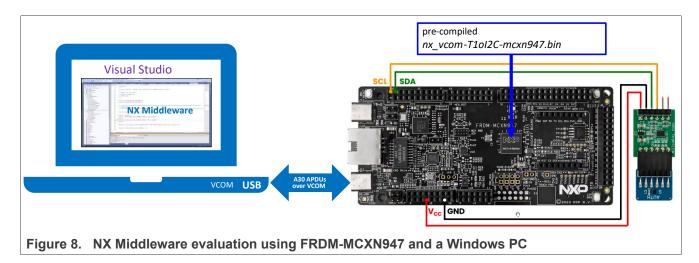
The required T=1 over I²C firmware is available both as an MCU project and as <u>a pre-compiled binary</u> on GitHub. The pre-compiled binaries can be easily programmed onto the supported MCU boards using the GUI Flash Tool integrated into the MCUXpresso IDE.

For detailed instructions, refer to the MCUXpresso IDE User Guide, section 17.1.3 "Advanced GUI Flash Tool – Programming an Arbitrary Binary".

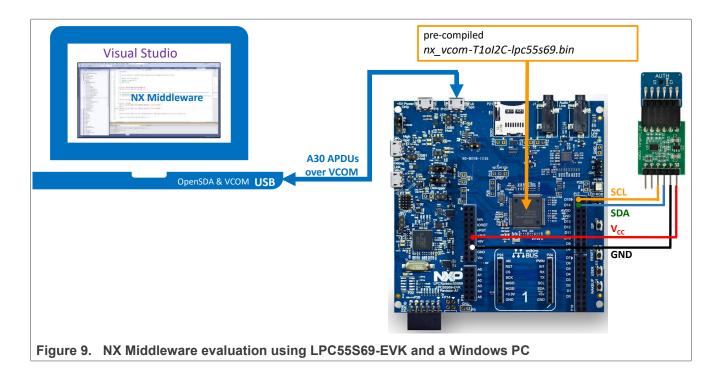
Alternatively, the firmware can be programmed without using the MCUXpresso IDE by using the standalone LinkFlash utility. The GUI LinkFlash utility, included with <u>LinkServer</u>, is designed for programming and managing flash memory on NXP target devices.







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4.4.1 Building NX Middleware on Windows

4.4.1.1 Prerequisites

To successfully build the NX Middleware on a Windows PC, ensure the following tools are installed:

- Visual Studio (Version 2015 or later)
- Python 3 (32-bit or 64-bit, Version 3.10.x to 3.11.x)
- Git (actual version)
- West (Zephyr's meta-tool) (Version 1.2.0 or later)
- CMake (Version 3.30.0 or later). Preferred installation path: C:/opt/cmake/bin/cmake.exe
- OpenSSL (64-bit, Version 3.4.0 or later)

4.4.1.2 Download or Clone the NX Middleware

Note: Avoid using spaces in the path for <nx_mw_root_folder>

Option A: Download the latest release of the NX Middleware via West

west init -m https://github.com/NXP/nxmw.git --mf mcu_sdk/west.yml workspace
cd workspace
west update

Resulting folder structure::

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```
|- nx_mw_root_folder>
|- workspace
|- .west
|- mcuxsdk
|- nxmw
```

Option B: Clone the latest release of the NX Middleware via Git

```
git clone --recurse-submodules https://github.com/NXP/nxmw.git nxmw
cd nxmw
west init -l --mf mcu_sdk/west.yml
cd ..
west update
```

Resulting folder structure::

```
|- nx_mw_root_folder>
|- .west
|- mcuxsdk
|- nxmw
```

4.4.1.3 Set Up the NX Middleware Development Environment

Note: The <code>env_setup.bat</code> script configures the necessary environment variables for the build tools. If your installations of MCUXpresso, Visual Studio, Java, Python, or CMake are in non-default directories, please update the paths in <code>env_setup.bat</code> accordingly.

Open a command prompt and navigate to the following directory:

```
cd nxmw\scripts
```

Run the environment setup script:

```
env_setup.bat
```

In the same directory, execute the Python script to generate the build files:

```
python create_cmake_projects.py
```

Depending on your Python installation, you may need to use:

```
py create_cmake_projects.py
```

The build files will be generated in: <nx mw root folder>\nxmw build

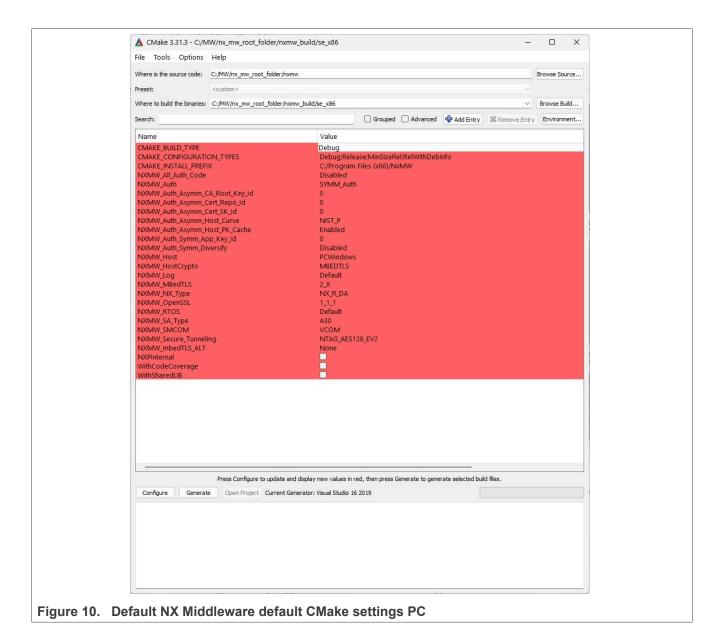
The Visual Studio solution file will be located at: <nx mw root folder>\nxmw build\se x86\NxMW.sln

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4.4.1.4 Open CMake GUI to explore the default CMake settings

Navigate to <nx_mw_root_folder>\nxmw_build\se_x86\ and run the follwing command to open the CMake GUI.

cmake-gui .



The default EdgeLock A30 CMake settings are:

```
CMAKE_BUILD_TYPE:STRING=Debug

CMAKE_CONFIGURATION_TYPES:STRING=Debug; Release; MinSizeRel; RelWithDebInfo

CMAKE_INSTALL_PREFIX:PATH=C:/Program Files (x86)/NxMW

NXMW All Auth Code:STRING=Disabled
```

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```
NXMW Auth:STRING=SYMM Auth
NXMW_Auth_Asymm_CA_Root_Key Id:STRING=0
NXMW_Auth_Asymm_Cert_Repo_Id:STRING=0
NXMW Auth Asymm Cert SK Id:STRING=0
NXMW Auth Asymm Host Curve: STRING=NIST P
NXMW Auth Asymm Host PK Cache: STRING=Enabled
NXMW Auth Symm App Key Id:STRING=0
NXMW Auth Symm Diversify:STRING=Disabled
NXMW Host:STRING=PCWindows
NXMW HostCrypto:STRING=MBEDTLS
NXMW_Log:STRING=Default
NXMW_NX_Type:STRING=NX_R_DA
NXMW_OpenSSL:STRING=1 1
NXMW RTOS:STRING=Default
NXMW SA Type=A30
NXMW SMCOM: STRING=VCOM
NXMW Secure Tunneling:STRING=NTAG AES128 EV2
NXPInternal:BOOL=ON
NXTST FTR TEST COUNT:BOOL=OFF
WithCodeCoverage:BOOL=OFF
WithSharedLIB:BOOL=OFF
```

There is no need to modify any CMake option for the following examples. For further details please refer to the NX Middleware documentation on GitHub.

4.4.1.5 Build NX Middleware using the command line

To build the NX Middleware including all demo/examples using the command line run:

```
cmake --build .
```

To build a single example run:

```
cmake --build . --target <example_name>
```

For example, the build only the ECC example code run:

```
cmake --build . --target ex_ecc
```

4.4.2 Running NX Middleware examples from command line

Navigate to <nx mw root folder>\nx-mw-top build\se x86\bin\Debug

Use the corresponding VCOM port number to run e.g. the example nx_ecc.exe

```
nx_ecc.exe COMxx
```



Figure 11. Windows Device Manger - Identify the correct VCOM port PC

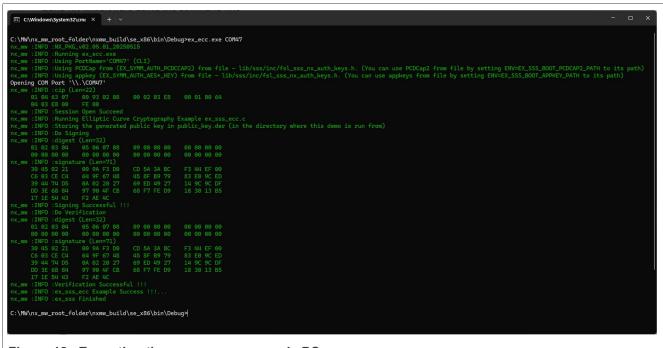


Figure 12. Executing the nx_ecc.exe example PC

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4.4.3 Running NX Middleware example projects in Visual Studio

4.4.3.1 Opening the Project in Visual Studio

Navigate to the following directory: $< nx_mw_root_folder> nxmw_build>se_x86$. Locate and double-click the NxMW.sln file to open the solution in Visual Studio.

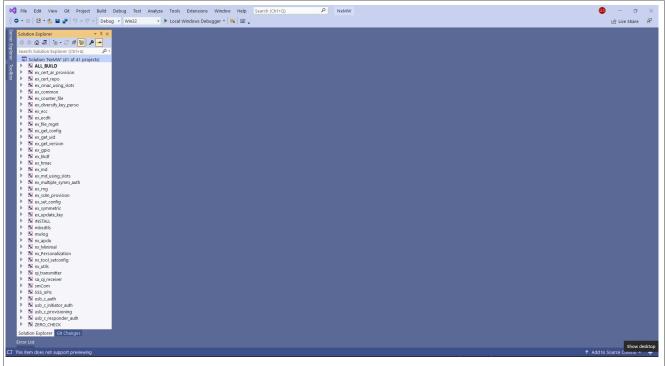


Figure 13. NxMW.sln Visual Studio project workspace

4.4.3.2 Configuring the VCOM Port

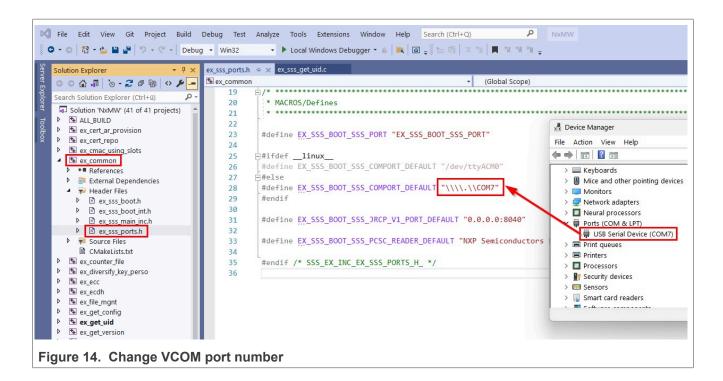
To configure the correct COM port for the NX middleware project:

- Navigate to the ex common project folder.
- Open the ex sss ports.h file located in the headers directory.
- · Locate the following line:

```
#define EX_SSS_BOOT_SSS_COMPORT_DEFAULT "\\\\.\\COMx"
```

Replace COMx with the actual COM port number assigned to your device (e.g., COM3).

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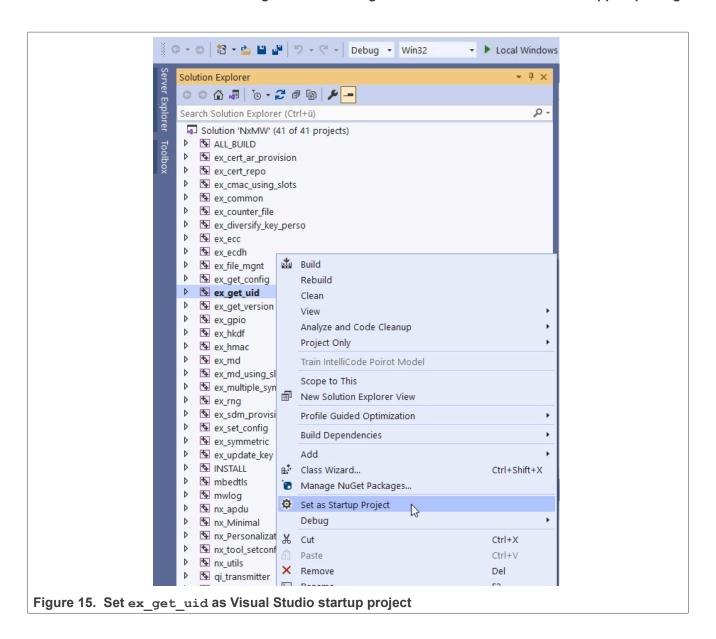


4.4.3.3 Set an Example Project as Startup

To define an example project (e.g., ex get uid) as the startup project:

- In Solution Explorer, right-click on the ex get uid project.
- Select Set as StartUp Project from the context menu.

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4.4.3.4 Build and Run the Project

- · Click Build to compile the project.
- Once the build completes successfully, click Start to run the application.

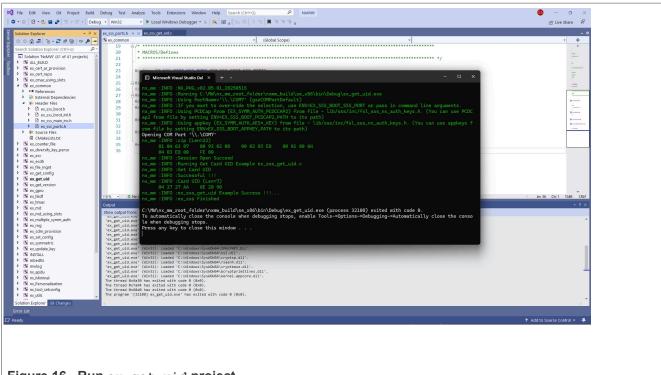


Figure 16. Run ex get uid project

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4.5 Evaluation using a MCU board

The NX Middleware includes a set of MCU-based examples that demonstrate the use of EdgeLock A30 in the latest authenticator security use cases. The following MCU boards are used as reference platforms:

- FRDM-MCXA153
- FRDM-MCXN947
- LPC55S69-EVK

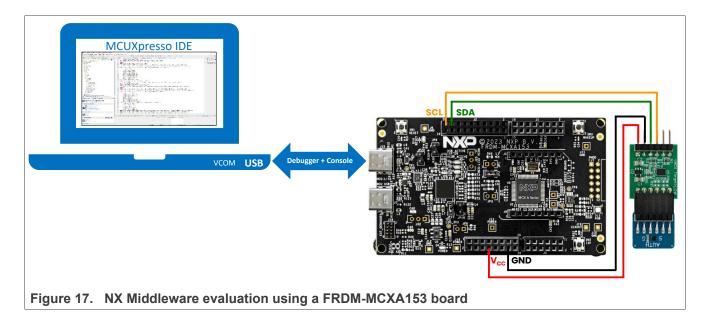
The NX Middleware stack is designed for easy portability to other MCU or MPU platforms.. For detailed implementation guidance, please refer to the NX Middleware documentation available on <u>GitHub</u>.

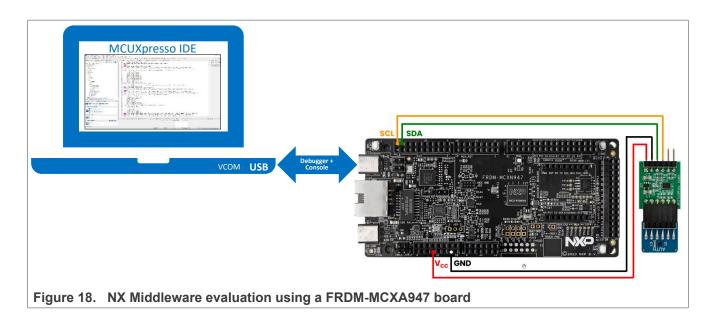
MCU project examples can be imported into MCUXpresso in one of the following formats:

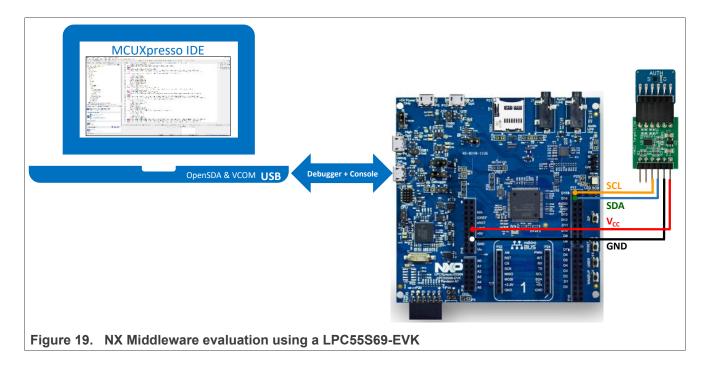
- Standalone MCUXpresso projects
- CMake-based projects

These examples provide a quick and practical way to evaluate EdgeLock A30 features. The source code is modular and can be adapted for customer-specific implementations.

To run the examples, connect the reference boards to a Windows PC via USB.







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4.5.1 Standalone MCUXpresso projects

MCUX presso standalone demo projects for the supported MCU boards are provided in their respective MCUX project folders on GithHub:

- FRDM-MCXA153 MCUXpresso projects
- FRDM-MCXN947 MCUXpresso projects
- LPC55S69-EVK MCUXpresso projects

To build and run the example projects, refer to the online guide <u>Getting Started on MCUs Using Standalone</u> <u>MCUXpresso Projects</u>, which provides step-by-step instructions for setting up the development environment, importing projects, and executing them on supported hardware.

Note: All project source files are symbolic links to the shared middleware components and are not physical copies. The only exception is the fsl_sss_ftr.h file, which is unique to each project and contains board-specific configuration settings. This distinction is important when creating new standalone MCUXpresso projects, as the symbolic linking and configuration must be handled correctly. Instructions for creating new projects are available in the documentation on GitHub.

Note: Projects are linked to specific versions of third-party software. Most demos use mbedTLS v2 by default. To switch to <u>mbedTLS v3</u>, you must:

- Remove mbedTLS v2 from the source tree
- Add mbedTLS v3
- · Update the macros in fsl_sss_ftr.h accordingly

Note: The FRDM-MCXA153 is a low-power MCU based on the Arm® Cortex®-M33 core, featuring up to 128 KB of flash and 32 KB of RAM. Due to its limited memory resources, adjustments to stack and heap sizes may be necessary depending on the selected authentication method - Symmetric or Asymmetric (Sigma-I). Detailed memory configuration guidelines are available in the project's documentation on <u>GitHub</u>.

4.5.1.1 Feature Configuration File (fsl sss ftr.h)

Each MCUXpresso project includes a dedicated feature configuration file named fsl_sss_ftr.h. This file defines compile-time settings that tailor the middleware stack to the specific MCU board, host environment, communication interface, cryptographic library, and authentication method.

The file enables or disables features using preprocessor macros, ensuring that only the relevant components are included during build time. This modular approach simplifies project customization and supports a wide range of use cases.

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```
199
                             200
 > Project Settings
                            201⊖/** NXMW_Auth : NX Authentication
 > Binaries
                            202
 > M Includes
                                  * This settings is used by examples to connect using va:
                            203
  >  components
                                  * to authenticate with the Nx SE.
                             204
 > 🕮 device
                                 * Refer to :numref: `nx-auth-sessions` --- :ref: `nx-auth-
                             205
  > 🚰 drivers
                            206

✓ 

Source

                            207
   > 🗁 board
                            208 /** Use the default session (i.e. session less) login */
   > 🛵 lib
                            209 #define SSS HAVE AUTH NONE 1
   > mbedtls
                            210
   > R nx_Minimal.c
                            211 /** SIGMA I Verifier */
   > c startup_mcxa153.c
                            212 #define SSS HAVE AUTH SIGMA I VERIFIER 0
  > 🗁 Debug
                             213
  fsl_sss_ftr.h
                            214 /** SIGMA I Prover */
   X nx_Minimal.mex
                            215 #define SSS HAVE AUTH SIGMA I PROVER 0
   🔜 nx_Minimal JLink Debug.launch
                            216
                            217 /** Symmetric Authentication */
                            218 #define SSS HAVE AUTH SYMM AUTH 0
Figure 20. Feature Configuration File (fsl sss ftr.h)
```

4.6 Evaluation using Raspberry Pi

The NX Middleware offers several components and example code to implement and verify EdgeLock A30 on devices running an embedded Linux distribution:

- OpenSSL engine compatible with OpenSSL versions 1.1.1
- OpenSSL provider compatible with OpenSSL versions 3.0
- PKCS#11 Plugin
- SSS API

The Raspberry Pi was selected as a reference MPU platform running embedded Linux. For further details, refer to the following chapters in the GitHub documentation:

- · Raspberry Pi Build
- · Access Manager
- · Introduction on OpenSSL engine
- Introduction on OpenSSL provider
- PKCS#11 Plugin
- OpenSSL Engine: TLS Client example
- AWS Demo for Raspberry Pi

Note: If several Linux processes want to access EdgeLock A30 at the same time, it is necessary to use the Access Manager. The Access Manager manages the simultaneous access of several Linux processes to EdgeLock A30. Linux client processes are connected to the Access Manager via the JRCPv1 protocol.

Figure 21 shows the principal hardware setup.

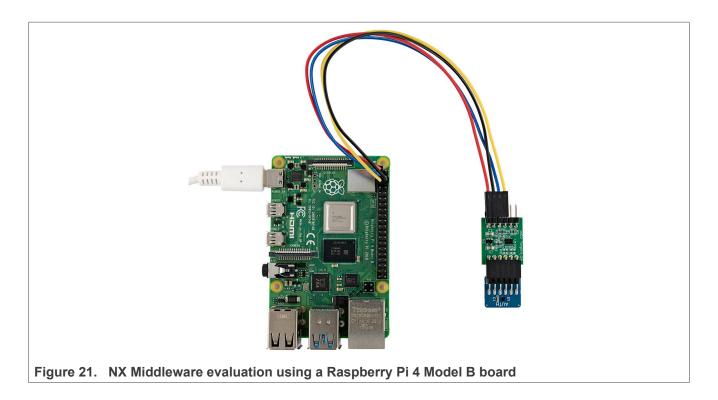


Figure 22 and Table 4 are showing the detailed connection of the EdgeLock A30 to the Raspberry Pi:

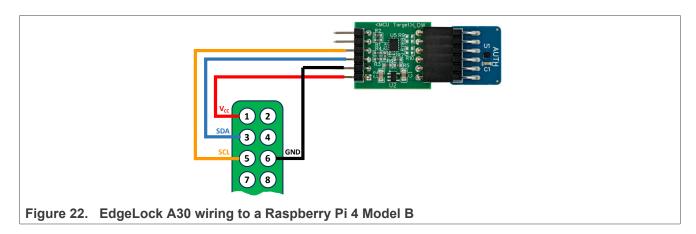


Table 4. EdgeLock A30 wiring to a Raspberry Pi 4 Model B board

Raspberry Pi 4 Model B (# jumper - # pin)	Level Shifter Board (# jumper - # pin)
J8-P5 (SCL)	SCL (HIGH <mcu)< td=""></mcu)<>
J8-P3 (SDA)	SDA (HIGH <mcu)< td=""></mcu)<>
J8-P6 (GND)	GND (HIGH <mcu)< td=""></mcu)<>
J8-P1 (3V3)	VCC (HIGH <mcu)< td=""></mcu)<>

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4.7 Device provisioning

The NX Middleware provides the following tools and examples for provisioning:

- NX Personalization Example
- NX-CLI Tool

4.7.1 NX personalization example

This example project demonstrates how to provision keys and certificates for the EdgeLock A30 Secure Authenticator, which are essential for enabling Sigma-I authentication.

Note: This demo does not modify the default symmetric keys. For production deployments, it is recommended to either provision symmetric keys or disable symmetric authentication entirely.

Symmetric key provisioning can be performed as shown in the <u>ex_update_key</u> and <u>diversify-key-perso</u> examples. For more information, refer to the online documentation.

4.7.2 NX-CLI Tool

The NX-CLI Tool is a command-line utility for evaluating the EdgeLock A30 Secure Authenticator in Windows and Linux environments. It supports the creation of scripts for provisioning and configuring the EdgeLock A30 device.

Key Features:

- Generate an EC key pair inside the EdgeLock A30 Secure Authenticator
 - The private EC key is stored securely inside the EdgeLock A30 Secure Authenticator
 - The public EC key is exported into a DER formatted file
- Generate reference keys and export them in PEM format
- Import private EC keys from PEM files into the EdgeLock A30 Secure Authenticator
- Manage the Sigma-I Certificate Repository (create, activate, reset)
- Insert X.509 certificates in PEM format into the EdgeLock A30 Secure Authenticator
- · Configure Sigma-I certificate repository access conditions
- Import Sigma-I host CA root keys from PEM files
- Generate random numbers within the EdgeLock A30 Secure Authenticator
- · Create, read and write standard data files
- Retrieve the EdgeLock A30 Secure Authenticator device UID
- · Configure the EdgeLock A30 I2C address

Note: During EC key pair generation, the private EC key is securely stored inside A30, ensuring it remains protected and inaccessible. The corresponding public EC key is returned to the host system. To optimize user NV memory usage, the A30 does not store the public EC key. Instead, the public key is typically embedded in an X.509 certificate. A30 supports to storing X.509 certificates inside standard data files.

Note: Standard OpenSSL and MbedTLS 3.0 APIs require a private key to perform cryptographic operations. Instead of using an actual private key, a reference key structure is provided that points to the private key securely stored inside the Secure Authenticator. This reference key is compatible with OpenSSL and ARM mbedTLS and behaves like a real key from the API's perspective, but it does not expose any secret material. This approach ensures secure key handling while maintaining compatibility with standard cryptographic libraries.

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The NX-CLI Tool supports the following commands:

Table 5. NX-CLI Tool commands

Description
Connect to Secure Authenticator
Disconnect from Secure Authenticator
Retrieve UID from Secure Authenticator
Generate ECC key in Secure Authenticator and export public key
Generate a reference key
Generate random numbers from Secure Authenticator
Import a private key into Secure Authenticator
Certificate repository management commands
Create a standard data file in Secure Authenticator.
Write data to a standard data file in Secure Authenticator
Read data from a standard data file in Secure Authenticator
List file IDs in Secure Authenticator
List EC keys and their properties in Secure Authenticator
Configure I2C settings
Configure certificate settings

For more details, refer to the NX-CLI Tool GitHub documentation.

The following example demonstrates how to retrieve the Secure Authenticator's device UID using the NX-CLI Tool in a Windows environment.

Note: This example assumes that the VCOM to T=1 over I^2C firmware is running on a supported NXP MCU development board. For more information on supported boards and setup, refer to Section 4.4. Pre-compiled binaries for both the VCOM to T=1 over I^2C firmware and the Windows version of the NX-CLI Tool are available on GitHub:

- Link to the pre-compiled VCOM to T=1 over I²C firmware binary:
 - nx vcom-T1oI2C-mcxa153.bin
 - nx vcom-T1oI2C-mcxn947.bin
 - nx_vcom-T1oI2C-lpc55s69.bin
- Link to the pre-compiled Windows NX-CLI-Tool binary: vcom_nxclitool.exe

Note: It is recommended to add the location of the <code>vcom_nxclitool.exe</code> binary to the Windows environment variable Path.

Use the following command to display the <code>vcom_nxclitool</code> built in help:

vcom nxclitool help

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```
Command Prompt
C:\> vcom_nxclitool -help
USAGE: nxclitool [COMMAND] [OPTIONS]
  Command Line Interface for SA
COMMAND:
  certrepo-activate
                                    Activates a certificate repository
                                    Creates certificate repository
  certrepo-create
                                    Loads certificates in repository
  certrepo-load-cert
  certrepo-load-key
                                    Loads keys in repository
  certrepo-load-mapping
                                    Loads mapping in repository
                                     Reads certificates from repository and store in a file
  certrepo-read-cert
  certrepo-read-metadata
                                    Reads metadata from repository
  certrepo-reset
                                    Resets certificate repository
                                    Stores connection properties for SA
  connect
                                    Creates a standard data file inside SA
  create-bin
  disconnect
                                    Deletes connection properties. Does not require additional options
                                    Get data from standard data file in SA
Generates ECC Key and stores the public key to a file
  getbin
  genkey
  get-ref-key
                                    Generates reference key from a public key and key ID
                                    Fetches the list and properties of EC keys inside SA Fetches the list of file IDs inside SA Get UID from SA. No additional options required
  list-eckey
  list-fileid
  get-uid
                                    Generate specified number of random bytes
  rand
  setbin
                                     Sets data to a standard data file inside SA
                                     Set a private key inside SA
  setkey
  i2c_mgnt
                                    Set config i2c management SA
For individual command help, enter the command with "-help" flag in the end EXAMPLE: nxclitool [COMMAND] -help
Figure 23. NX-CLI-Tool - help
```

To establish a connection, use the following command:

```
vcom_nxclitool connect -smcom vcom -port COMxx -auth symmetric ^
-sctunn ntag_aes128_ev2 -keyid 0x00
```

This command specifies:

- -smcom vcom: Host communication via VCOM
- -port COMxx: VCOM port number (replace COMxx with the actual port)
- -auth symmetric: Symmetric authentication method
- -sctunn ntag aes128 ev2: Secure channel/tunnel type
- -keyid 0x00: Authentication key number/slot

Once connected, retrieve the UID with:

```
vcom_nxclitool get-uid
```

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```
Command Prompt
C:\Users\atp01729>vcom_nxclitool connect -smcom vcom -port COM50 -auth symmetric -sctunn ntag_aes128_e
v2 -keyid 0x00
nx_mw :INFO :Connected to SA
C:\Users\atp01729> vcom_nxclitool get-uid
nx_mw :INFO :Using PCDCap from (EX_SYMM_AUTH_PCDCCAP2) from file - lib/sss/inc/fsl_sss_nx_auth_keys.h.
(You can use PCDCap2 from file by setting ENV=EX_SSS_BOOT_PCDCAP2_PATH to its path)
nx_mw :INFO :Using appkey (EX_SYMM_AUTH_AES*_KEY) from file - lib/sss/inc/fsl_sss_nx_auth_keys.h. (You
can use appkeys from file by setting ENV=EX_SSS_BOOT_APPKEY_PATH to its path)
Opening COM Port '\\.\COM50'
nx_mw :INFO :cip (Len=22)
     01 04 63 07
                   00 93 02 08
                                   00 02 03 E8
                                                  00 01 00 64
nx_mw :INFO :Session Open Succeed
nx_mw : INFO : Card UID (Len=7)
     04 27 27 AA
                   6E 20 90
nx_mw :INFO :Operation successful!!
C:\Users\atp01729>
```

Figure 24. NX-CLI-Tool - get-uid command

4.8 Device configuration

The NX Middleware provides command-line examples to set and retrieve the chip configuration. The configuration options are described in the DS9767xx EdgeLock A30 Secure Authenticator data sheet chapter 6.5.2 Card Configuration.

4.8.1 Get configuration

The NX Middleware example get configuration retrieves the chip configuration and print the information to the console. This example can be executed on all supported environments (Windows, Linux and MCUs).

The following example demonstrates how to get the configuration in a Windows environment. Instructions on how to build the example are provided in <u>Section 4.4.1</u>.

Note: This example assumes that the VCOM to T=1 over I²C firmware is running on a supported NXP MCU development board. For more information on supported boards and setup, refer to Section 4.4. Pre-compiled binaries for both the VCOM to T=1 over I²C firmware is available on GitHub.

To invoke the get configuration example (see also Section 4.4.2), use the following command:

```
ex_get_config.exe COMxx
```

This command specifies:

-port COMxx: VCOM port number (replace COMxx with the actual port)

An EdgeLock A30 chip fresh from the factory returns the following configuration.

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```
C:\nx mw root folder\nxmw build\se x86\bin\Debug>ex get config.exe COM50
nx mw :INFO :NX PKG v02.05.01 20250515
nx mw :INFO :Running ex get config.exe
nx_mw :INFO :Using PortName='COM50' (CLI)
nx mw :INFO :Using PCDCap from (EX SYMM AUTH PCDCCAP2) from file - lib/
sss/inc/fsl sss nx auth keys.h. (You can use PCDCap2 from file by setting
ENV=EX SSS BOOT PCDCAP2 PATH to its path)
nx_mw : INFO : Using appkey (EX SYMM AUTH AES* KEY) from file - lib/sss/
inc/fsl sss nx auth keys.h. (You can use appkeys from file by setting
ENV=EX_SSS_BOOT_APPKEY_PATH to its path)
Opening COM Port '\\.\COM50'
nx_mw :INFO :cip (Len=22)
     01 04 63 07 00 93 02 08 04 03 E8 00 FE 00
                               00 02 03 E8 00 01 00 64
nx mw :INFO :Session Open Succeed
nx mw :INFO :Running Get Configuration Example ex sss set config.c
nx mw :INFO :Get Manufacture Features successful !!!
nx mw : INFO : Support ECC-based Unilateral Authentication: Enabled
nx mw : INFO : Support Import of ECC Private Key: Enabled
nx mw :INFO : Enable Enforce User Data Files 1k Limit: Disabled
nx mw : INFO : Counter Support: Enabled
nx mw : INFO : EC DSA 4.0: Disabled
nx mw :INFO : CCM AES-256 Secure Channel: Disabled
nx mw :INFO : NTAG AES-256 Secure Channel: Enabled
nx mw : INFO : NTAG AES-128 Secure Channel: Enabled
nx mw : INFO : External Crypto API ECC Support: Enabled
nx mw : INFO : External Crypto API AES Support: Enabled
nx mw : INFO : GPIO Support: Enabled
nx mw : INFO : I2C IO Support: Enabled
nx mw : INFO : NFC IO Support: Enabled
nx mw :INFO :Get PICC Configurations successful !!!
nx mw :INFO : User RID (Random ISO ID): Disabled
nx mw :INFO :========
nx mw :INFO :Get ATS Update Configurations successful !!!
nx mw : INFO : User defined ATS: (Len=6)
    06 78 77 71 02 80
nx mw : INFO : Get SAK Update Configurations successful !!!
nx mw : INFO : User defined User defined SAK1: 0x4
nx mw :INFO : User defined User defined SAK2: 0x20
nx mw : INFO :Get Secure messaging configuration successful !!!
nx mw : INFO : Chained writing: Enabled
nx mw :INFO :Get Capability Data successful !!!
nx mw :INFO : User configured PDCap2.5: 0x0
nx mw :INFO : User configured PDCap2.6: 0x0
nx mw :INFO :========================
nx mw :INFO :Get ATQA Update successful !!!
nx mw : INFO : User defined ATQA: (Len=2)
     44 03
nx mw : INFO : Get Silent Mode Configuration successful !!!
nx mw :INFO : Customized REQS/WUPS: Disabled
nx mw :INFO : Silent mode: Disabled
nx mw :INFO :============
nx mw : INFO :Get Enhanced Privacy Configuration successful !!!
```

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```
nx mw : INFO : Originality Check: Enabled
nx mw : INFO : Manufacturer data masking: Disabled
nx mw :INFO : KeyID.AppPrivacyKey: Disabled
nx mw :INFO : KeyID.AppPrivacyKey definition: 0x0
nx mw :INFO :============
nx mw : INFO :Get NFC Management Configuration successful !!!
nx mw : INFO : NFC I/O: Disabled
nx mw :INFO : SIGMA-I Verifier: Enabled
nx mw : INFO : SIGMA-I Prover: Enabled
nx mw : INFO : NTAG EV2 secure messaging: Enabled
nx mw : INFO : Secure Tunnel strength: AES-256 supported
nx mw : INFO : Secure Tunnel strength: AES-128 supported
nx mw : INFO : ECC-based Card-Unilateral Authentication: Enabled
nx mw : INFO : AES-based Symmetric Authentication: Enabled
nx mw :INFO :==========
nx mw : INFO : Get I2C Management Configuration successful !!!
nx mw :INFO : I2C I/O: Enabled
nx mw :INFO : SIGMA-I Verifier: Enabled
nx mw :INFO : SIGMA-I Prover: Enabled
nx mw : INFO : NTAG EV2 secure messaging: Enabled
nx mw : INFO : Secure Tunnel strength: AES-256 supported
nx mw :INFO : Secure Tunnel strength: AES-128 supported
nx mw :INFO : ECC-based Card-Unilateral Authentication: Enabled
nx mw : INFO : AES-based Symmetric Authentication: Enabled
nx mw :INFO :===========
nx mw :INFO :Get GPIO Management Configuration successful !!!
nx mw : INFO : GPIO 1 Mode: Disabled
nx mw :INFO : GPIO 1 supply selection: 1V1 and 1V2 signaling in I2c mode
nx mw :INFO : GPIO 2 Mode: Disabled
nx mw :INFO : GPIO 2 supply selection: 1V1 and 1V2 signaling in I2c mode
nx mw : INFO : Cmd.ManageGPIO communication modes: No protection.
nx mw :INFO : Cmd.ManageGPIO access condition 0xf
nx mw : INFO : Cmd.ReadGPIO communication modes: No protection.
nx mw :INFO : Cmd.ReadGPIO access condition 0xf
nx mw :INFO :Get ECC Key Management Configuration successful !!!
nx mw :INFO : Cmd.ManageKeyPair communication modes: Full protection.
nx mw :INFO : Cmd.ManageKeyPair access condition 0x0
nx mw :INFO : Cmd.ManageCARootKey communication modes: Full protection.
nx mw :INFO : Cmd.ManageCARootKey access condition 0x30
nx mw :INFO :============
nx mw :INFO :Get Certificate Management Configuration successful !!!
nx mw :INFO : End Leaf certificate cache size: 0x5
nx mw :INFO : Intermediate certificate cache size: 0x4
nx mw :INFO : SIGMA-I Cache: Disabled
nx mw :INFO : Cmd.ManageCertRepo communication modes: MAC protection.
nx mw :INFO : Cmd.ManageCertRepo access condition 0x0
nx mw :INFO :Get Watchdog Timer Management Configuration successful !!!
nx mw :INFO : Halt Watchdog Timer (HWDT) Value: 0x0
nx mw :INFO : Authorization Watch-Dog Timer (AWDT1) Value: 0x0
nx mw : INFO : Authorization Watch-Dog Timer (AWDT2) Value: 0x0
nx mw :INFO :Get Crypto API Management Configuration successful !!!
nx mw : INFO : Asymmetric Crypto API: Enabled
nx mw :INFO : Symmetric Crypto API: Enabled
nx mw : INFO : Cmd.CryptoRequest communication modes: MAC protection.
nx mw : INFO : Cmd.CryptoRequest access condition 0x0
nx mw :INFO : Cmd.CryptoRequest access condition for ChangeKey command targeting
CryptoRequest Keys 0x0
```

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```
nx mw :INFO :Crypto API TB Policy: (Len=24)
     00 00 00 01
                  00 00 02 00
                                00 03 00 00
                                              04 00 00 05
     00 00 06 00
                  00 07 00 00
nx mw :INFO :Crypto API SB Policy: (Len=42)
     00 00 00 01 00 00 02 00
                                00 03 00 00
                                              04 00 00 05
                  00 07 00 00
     00 00 06 00
                                08 00 00 09
                                              00 00 0A 00
     00 OB 00 00
                 OC 00 00 0D
                               00 00
nx mw :INFO :Get Lock Configuration successful !!!
nx mw : INFO : Lock bitmap: 0
nx_mw :INFO :ex_sss_get_config Example Success !!!...
nx mw : INFO : ex sss Finished
```

Note: Information related to an ISO/IEC14443 contactless interface is not relevant for the EdgeLock A30 and can be ignored:

- ATQA
- ATS
- · Capability Data
- NFC IO Support
- NFC Management Configuration
- SAK
- Silent Mode Configuration

4.8.2 Set configuration

The $nx_tool_setconfig$ can be used to configure the EdgeLock A30 Secure Authenticator in a Windows or Linux environment.

Supported Configuration Options:

- gpio1mode: Set GPIO1 to disabled, input, output, input tag tamper or down-stream power out
- gpio2mode: Set GPIO2 to disabled, input or output
- gpio1Notif: Set GPIO1 notification on authentication. It can be disabled, enabled for authentication or enabled for presence of NFC field
- gpio2Notif: Set GPIO2 notification on authentication. It can be disabled, enabled for authentication or enabled for presence of NFC field
- gpioMgmtCM: Set ManageGPIO communication mode
- gpioReadCM: Set ReadGPIO communication mode
- gpioMgmtAC: Set ManageGPIO access condition
- gpioReadAC: Set ReadGPIO access condition
- cryptoCM: Set Crypto API communication mode
- · cryptoAC: Set Crypto API access condition
- keypairCM: Set ManageKeyPair communication mode
- keypairAC: Set ManageKeyPair access condition
- caRootKeyCM: Set ManageCARootKey communication mode
- caRootKeyAC: Set ManageCARootKey access condition

Please refer to the documentation on GitHub for detailed descriptions and usage guidelines.

The following example illustrates how to configure GPIO1 to operate in output mode using a Windows environment.

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```
nx_tool_setconfig -gpio1mode output COMxx
```

This command specifies:

-port COMxx: VCOM port number (replace COMxx with the actual port)

```
Microsoft Windows [Version 10.0.22631.5768]
(c) Microsoft Corporation. All rights reserved.

C:\Mw\nx_mw_root_folder\nxmw_build\se_x86\bin\Debug>lscom
USB Serial Device (COMB0)

C:\Mw\nx_mw_root_folder\nxmw_build\se_x86\bin\Debug> nx_tool_setconfig -gpiolmode output COM50

nx_mw :INFO :Nx_PRG_v02.05.01_20250515

nx_mw :INFO :Nx_mm_g PCCbag from (CLI)

nx_mw :INFO :Ising PortName='COMS0' (CLI)

nx_mw :INFO :Ising PCDag from file by setting ENV=EX_SSS_BOOT_PCDCAP2_PATH to its path)

nx_mw :INFO :Ising appley (EX_SYMM_AUTH_PCDCCAP2) from file - lib/sss/inc/fsl_sss_nx_auth_ke

ys.h. (You can use appleys from file by setting ENV=EX_SSS_BOOT_PCDCAP2_PATH to its path)

Nx_mw :INFO :Ising appley (EX_SYMM_AUTH_SEX_MEX) from file - lib/sms/infsl_sss_nx_auth_keys.h.

(You can use appleys from file by setting ENV=EX_SSS_BOOT_APPREY_PATH to its path)

Opening COM Port '\\.COMSO'S

nx_mw :INFO :ising of the property of the property of the path of the pat
```

Note: The <u>Set Configuration Example</u> is an alternative method that demonstrates how to perform configuration operations using the NX APIs. This example can also be executed on MCUs.

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5 EdgeLock 2Go service for EdgeLock A30

5.1 Overview

EdgeLock A30 products are delivered with an NXP trust-provisioned device unique application private EC key and application X.509 certificate containing the corresponding public EC key. The EdgeLock A30 application credentials can be used for Sigma-I Authentication or ECDSA signature generation.

To simplify the OEMs products process and reduce the production cost the EdgeLock A30 device UID and the application X.509 certificate can be downloaded via the EdgeLock 2Go service. This eliminates the need for OEMs to read the credential from each individual EdgeLock A30 device.

EdgeLock A30 reels are shipped with a label containing an access code (reference code and authorization key).



To download the EdgeLock A30 UIDs and application certificates can be simple done by the following steps:

- Go to https://www.edgelock2go.com/downloads and enter the reference code and authorization key.
- After successful registration, Device UIDs and application X.509 certificates become available for download. If wafer delivery is selected, the corresponding Wafer Map can also be retrieved.

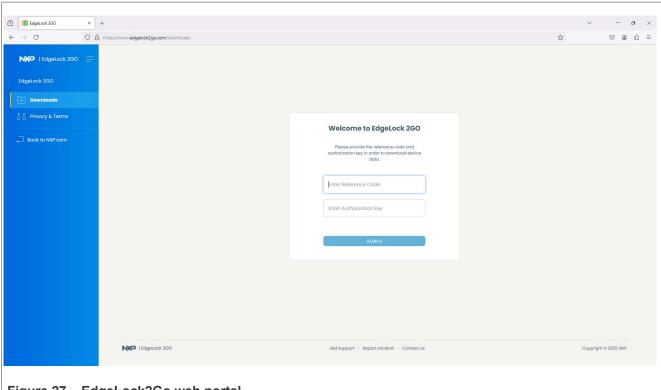


Figure 27. EdgeLock2Go web portal



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6 Supported EdgeLock A30 documentation

Table 6 summarizes the EdgeLock A30 dedicated documents.

Note: Click on the hyperlink to download the document.

Table 6. Dedicated EdgeLock A30 documentation

Documentation number	Title
DS9767xx	EdgeLock A30 Secure Authenticator data sheet
AD9772xx	Edgelock A30 Delivery specification
AN14559	Migration Guide from EdgeLock A5000 to EdgeLock A30
AN14123	NX Middleware Documentation
Global Platform	GlobalPlatform Technology- APDU Transport over SPI / I ² C Version 1.0, January 2020
<u>UM10204</u>	I ² C-bus specification and user manual

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7 Appendix

7.1 EdgeLock A30 product identification

The EdgeLock A30 product identification can be retrieved by sending the APDU GetVersion command. The GetVersion response contains the major/minor version identifiers of the IC hardware and software required for EdgeLock A30 product identification. A detailed description of the GetVersion response can be found in the DS9767xx EdgeLock A30 Secure Authenticator data sheet. The exact EdgeLock A30 product identification is covered by the following two parameters:

- Hardware Major/Minor Version value: 0xA0 0x00
- Software Major/Minor Version value: 0x00 0x01

The NX Middleware includes an example for running GetVersion on all supported platforms (Windows, Linux and MCUs). Please refer to Section 4.4.1 Building NX Middleware on Windows and Section 4.4.2 Running NX Middleware examples from command line for a detailed description how compile and run an example on a Windows environment.

To invoke the GetVersion example, use the following command:

```
ex_get_version COMxx
```

This command specifies:

-port COMxx: VCOM port number (replace COMxx with the actual port)

```
::\MW\nx_mw_root_folder\nxmw_build\se_x86\bin\Debug>ex_get_version COM54
       :INFO :Using PCDCap from (EX_SYMM_AUTH_PCDCCAP2) from file - lib/sss/inc/fsl_sss_nx_auth_keys.h. (You can use PCDCap2 from
 file by setting ENV=EX_SSS_BOOT_PCDCAP2_PATH to its path)

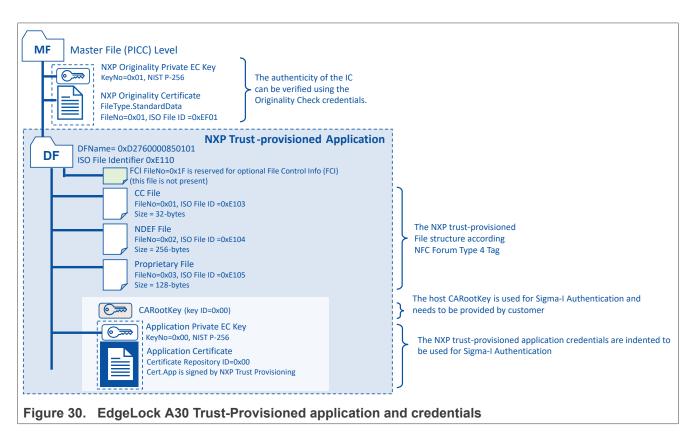
x_mw :INFO :Using appkey (EX_SYMM_AUTH_AES*_KEY) from file - lib/sss/inc/fsl_sss_nx_auth_keys.h. (You can use appkeys from file
     setting ENV=EX
Opening COM Port '\\.\COM54'
       01 04 63 07 00 93
04 03 E8 00 FE 00
                                                00 02 03 E8 00 01 00 64
nx_mw :INFO :Running Get Card Version Example ex_sss_get_version.c
nx_mw :INFO :Get Card Version
nx_mw :INFO :HW type: 0x0A (IoT)
nx_mw :INFO :HW subtype: 0x41 (17 pF, Tag Tamper)
                :HW storage size: 0x1C (16 kB)
nx_mw :INFO :HW protocol type: 0x35 (I2C and ISO/IEC 14443-4 support with Silent Mode support)
nx_mw :INFO :SW Vendor ID: 0x04 (NXP Semiconductors)
nx_mw :INFO :SW type: 0x0A (A30)
nx_mw :INFO <u>:SW subtype: 0x01 (Standa</u>lone)
x_mw :INFO
nx_mw :INFO :SW storage size: 0x1C (16 kB)
nx_mw :INFO :SW protocol type: 0x35 (I2C and ISO/IEC 14443-4 support with Silent Mode support)
       04 27 41 AA 6E 20 90
:INFO :BatchNo: 0x672321
nx_mw :INFO :FabKey identifier: 0x5234
nx_mw :INFO :Calendar week of card production in BCD coding: 0x09
        :INFO :ex_sss_get_version Example Success !!!..
:INFO :ex_sss Finished
```

Figure 29. EdgeLock A30 product identification using GetVersion

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7.2 Available free memory of EdgeLock A30 ICs

EdgeLock A30 ICs are trust-provisioned during manufacturing.



The default trust-provisioning includes the following components:

- ECC-Based Key Pair and Certificate at the Master File Level
 - Each device is provisioned with an ECC key pair and a corresponding certificate, enabling verification of the IC's authenticity. This is achieved through a card-unilateral authentication mechanism using a challenge-response protocol. To mitigate privacy concerns—since the protocol may leave a non-repudiable trace—the same key pair is shared across all ICs within a production batch. Additionally, this feature can be disabled if required. The NX Middleware provides the Originality Check example to verify this feature.
 - The NXP Originality Certificate is signed by NXP Trust Provisioning using a dedicated CA key pair for this
 product. The corresponding Root Certificate can be retrieved from https://www.gp-ca.nxp.com/CA/getCA?caid=63709320110003
- Pre-Configured Application with NFC Forum Type 4 Tag File Structure
 - The device includes a pre-installed application structured according to the NFC Forum Type 4 Tag specification.
 - An Application Key Pair and its associated certificate, which support EC-based Mutual Authentication using the Sigma-I protocol.
 - The option to modify the default KeyPolicy, allowing the Application Key Pair to be used for other supported elliptic curve operations.
 - The ability to replace the provisioned Application Certificate, if needed.
 - The NXP Application Certificate is signed by NXP Trust Provisioning using a dedicated CA key pair.
 The corrsponding Root Certificate can be retrieved from https://www.gp-ca.nxp.com/CA/getCA?
 caid=63709320101003

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Note: NXP provides commercial customization options for trust-provisioning. This allows for a customer-dedicated delivery configuration. This may include the provisioning of a customer-specific CARootKey. This allows to do the initial personalization with the ECC-based SIGMA-I authentication. By this, the need for a secure environment can be removed, compared to when doing the initial personalization based on the default AES keys. Additionally, also customer-specific AES keys, certificates and/or a customized file system and configuration can be provisioned. Reach out to your local sales representative for more information.

Note: For more details, please refer to EdgeLock A30 Secure Authenticator data sheet <u>DS9767xx</u>.

The trust-provisioned application and credentials consumes **2,880 bytes** of the total **16,384 bytes** of user memory available on the IC.

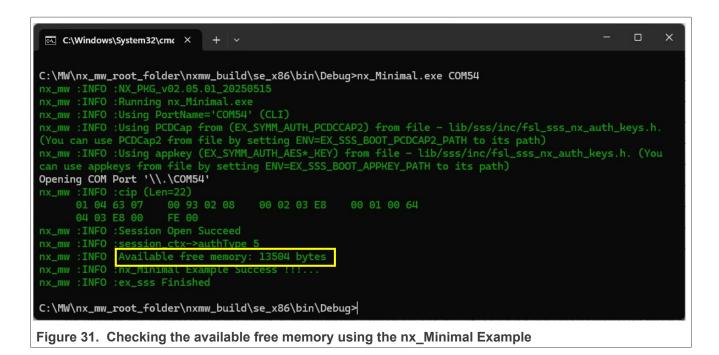
Thus, the available free memory for user-defined data and applications is: 16,384 bytes – 2,880 bytes = 13,504 bytes

Table 7. Trust-Provisioned device - memory consumption

Component	Memory size	Remark
File info headers for 3 NDEF files	64 B	NDEF File System
Additional file info header for Secure Dynamic Messaging (SMD)	32 B	NDEF File System
CC File (FID 0xE103)	32 B	NDEF File System
NDEF File (FID 0xE104)	256 B	NDEF File System
Proprietary File (FID 0xE105)	128 B	NDEF File System
Host CA Root Key (Reserved Memory)	128 B	Required for Sigma-I Authentication
Application Private EC Key	96 B	Required for Sigma-I Authentication
Application Certificate (Certificate Repository Size)	2144 B	Required for Sigma-I Authentication

The available free memory of a device can be verified using the example NX Minimal. Please refer to Section 4.4.1 Building NX Middleware on Windows and Section 4.4.2 Running NX Middleware examples from command line for a detailed description how compile and run an example on a Windows environment.

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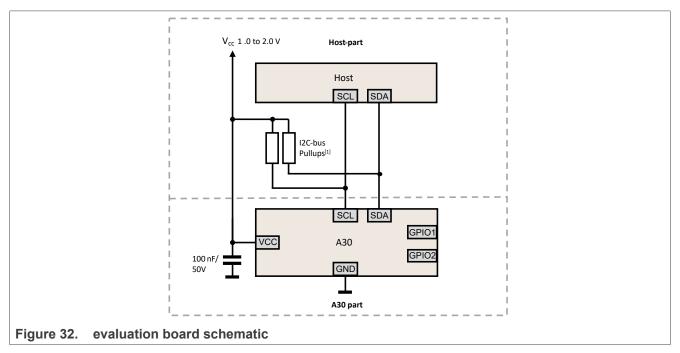


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7.3 EdgeLock A30 application circuit diagram

Figure 32 shows an application circuit diagram with the following design considerations:

- Power-On-Reset
 - It is recommended that the hostcontroller can perform a Power-On-Reset by controlling V_{CC}.
 - It is possible to supply A30 via the MCU/MPU GPIO. The GPIO shall be able to deliver current up to 15 mA.
- A30 triggers a Reset via T=1' over I²C protocol
 - Using a proprietary NXP S-Blocks chip reset request/response.
- A30 enables Deep Power Down via T=1' over I²C protocol
 - Using a proprietary NXP S-Block Deep Power Down request/response.



Note: [1] .. According <u>UM10204</u> I²C-bus specification and user manual Rev 7, chapter 7.1 Pull-up resistor sizing

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7.4 EdgeLock A30 WLCSP16 pin description

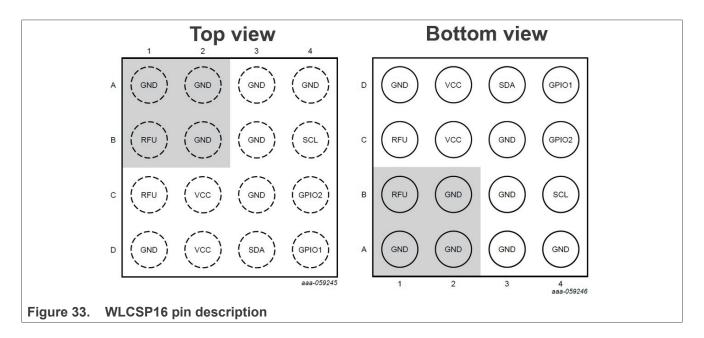


Table 8. WLCSP16 pin description

Pin	Symbol	Description
A1	GND	Ground
A2	GND	Ground
А3	GND	Ground
A4	GND	Ground
B1	r.f.u.	Connect to Ground
B2	GND	Ground
В3	GND	Ground
B4	SCL	SCL - I2C Target Clock Input
C1	r.f.u.	Connect to Ground
C2	VCC	Logic and I2C/GPIO power supply voltage input
C3	GND	Ground
C4	GPIO2	General Purpose IO
D1	GND	Ground
D2	VCC	Logic and I2C/GPIO power supply voltage input
D3	SDA	SDA - I2C Target Data
D4	GPIO1	General Purpose IO

Note: A 100nF decoupling capacitor must be located as close as possible between pin D1 and D2.

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7.5 EdgeLock A30 HVQFN20 pin description

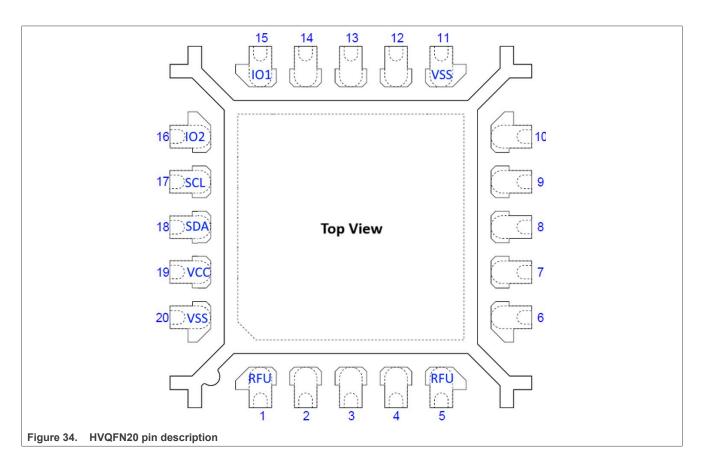


Table 9. HVQFN20 pin description

Pin	Symbo	Description
1	r.f.u.	Connect to V _{SS}
5	r.f.u.	Connect to V _{SS}
11	VSS	Ground
15	GPIO1	General Purpose IO
16	GPIO2	General Purpose IO
17	SCL	SCL - I2C Target Clock Input
18	SDA	SDA - I2C Target Data
19	VCC	Logic and I2C/GPIO power supply voltage input
20	VSS	Ground

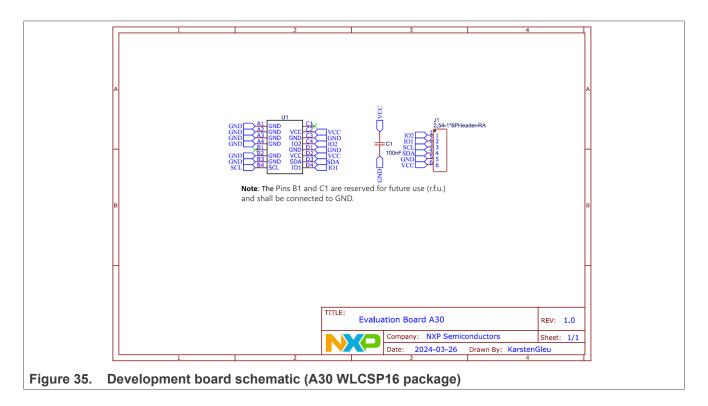
Note: All other pins are not connected. Not connected pins can either be left open or grounded in the application.

Note: The HVQFN20 package exposed center pad can be connected to GND.

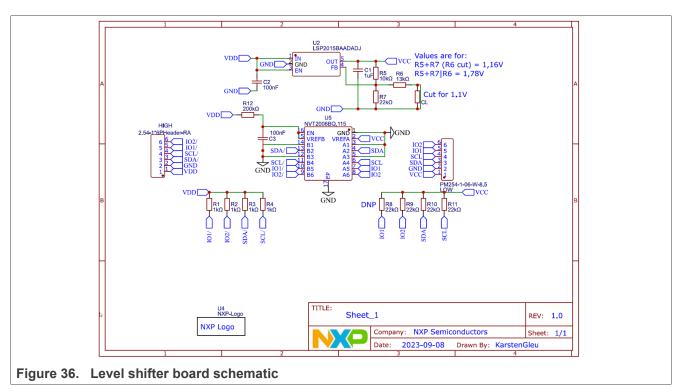
Note: A 100nF decoupling capacitor must be located as close as possible between pin 19 and 20.

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7.6 EdgeLock A30 development board schematic



7.7 Level shifter board schematic



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8 Abbreviations

Table 10. Abbreviations

Acronym	Description
AID	Application IDentifier
APDU	Application Protocol Data Unit
CA	Certificate Authority
C-APDU	Command APDU
CMAC	MAC according to NIST Special Publication 800-38B
CRC	Cyclic Redundancy Check
DF-Name	ISO7816 Dedicated filename
ECC	Eliptic Curve Cryptography
GUI	Graphical User Interface
IC	Integrated Circuit
IDE	Integrated Development Environment
MCU	Microcontroller Unit
MPU	Microprocessor Unit
NDEF	NFC Data Exchange Format
NFC	Near Field Communication
r.f.u.	Reserved for Future Use
R-APDU	Response APDU
SCL	Serial Clock
SDA	Serial Data
T=1' over I ² C	GlobalPlatform Technology- APDU Transport over SPI / I ² C Version 1.0, January 2020
SCL	Serial Data
UID	Unique Identifier
VCC	Voltage Common Collector
VCOM	VCOM
VSS	Voltage Source Supply (Ground)

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10 Revision history

Table 11. Revision history

Document ID	Release date	Description
AN14238 v.2.0	2 September 2025	Editorial changes (typos, etc.). Document title changed to "Getting started with EdgeLock A30 secure authenticator support package". • Section 1 "About EdgeLock A30 secure authenticator": updated. • Section 2 "EdgeLock A30 development kit": updated. • Section 3 "MCU/MPU reference boards": updated. • Section 4 "NX Middleware": updated. • Section 4.2 "Architecture overview": updated. • Section 4.3 "Code documentation ": updated. • Section 4.4 "Evaluation using a Windows PC": updated. • Section 4.4.1 "Building NX Middleware on Windows": added. • Section 4.4.2 "Running NX Middleware examples from command line": added. • Section 4.4.3 "Running NX Middleware example projects in Visual Studio": added. • Section 4.5 "Evaluation using a MCU board": updated. • Section 4.5 "Evaluation using Raspberry Pi": updated. • Section 4.6 "Evaluation using Raspberry Pi": updated. • Section 4.7 "Device provisioning": added. • Section 5 "EdgeLock 2Go service for EdgeLock A30": updated. • Section 6 "Supported EdgeLock A30 documentation": updated. • Section 7.1 "EdgeLock A30 product identification": added. • Section 7.2 "Available free memory of EdgeLock A30 ICs": added. • Section 7.5 "EdgeLock A30 HVQFN20 pin description": added. • Section 7.5 "EdgeLock A30 HVQFN20 pin description": added. • Section 8 "Abbreviations": added.
AN14238 v.1.0	15 January 2025	Initial version.

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