EdgeLockTM SE05x for secure access control in Industrial IoTRev. 1.2 — 7 December 2020Application note565712565712

#### Document information

Information	Content
Keywords	EdgeLock SE05x, Industrial access control, MIFARE® DESFire® EV2
Abstract	This application note describes how EdgeLock SE05x, in combination with a microcontroller, supports secure access control in any industrial operation or environment. It gives insights into the integration of EdgeLock SE05x from a hardware and software perspective for this use case. It also provides detailed instructions to run a set of code examples that demonstrate how to leverage EdgeLock SE05x and LPC55S to support secure operation with a MIFARE DESFire EV2 card. In this case, the LPC55SS is used as an example and the same concept is applicable using another host MCU.



# **Revision history**

Revision history		
Revision number	Date	Description
1.0	2019-11-25	First document release
1.1	2019-11-27	Corrected USB port in Figure 11
1.2	2020-12-07	Updated to latest template and fixed broken URLs

## 1 EdgeLock SE05x for secure access control in industrial IoT use cases

In any industrial operation or environment there is equipment and restricted areas that need to be secured, for safety reasons as well as to protect key equipment from damage or loss. IoT opens new opportunities for designing advanced access control solutions for machines, production lines, and other types of valuable items in manufacturing and industrial settings.

Many industrial operations have supply cabinets, equipment lockers or data centers that must be protected. Electronic locks can restrict access to these areas to individuals who are authorized to enter or withdraw items from these secure locations. In addition, electronic access control solutions can ensure that only authorized personnel are allowed to operate potentially dangerous machinery or secure areas that are only accessed by trained, certified employees.

State-of-the-art electronic access control solutions use contactless cards or NFCenabled smarpthones as the standard means of authentication and validation of user credentials. The use of contactless cards to provide physical or logical access to facilities or equipment are extremely secure and much easier to manage and update than conventional mechanical keys. As such, they can provide a better and more simplified means of controlling and monitoring access to devices, systems, and equipment.

In this context, the EdgeLock SE05x can be used by a card reader to setup a secure transaction with MIFARE DESFire EV2 contactless cards. On the one hand, the EdgeLock SE05x stores the MIFARE secret key, authenticates the MIFARE DESFire EV2 and exports the MIFARE session key to the host MCU. In turn, the host MCU implements the MIFARE application logic and handles the MIFARE DESFire EV2 command set and secure messaging. On the other hand, MIFARE DESFire EV2 contains a security system with tamper-resistant properties and is capable of providing confidentiality, authenticity and integrity security services. The user credential is protected in this contactless IC based on the configured access rights and the knowledge of the secret keys that fulfill these access rights.

As such, EdgeLock SE05x helps you to provide a higher level of security in your access control system by:

- **Protecting the master keys**: The master keys used for card authentication are protected inside the EdgeLock SE05x and can not be read or manipulated.
- Authenticating the card: EdgeLock SE05x supports the authentication protocol and the session key generation algorithm of MIFARE DESFire EV2 card.
- **Performing securely related commands**: EdgeLock SE05x supports secure key change or key diversification of MIFARE DESFire EV2 cards.

## 2 EdgeLock SE05x application diagram

The EdgeLock SE05x works as a secure access module to increase the security of your IoT-enabled card reader for physical or logical access. The EdgeLock SE05x connection to the main host MCU is done using the chip's  $I^2C$  interface. The host MCU is also connected to a contactless transceiver, used to communicate with the user contactless credential. Optionally, the host MCU can also interface with a backend system and with user interface components such as keypads, LEDs, LCD screens or others as described in Figure 1:



The OM-SE050ARD is an ideal development kit to evaluate the EdgeLock SE05x features, build a proof of concept or prototype our IoT-enabled card reader solution before going into production. It comes with headers and connectors that allow us to access the EdgeLock SE05x interfaces, including the I<sup>2</sup>C slave lines to connect a host MCU board. Check <u>Section 4</u> for more details about how the OM-SE050ARD is used in the EdgeLock SE05x secure access demo.

## 3 Software integration with EdgeLock SE05x Plug & Trust middleware

The host MCU implements the application logic and drives the operation of the IoTenabled card reader. In this respect, the EdgeLock SE05x acts as a companion security IC to setup a secure transaction with a user credential stored in MIFARE DESFire EV2 contactless card. The host MCU uses the EdgeLock SE05x to authenticate the MIFARE DESFire EV2 credential and to derive the session key for each transaction. Then, this session key is exported to the host MCU and used to handle the standard MIFARE command set as part of the MCU application logic as depicted in Figure 2.



The EdgeLock SE05x Plug & Trust Middleware is a single software stack designed to facilitate the integration of EdgeLock SE05x into your host MCU software. This middleware has built-in cryptographic and device identity features, abstracts the commands and communication interface exposed by the EdgeLock SE05x, and it is directly accessible from stacks like OpenSSL, mbedTLS or other cryptographic libraries. It also comes with support for various NXP MCU / MPU platforms and can be ported to multiple host platforms and host operating systems. Figure 3 is a simplified representation of the layers and components which EdgeLock SE05x Plug & Trust Middleware is made of:



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This section gives insights into the integration of EdgeLock SE05x from a software perspective. It includes an overview of the useful functions included in the EdgeLock SE05x Plug & Trust middleware for secure operation with MIFARE DESFire EV2 cards.

### 3.1 EdgeLock SE05x Plug & Trust middleware MIFARE DESFire EV2 API

The EdgeLock SE05x Plug & Trust Middleware provides functions that support MIFARE DESFire EV2 card authentication, session key generation and key personalization. Refer to MIFARE DESFire EV2 datasheet (ds226031) for more details about MIFARE DESFire EV2 commands.

#### 3.1.1 Se05x\_API\_DFAuthenticateFirstPart1()

Performs the first part of the AuthenticateEV2First three-pass mutual authentication between MIFARE DESFire EV2 and the IoT-enabled card reader. Calling this function, the EdgeLock SE05x takes care of:

- Deciphering the random number (RndB) generated by MIFARE DESFire EV2 with the selected key.
- Rotating the first byte of RndB to the end (RndB').
- Concatenating RndB' with its own generated random number (RndA + RndB') and returning them encrypted to the host MCU with the selected key

#### 3.1.2 Se05x\_API\_DFAuthenticateFirstPart2()

Performs the second part of the AuthenticateEV2First three-pass mutual authentication between MIFARE DESFire EV2 and the IoT-enabled card reader. Calling this function, the EdgeLock SE05x takes care of:

- Deciphering the RndA' received from the MIFARE DESFire EV2.
- Comparing it with the RndA generated internally. If the comparison is successful, the authentication is successful.

#### 3.1.3 Se05x\_API\_DFAuthenticateNonFirstPart1()

Performs the first part of the AuthenticateEV2NonFirst three-pass mutual authentication between MIFARE DESFire EV2 and the IoT-enabled card reader. Calling this function, the EdgeLock SE05x takes care of::

- Deciphering the random number (RndB) generated by MIFARE DESFire EV2 with the selected key.
- Rotating the first byte of RndB to the end (RndB').
- Concatenating RndB' with its own generated random number (RndA + RndB') and returning them encrypted to the host MCU with the selected key

#### 3.1.4 Se05x\_API\_DFAuthenticateNonFirstPart2()

Performs the second part of the AuthenticateEV2NonFirst three-pass mutual authentication between MIFARE DESFire EV2 and the IoT-enabled card reader. Calling this function, the EdgeLock SE05x takes care of:

- Deciphering the RndA' received from the MIFARE DESFire EV2.
- Comparing it with the RndA generated internally. If the comparison is successful, the authentication is successful.

#### 3.1.5 Se05x API DFDumpSessionKeys

Returns the transaction identifier (TI) and the session key of the current active authentication with the MIFARE DESFire EV2 to the host MCU.

#### 3.1.6 Se05x\_API\_DFChangeKeyPart1()

Performs the first part of the ChangeKey command between MIFARE DESFire EV2 and the IoT-enabled card reader. Calling this function, the EdgeLock SE05x takes care of generating the cryptogram required to update a MIFARE DESFire EV2 key

#### 3.1.7 Se05x\_API\_DFChangeKeyPart2

Performs the second part of the ChangeKey command between MIFARE DESFire EV2 and the IoT-enabled card reader. Calling this function, the EdgeLock SE05x takes care of verifying the MAC returned by theMIFARE DESFire EV2 after the ChangeKey command.

#### 3.1.8 Se05x API DFDiversifyKey()

The EdgeLock SE05x creates a diversified key using a diversification input up to 31 bytes. Refer to <u>AN10922</u> for more details about the AES128 key diversification algorithm used by EdgeLock SE05x

### 3.1.9 Se05x API DFKillAuthentication

The EdgeLock SE05x invalidates any active authentication with a MIFARE DESFire EV2 and clear the EdgeLock SE05x internal state.

# 3.2 EdgeLock SE05x Plug & Trust middleware MIFARE DESFire EV2 API documentation

You can refer to the code documentation provided as part of the EdgeLock SE05x middleware for full details about the MIFARE DESFire EV2 support. To open the HTML documentation:

- 1. Go to the directory where you unzipped the EdgeLock SE05x Plug & Trust middleware;
- 2. Go to the simw-top\doc folder;
- 3. Double click the index.html to open it in your default browser;

4. The documentation landing page will appear as shown in <u>Figure 4</u>. Use the left-hand side menu to navigate through the documentation or use the search textbox to find the functions described in <u>Section 3.1</u>.



If you are interested in the low level commands for secure operation with MIFARE DESFire EV2, you can refer to <u>AN12412 - SE050 APDU specification document</u>.

## 4 Running the EdgeLock SE05x secure access module project examples

The EdgeLock SE05x Plug & Trust Middleware includes a set of project examples that demonstrate the use of EdgeLock SE05x to support secure operation with a MIFARE DESFire EV2 card. These project examples are:

- ex\_prepare\_se05x
- ex\_prepare\_MFDFEV2
- ex\_Ev2Auth\_se05x
- ex\_Ev2ChangeKey\_se05x
- ex\_Ev2DivChngKey\_se05x

The ex\_prepare\_se05x project example provisions the EdgeLock SE05x with sample keys and credentials. Executing this project example is a pre-requisite before running ex\_Ev2Auth\_se05x, ex\_Ev2ChangeKey\_se05x or ex\_Ev2DivChngKey\_se05x project examples.

The <code>ex\_prepare\_MFDFEV2</code> project example personalizes the MIFARE DESFire EV2 card with a sample application and keys. Executing this project example is a pre-requisite before running <code>ex\_Ev2Auth\_se05x</code>, <code>ex\_Ev2ChangeKey\_se05x</code> or <code>ex\_Ev2DivChngKey\_se05x</code> project examples.

The <code>ex\_Ev2Auth\_se05x</code> project example demonstrates how to authenticate and perform a sample secure transaction with a MIFARE DESFire EV2 leveraging the keys stored in the EdgeLock SE05x.

The ex\_Ev2ChangeKey\_se05x project example demonstrates how to change a MIFARE DESFire EV2 application key leveraging the keys stored in the EdgeLock SE05x

And, the <code>ex\_Ev2DivChngKey\_se05x</code> project example demonstrates how to change and diversify a MIFARE DESFire EV2 application key leveraging the keys stored in the EdgeLock SE05x

Figure 5 depicts the setup to run the EdgeLock SE05x secure access project examples. It consists of an LPC55S board (LPC55S69), a CLRC663 development board (CLEV6630B), an EdgeLock SE05x board (OM-SE050ARD) and a MIFARE DESFire EV2 acting as the user credential.

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The steps required to run the project examples that use EdgeLock SE05x as a secure access module to operate with MIFARE DESFire EV2 cards are:

- 1. Get the required hardware.
- 2. Download and install the NDA-protected LPC55S69 SDK version.
- 3. Prepare the boards setup and wiring.
- 4. Run the different project examples.

### 4.1 Hardware required

The hardware required to run the project examples related with the EdgeLock SE05x secure access demo is:

1. OM-SE050ARD development kit:

Table 1. OM-SE050ARD development kit details

Part number	12NC	Content	Picture
OM-SE050ARD	935383282598	EdgeLock SE050 development board	

2. LPC55S69 evaluation kit

Table 2. LPC55S69 evaluation kit details

Part number	12NC	Content	Picture
<u>LPC55S69-EVK</u>	935377412598	LPCXpresso55S69 Development Board	

3. CLEV6630B development board

### Table 3. CLEV6630B

Part number	12NC	Content	Picture
<u>CLEV6630B</u>	935339149699	CLRC663 Frontend Development Board	

4. MIFARE DESFire EV2 card. You can order samples free of charge by getting in touch with your local sales representative

### 4.2 Download and install the NDA-protected LPC55S69 SDK version

The project examples for the EdgeLock SE05x secure access module use case are included as part of the LPC55S69 under NDA version.

- 1. Download the NDA-protected LPC55S69 SDK version from <u>NXP Docstore</u>, under the IOT solutions /SE050 content class category.
- 2. Drag and drop the NDA-protected LPC55S69 SDK zip file in the *Installed SDKs* section in the bottom part of the MCUXpresso IDE.

- 3. Check that the NDA-protected LPC55S69 SDK is installed successfully.
  - a. Click Import SDK examples from the MCUXpresso IDE quick start panel.
  - b. Make sure that a picture of an LPC55S69 with a red ribbon with the label "SE050 (NDA)" on it is available as shown in <u>Figure 6</u>

ille Edit Navigate Search Project ConfigTools Run Analysi:	SDK Import Wizard		
Project XX 📴 Periphe IIII Registers 🍁 Faults 🔍 🗖	Board and/or Device selection page		
	SDK MCUs     Available boards		
	NXP MIMXRTI052xxxxB     Supported hoards for device: MIMXRTI052xxxxB		
	> K&c > LPC5bx > MAXOT1052bccc8       MAXOT1052bccc8       form&def       conv       form&def	SEOSO seoso semantico	-
U Qui 🕴 (X)- Vari 📴 Outl 💁 Bre (V)- Glo 🖓 🔲			
) Qui 😢 (** Vari ⊱ Outl •e Bre (** Glo 😁 🗆			
) Quia 🕺 (NY-Varia 🔮 Qutia 9g Brea (NY-Gio.) 🖻 🗖	Selected Device:	SDKs for selected MCU	
Qui. 20 0+ Von 2€ Out % Bre 0+ Go □ □ MCUXpresso IDE - Quickstart Panel No project stetcted Create or import a project. Minoper SDIC example()	Selected Device: Target Core: cm7 Description: MMXRT1052: LMX14 MIMXRT1052 600MHz; 512XB SRAM Microcontrollers (MCUs) based on ARM4 Cortes 4-MT Core	SDKs for selected MCU         SDK Version         Manifest Ve.           HI SDK 2x, EVR8-MXRT1050         2.6.5         3.4.0	
) Qui. 20 Orl Ven. 2: Out. 9: Bree. 04-Go. 9: C	Selected Device: Target Core: cm7 Description: MIN/08T1052: IAX1® MIN/08T1052 600MHz; 512/08 SRAM Microcontrollers (MCUs) based on ARM® Cortex ®-M7 Core	SDKs for selected MCU           Name         SDK Version         Manifest Ve.           ⊞ SDK 2a, EWEHMORT1050         2.6.5         3.4.0	
Qui_       321       C0+Vari.       % Out.       % Bre.       04+Glo       Image: Comparison of the comp	Selected Device: Target Care: cm7 Description: MIM/XRT1052 LMX # MIM/XRT1052 600/Hz, 512/XB SRAM Microcontrollers (MCUs) based on ARM # Contex #-M7 Care	SDK-for selected MCU           Name         SDK Version         Manifest Ve.           ⊞ SDK2.x_EVK8-MXRT1050         2.6.5         3.4.0	

**Note:** For more detailed instructions on how to install the LPC55S69 SDK into our MCUXpresso workspace, refer to <u>AN12542</u> - <u>Quick start guide with LPC55S69</u>.

### 4.3 Board setup and wiring

This section describes the board setup and wiring of the OM-SE050ARD), the LPC55S69 and the CLEV6630B boards.

#### 4.3.1 OM-SE050ARD jumper configuration

We use the Arduino headers to connect the host MCU board to the OM-SE050ARD. The jumper settings to enable the  $I^2C$  slave interface over the Arduino header are shown in Figure 7:



Remember to change J14 to position 1-2, which connects SE\_VDD directly to 3V3 and bypasses enable signal. This is required because enable pin on LPC55S coincides with Silex-2401 SPI pins so we cannot use SE\_EN signal to drive SE\_VDD.

### 4.3.2 OM-SE050ARD connection with host MCU board (LPC55S69)

We use the Arduino connectors to mount the OM-SE050ARD board on top of the LPC55S69 as shown in Figure 8. Note that OM-SE050ARD should be aligned with A5 pin in LPC55S69 P19 header and D0 pin in LPC55S69 P18 header. The two last pins in P16 and the two first pins in P18 should be left open.



Figure 8. Arduino connectors of OM-SE050ARD and LPC55S69 boards

Double check that the two boards are connected as shown in Figure 9

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#### Figure 9. OM-SE050ARD connection to LPC55S69

### 4.3.3 Host MCU connection to NFC frontend (CLEV6630B)

We need to connect the LPC55S69 board via SPI to the CLEV6630B board, acting as the NFC frontend of the system. For that, we wire the LPC55S69 and CLEV6630B boards as shown in Figure 10:

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Table 4 shows the detailed connection of the LPC55S69 to CLEV6630B

Table 4	LPC55S69	connection t	o Cl	EV6630B	via SPI
	LF 033303	connection t			VIA OFI

LPC55S69 (# jumper - # pin)	CLEV6630B (# jumper - # pin)
P18-11 (RESET)	J301-2 (CLRC_NRST)
P17-6 (SPI CSEL)	J301-3 (SSEL)
P17-10 (MOSI)	J301-4 (MOSI)
P17-12 (MISO)	J301-5 (MISO)
P17-14 (SPI SCK)	J301-6 (SCK)
P17-7 (GND)	J302-1 (GND)
P18-3 (IRQ)	J302-2 (IRQ)

### 4.3.4 Laptop connection and TeraTerm configuration

Attach a USB cable to the LPC55S69 board debug link port and to the CLEV6630B board as shown in Figure 11.

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After connecting the boards to the laptop, open a Teraterm session and configure the serial communication, as shown in <u>Figure 12</u>:

rm: New co	nnection	×	Port:	СОМ7 ~ ОК
CP/ <u>I</u> P	Hos <u>t</u> :	myhost.example.com	Baud rate:	9600
		History	Data:	110 300 Cancel
	Service:	O Telnet	Parity:	600
		● <u>SSH</u> SSH version: <u>SSH2</u> ∨	Stop:	2400 Help
		Protocol: UNSPEC ~	Flow control:	9600
<u>e</u> rial	Po <u>r</u> t:	COM51: Standard Serial over Bluetoo V	Transmit delay	144UU 19200 38400
		COM51: Standard Serial over Bluetooth link (COM51) COM52: Standard Serial over Bluetooth link (COM52)	0 msec/	57600 me elline
	1	COM65: LPC-Linkli UCom Port (COM65)		230400
		~	L	460800 921600

Optionally, you may configure the TeraTerm terminal window to optimize the visualization of the logs. For that, change the New-line receive setting to **AUTO** as shown in Figure 13:

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0		Terminal size	New-line OK
		156 × 40	Receive: AUTO V
		Term size = win size	Transmit: cp
			2
		Auto willdow icsize	Heln
		Terminal ID: VT100 ~	Local echo
		Answerback:	Auto switch (VT<->TEK)
		Coding (receive)	Coding (transmit)
		UTF-8 V	UTF-8 v
		locale: american	
	~		
			Terminal size Terminal size Terminal size Terminal ID: VT100 ~ Answerback: Coding (receive) UTF-8 ~ Iocale: american

### 4.4 Project examples execution

After completing the board setup and wiring, import and run the project examples as described in this section.

### 4.4.1 Import the project examples in MCUXpresso workspace

The LPC55S69 SDK under NDA version include project examples that use EdgeLock SE05x for MIFARE DESFire EV2 card authentication, session key generation and key personalization operations as detailed in <u>Section 4</u>.

Import the project examples in your MCUXpresso worskpace as shown in Figure 14:

- 1. Click Import SDK examples from the MCUXpresso IDE quick start panel.
- 2. Select the LPC55S69 board from the list of available boards. The board picture should include a red ribbon with the label "SE050 (NDA)" on it as shown in <u>Section 4.2</u>.
- 3. Select the following project examples from the list and click the *Finish* button:
  - a. se\_SE05x\_ex\_Ev2Auth\_SE05x
  - b. se\_SE05x\_ex\_Ev2ChangeKey\_SE05x
  - **C**. se SE05x ex Ev2DivChngKey SE05x
  - d. se\_SE05x\_ex\_Prepare\_MFDFEV2
  - **e**. se\_SE05x\_ex\_Prepare\_SE05x
- 4. The project should now be visible in your MCUXpresso workspace.

**Note:** For detailed instructions on how to import project examples from LPC55S69 SDK, check <u>AN12542 - Quick start guide with LPC55S</u>

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🔀 workspace - Welcome page - MCUXpresso IDE	SDK Import Wizard	🔀 workspace - Welcome page - MCUXpresso IDE
File Edit Navigate Search Project ConfigTools Run Analysi:		File Edit Navigate Search Project ConfigTools Run Analys
: █ ▾  噫   ☜ ▾ 瓠 ▾ ở ♡ : ◙ ▾ ⊑ : ◙   ▶ ፡፡ ।	The source from the SDK will be copied into the workspace.     If you want to use linked files, please unzip the 'SDK_2.x_LPCXpresso55569' SD	➡ ▼
Project 🛛 🐕 Periphe 🚟 Registers 🏇 Faults 🖓 🗖	Import projects	Project 🛛 🚼 Periphe 💷 Registers 🏘 Faults 😐 🗖
	Project name prefix: Ipcxpresso55s69	> 🚰 lpcxpresso55s69_se_SE05x_ex_Ev2Auth_SE05x > 🚰 lpcxpresso55s69_se_SE05x_ex_Ev2ChangeKey_SE05x
	Use default location	>  Ipcxpresso55s69_se_SE05x_ex_Ev2DivChngKey_SE05x
	Location: C:\Users\Jordi Jofre\Documents\MCUXpressolDE_11.0.0_2516\work	> im ipcxpresso53569_se_SE05x_ex_prepare_MPDFEV2 > import in the impo
	Project Type	
	○ C Project ○ C++ Project ○ C Static Library ○ C++ Static Library	
	Examples	
	type to filter	
	Name	
	✓ ■ ≤ se_hostlib_examples Se SEDSy cloud aws wifi	
	□ ≡ se_SE05x_cloud_azure_wifi	
	se_SE05x_cloud_gcp_wifi	
	se sebs cloud lom will	
🕑 Qui 💥 (x)= Vari 📴 Outl 🗣 Bre (x)= Glo 🖓 🗖	Image: Separation of the separ	() Qui ☆ (×)= Vari 😤 Outl • Bre ⋈= Glo 📟 🗖
	se_SEO5x_ex_ecc	
MCUXpresso IDE - Quickstart Panel	□ se_SE05x_ex_mals □ se_SE05x ex_md	MCUXpresso IDE - Quickstart Panel
		✓ Create or import a project
New project	se_SE05x_ex_rsa	New project
Import SDK example(s)		Import SDK example(s)           Import SDK example(s)           Import project(s) from file system
✓ Build your project		➡ Build your project
Suild Clean		Build
·	U	0 items and and
		U items selected

### 4.4.2 Provision EdgeLock SE05x (ex\_prepareSe050)

The first step is to inject in the keys that will be used for authenticating the MIFARE DESFire EV2 card in the EdgeLock SE05x. This can be done by executing the <code>ex\_prepareSe050</code> project example, which injects two AES keys into the EdgeLock SE05x as depicted in Figure 15



To run the ex\_prepareSe050 project example, follow these steps:

1. Select the ex prepareSe050 project example from your workspace.

2. Go to the MCUXpresso Quickstart Panel and click *Debug* button, wait a few seconds until the project executes and click on *Resume* as shown in Figure 16



3. Go to TeraTerm, you should see in logs similar to the one shown in Figure 17:

COM13 - Tera Term VT     Edit Setup Control Window Help			[		×
App:INFO :PlugAndTrust_v02.11.03_20191107 sss:INFO :atr (Len=35) 00 A0 00 00 03 96 04 03 E8 00 FE 02 01 00 00 00 00 64 00 00 0A 4A 43 4F 54 50 4F	0B 50	Ø3 34	E8 20	08 41	^
sss:WHRN :Communication channel is riain. sss:WARN :!!!!Not recommended for production use.!!! App:INFO :SE050 prepared successfully for MIFARE DESFire App:INFO :ex_sss Finished	EV2	ex	amp	les	

### 4.4.3 Personalize MIFARE DESFire EV2 card (ex\_prepareMFDFEV2)

The second step is to personalize a blank MIFARE DESFire EV2 card. This can be done by executing the <code>ex\_prepareMFDFEV2</code> project example, which formats the card and creates a sample application with default key values inside the MIFARE DESFire EV2 card as depicted in Figure 18:

**Note:** The card personalization performed by the *ex\_prepareMFDFEV2* project example is provided for illustrative purposes. This card personalization needs to be adapted based on the card memory map required for each specific application.

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To run the <code>ex\_prepareMFDFEV2</code> project example, place your blank MIFARE DESFire EV2 card over the CLEV6630B antenna and follow these steps:

- 1. Select the ex\_prepareMFDFEV2 project example from your workspace.
- 2. Go to the MCUXpresso Quickstart Panel and click *Debug* button, wait a few seconds until the project executes and click on *Resume*

3. If the card personalization succeeds, your Teraterm window should look similar to Figure 19:

COM13 - Tera Term VT     Edit Setup Control Window Help		-		×
App:INFO :PlugAndTrust_v02.11.03_20191107 sss:INFO :atr (Len=35) 00 A0 00 00 03 96 04 03 E8 00 FE 02 01 00 00 00 00 64 00 00 0A 4A 43 4F	0B 50	03 E8 34 20	Ø8 41	ŕ
54 50 4F sss:WARN :Communication channel is Plain. sss:WARN :!!!Not recommended for production use.!!! App:INFO :UID after L3 Activation (Len=10) 08 8F 23 01 00 00 00 00 00 00 App:INFO :ATS after L4 Activation (Len=6) 06 75 77 81 02 80 App:INFO :Performing Pre Personalization				
App:INFO :Formating the card Successful				
App:INF0 :bCardUid[0] = 0x4				
App:INFO :bCardUid[1] = 0x50				
App:INFO :bCardUid[2] = 0x2c				
App:INFO :bCardUid[3] = 0x42				
App:INFO :bCardUid[4] = 0x4d				
App:INFO :bCardUid[5] = 0x4d				
App:INFO :bCardUid[6] = 0×80				
App:INFO :bCardUid[7] = 0x0				
App:INFO :bCardUid[8] = 0x0				
App:INFO :bCardUid[9] = 0x0				
App:INFO :Create AFC Application Successful				
App:INFO :Select the AFC Application Successful				
App:INFO :phEx_Create_ValueFile				
App:INFO : create Transaction MAC File Successful				
App:INFO : create Value File Successful				
App:INFO :***** Creating standard data file SUCCESS!!**				
App:INFO :ex_sss Finished				

Figure 19. ex\_prepareCard output project log

### 4.4.4 Authenticate and operate MIFARE DESFire EV2 card (ex Ev2Auth se05x)

Now that both the EdgeLock SE05x IC and the MIFARE DESFire EV2 card have been provisioned with the required keys, it is possible to authenticate the card with the EdgeLock SE05x and to start a secure transaction. This can be done by executing the ex Ev2Auth se05x project example.

First, the <code>ex\_Ev2Auth\_se05x</code> leverages EdgeLock SE05x to authenticate the card, as depicted in Figure 20

EdgeLock<sup>™</sup> SE05x for secure access control in Industrial IoT



After that, it generates and exports the MIFARE DESFire EV2 session key to the host MCU, which uses this session key to set up a secure transaction with the card, as depicted in Figure 21.

**Note:** The secure transaction between the EdgeLock SE05x and the MIFARE DESFire EV2 card performed by the *ex\_Ev2Auth\_se05x* project example is provided for illustrative purposes. The commands required in a transaction needs to be adapted based on each specific application.



To run the <code>ex\_Ev2Auth\_se05x</code> project example, place your blank MIFARE DESFire EV2 card over the CLEV6630B antenna and follow these steps:

- 1. Select ex\_Ev2Auth\_se05x project example from your workspace
- 2. Go to the MCUXpresso Quickstart Panel and click *Debug* button, wait a few seconds until the project executes and click on *Resume*

3. In Teraterm you should see the logs of the ex\_Ev2Auth\_se05x project example, which should look similar to Figure 22:

🗵 COM13 - Tera Term VT	- 0	×
File Edit Setup Control Window Help		
sss:WARN :Communication channel is Plain. sss:WARN :fftNot recommended for production use.fff App:INFO :UID after L3 Activation (Len=18) 09 80 80 20 8 ED 20 5C 80 80 80 80 80		^
App:INFO :ATS after L4 Activation (Len=6) 06 25 22 81 02 80		
App:INFO :Select the AFC Application Successful		
App:INFO :attempting to authenticate with cardkey = 0 and Se00bj ID = 2103 App:INFO : CARD =====> SE050 16-byte Ek(RndB) =		
(Len=16) 85 76 49 BC B0 3F 2D 58 48 24 04 BD FF 1E 62 39		
App:INFO : CARD <====== SE050 E(Kx, RandA    RandB') =		
CLen-32) ED 57 9D 0C 07 75 BD C5 92 CC DD 32 C7 30 B8 46 95 8A 02 63 4E E5 81 23 C2 95 D9 FB 27 7F 4A DE		
<pre>Rpp:INPO : CARD =====&gt; SE050 32-byte E(Kx, II!!RndA'!!PDCap2!!PCDcap2&gt;</pre>		
45 7D 61 AD 5A 5B 86 57 69 6E F5 EA FC C1 E6 3E 05 9A 28 B6 D1 97 6E 1B D9 1A 93 BE C1 BC 80 40	(Len=32)	
CARD <====== SE050 E(Kx, RandA    RandB') = (lens12)		
00 00 00 00 00 00 00 00 00 00 00 00 00		
DA E1 B5 49 EC F9 5D 86 C2 57 81 08 BA 0E 18 09		
BA 08 73 51 6F EC 68 CD 70 C6 4F 6E AA 60 A8 33 Ann: INFO : Dunned TL is (Len=4)		
App:INFO :pDataFarams->uCndCtr=8 App:INFO : EUZ First Authenticate Successful		
CRRD =====> SEMS 16-Dyte Ek(RndB) = (Len=16) 7D D7 38 CS F4 5E 54 82 9A 78 56 5E 95 DF 90 D5 App:INFO :		
CARD <===== SE050 E(Kx, RandA    RandB') = (Len=32)		
6C A1 3B 71 6B 8A FE 83 85 4F C1 93 23 55 FA 8D 8E 98 79 83 B1 E3 3F 67 1A 4B 73 92 4B 78 A7 B4 App:INFO :		
CARD =====> SE050 32-byte E(Kx, TI!!RndA'!!PDCap2!!PCDcap2>	= (Len=16)	
11 DE 46 3C 08 5D 6E D9 7E 99 06 37 C1 DA 5E D8 App:INFO :Dunped Session Key is (Len=16)		
D1 37 88 EB D5 83 A3 F1 63 39 AC 69 C8 A7 2B 78 App:INFO :Dumped Session Mac is (Len=16)		
SB 16 1D SC 64 02 34 78 DD DD 94 DE 02 E4 4A AC App:INFO :Dunped TI is (Len=4)		
DC 97 CA 78 App:INFO :pDataParams->uCndCtr-0 App:INFO : EU2 Following Authenticate Successful		
App:INFO :Authenticated with cardkey = 0 and Se00bj 1D = 2103308288 App:INFO :CARD UID is as below (Len=?) 0 S0 2C 42 4D 4D 80		
App:INFO :phEx_Use_ValueFile		
App:INFO : <enc add="" communication="" key)trying="" money="" session="" th="" the<="" to="" using=""><td></td><td></td></enc>		
App:INFO :Add money to the account successful		
App:INFO : The amount in your account After credit is 0 0 0 23		
App:INFO : Auth session is reset in software App:INFO : Auth session is killed in SE App:INFO :ex_sss Finished		~
Figure 22. ex Ev2Auth se05x log output project log		

### 4.4.5 Change MIFARE DESFire EV2 application key (ex\_Ev2ChangeKey\_se05x)

The EdgeLock SE05x also allows you to securely change MIFARE DESFire EV2 application keys. This can be done by executing the <code>ex\_Ev2ChangeKey\_se05x</code> project example or the <code>ex\_Ev2DivChngKey\_se05x</code> project example in case we want to use key diversification.

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In this context, we can leverage EdgeLock SE05x to authenticate with the current key value and then, to securely compute the MIFARE DESFire EV2 change key command as depicted in Figure 23:

**Note:** The MIFARE DESFire EV2 application key change performed by the ex\_Ev2DivChngKey\_se05x project example is provided for illustrative purposes. This process might need to be adapted depending on the MIFARE DESFire EV2 personalization, memory map, or specific application requirements.



To run the <code>ex\_Ev2ChangeKey\_se05x</code> project example, place your blank MIFARE DESFire EV2 card over the CLEV6630B antenna and follow these steps:

- 1. Select ex Ev2ChangeKey se05x project example from your workspace.
- 2. Go to the MCUXpresso Quickstart Panel and click *Debug* button, wait a few seconds until the project executes and click on *Resume*

3. In Teraterm you should see the example logs of the ex\_Ev2ChangeKey\_se05x project example, which should look similar to Figure 24:

COM12 - Tera Term VI	_		×
File Edit Setur Control Window Help			~
App:INFO :PlugAndTrust_v02.11.03_20191107			^
sss:INFO (Len=35) 00 A0 00 00 00 A3 96 04 03 E8 00 FE 02 0B 03 E8 08 01 00 00 00 00 64 00 00 0A 4A 43 4F 50 34 20 41 54 50 4F			
sss:WARN :Communication channel is Plain. sss:WARN :!!!Not recommended for production use.!!! App:INFO :UID after L3 Activation (Len=10)			
08 E6 38 04 00 00 00 00 00 00 App:INFO :ATS after L4 Activation (Len=6) App:LNEO 66 75 77 81 02 80			
Hpp:INFO :Select the HFC Hpplication Successful			
App:INFO : CORD =====> 2E050 16-bute Eb(RodR) =	0200		
(Len=16) D2 0E 64 9E 0B 21 56 86 14 0E F0 F9 D5 25 62 65			
App:INFO : $CARD \langle ===== SE050   E(K_x, RandA     RandB') =$			
(Len=32) 43 05 98 F1 5D 84 9D D2 2B 3C BA B3 12 54 48 ED 4E A4 86 FC A7 C8 18 62 8F 93 95 DB 03 9F 56 8F			
App:INFO : CARD =====> SE050 32-byte E(Kx, TI!!RndA'!!PDCap2!!PCDcap2> =			
DB 17 C2 9D 2C D6 30 78 6A 6D 97 34 A8 16 FF 12	(Len=)	32)	
App:INFO : CARD <===== \$E050 E(Kx, RandA !! RandB') = (Lan=12)			
00 00 00 00 00 00 00 00 00 00 00 00 00			
C9 41 DD 33 AF 97 7E EC 2C 5B A2 CE C6 E3 F5 0C App:INFO :Dumped Session Mac is (Len=16)			
5F F8 E4 2F F9 4A 34 3E C2 B3 9A A7 67 A3 36 67 App:INFO :Dumped TI is (Len=4)			
BE 90 09 4B App:INFO :pDataParams->wCmdCtr=0 App:INFO : EVZ First Authenticate Successful			
App:INFO :			
(Len=16) 78 91 60 96 00 63 08 2F F6 C1 R0 4F 69 40 F1 25			
App:INFO : $CARD \langle ====== SE050   E(Kx, RandA     RandB') =$			
(Len=32) BØ 21 36 74 8F F4 84 D7 F7 5A AF D2 67 Ø8 69 CB			
03 7D C1 DA BA E6 33 5F 60 62 4F BA 41 25 71 47 App:INFO :			
CARD =====> SE050 32-byte E(Kx, TI!!RndA'!!PDCap2!!PCDcap2) =	(Len=	16>	
18 8F 04 99 D3 2B 37 3E CF F9 97 43 40 6A 95 B9 App:INFO :Dumped Session Key is (Len=16)			
12 F8 87 7E D8 5C 77 C2 95 D3 76 BB B2 2D CC 6B App:INFO :Dumped Session Mac is (Len=16)			
20 42 DB R7 19 4F B8 32 8E 65 C3 3F 76 RD 9E 49 App:INFO :Dumped TI is (Len=4)			
App:INFO : EV2 Following Authenticate Successful			
App:INFO :Authenticated with cardkey = 0 and Se0Obj ID = 2103308288 App:INFO :attempting to change cardkey = 2 from Old Se0500bjID= 2103308288 iID= 2103308289		SeØ50	Ю
App:INFO : Change Key for card key 2 is Successful to Se0500bjID= 2103308289			
App:INFO :Checking that the previous auth session is still valid by trying a mmunication		ypted	CO
App:INFO :CARD UID is as below (Len=?) 04 50 2C 42 4D 4D 80			
App:INFO :Previous auth session is still valid App:INFO :Auth with the changed cardkey 2 App:INFO :Select the AFC Application Successful			
•••			
			~
Figure 24. ex_Ev2ChangeKey_se05x output project log			

### **5** Conclusions

The EdgeLock SE05x works as a secure access module for an IoT-enabled card reader, attached through a standard  $I^2C$  interface to the device host controller. It is used to increase the security of an access control system by protecting the MIFARE secret keys, authenticating MIFARE DESFire EV2 credentials and performing secure key exchange or key diversification.

The application logic running on the host controller is responsible for handling the contactless communication and implementing the MIFARE DESFire EV2 command set. The host controller relies on the EdgeLock SE05x to authenticate cards and to derive the corresponding session key for each transaction. Using this session key, the host controller is able to establish a secure channel with MIFARE DESFire EV2 to perform a specific transaction.

The EdgeLock SE05x support package is designed in order to reduce complexity, speed up design and add flexibility in each part of the product development process. It offers libraries for different MCUs, integration with the most common OSs and source code examples for the latest IoT security use cases.

The EdgeLock SE05x Plug & Trust Middleware facilitates the integration of EdgeLock SE05x into your host MCU software. In the context of secure access control in industrial IoT applications, it offers a high level API for secure operation with MIFARE DESFire EV2 and comes with project examples for MIFARE DESFire EV2 card authentication, session key generation and key personalization operations.

In addition, the EdgeLock SE05x support package includes project examples to evaluate EdgeLock SE05x in a secure access control framework as explained in <u>Section 4</u>. You can adapt these project examples and re-use the source code examples as needed to speed up the development of your IoT-enabled card reader design.

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