AN10498 LPC952 debugging/programming using the debug interface Rev. 01 – 20 July 2006 Application no

Application note

Document information

Info	Content
Keywords	P89LPC952, microcontrollers, Flash programming, debugging.
Abstract	This appnote describes how to use the 2-wire debug interface on the LPC952 for programming and emulation. The LPC952 is programmed on an MCB950 development board using the FS2 System Navigator debugger. The information that is needed to accomplish the programming and debugging are described step by step in detail in this appnote.



Revision history

Rev	Date	Description
01	20060720	Initial revision

Contact information

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AN10498_1

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AN10498

1. Introduction

The P89LPC952 is a microcontroller with on-board flash and a 2-wire debugging interface. The 2-wire debug interface can both be used to program and debug the application that the microcontroller is used in. This application note will describe in detail how to use the development tools to program the internal program Flash memory, and how to debug the application using the same development tools.

2. Development tools for the LPC952

This section describes different tools that are available for the development for the LPC952.

These tools include:

- 1. MCB950 Development board; Hardware
- 2. FS2 System Navigator 2-wire debugger; Hardware
- 3. Keil µVision 3 IDE (Integrated Development Environment); Software

2.1 MCB950 development board

The MCB950 is a low cost development board for the LPC952 microcontroller. The LPC952 can enter debug mode on this board using the FS2 System Navigator for emulation.

Either the FS2 System Navigator or the FlashMagic ISP software can program the LPC952 microcontroller on the MCB950 development board.

Fig 1 shows the MCB950 development board.



2.2 FS2 system navigator

The FS2 System Navigator is the 2 wire debugger that can be used for emulation on the LPC952. The FS2 System Navigator is also capable of programming the internal Flash memory of the LPC952.

The latest firmware for the FS2 System Navigator can be downloaded from:

http://www.semiconductors.philips.com/fs2_sysnav_firmware

Follow the software installation procedure provided by FS2.

Fig 2 shows the FS2 System Navigator.



2.3 Keil's µVision 3 IDE

The MCB950 comes with a CD that contains the LPC Development Studio version of the μ Vision 3 IDE. The latest version of the LPC Development Studio can be downloaded from: <u>http://www.semiconductors.philips.com/keil_LPCdevstudio</u>

 $\mu\text{V}\textsc{ision}$ 3 will be the IDE used for both the programming and the debugging of the LPC952.

3. Programming the LPC952 using the FS2 system navigator

In section 2.2 the FS2 System Navigator and the software installation for this tool was described.

In this section we will look at how to use the System Navigator and the μ Vision 3 IDE to program the Flash memory of the LPC952 on the MCB950.

3.1 Hooking up the system navigator to the MCB950

The power for the MCB950 is supplied through the USB cable. The System Navigator can be hooked up to the 10-pin connector on the MCB950. Make sure the Jumper on the MCB is in the RUN position.

AN10498 1



Fig 3 shows the MCB950 development board.

3.1.1 System navigator set-up

The System Navigator can be set-up with the Console program that is included with the System Navigator software.

Start up the System Navigator Console (with Connect Dialog). You should get a window as in Fig 4.

Click the following radio buttons:

SysNav

USB

FS2DI

Configuration:	
m8051ew-single-core	~
	Duplicate
	<u>R</u> emove.
	Configure
Probe: 💽 <u>S</u> ysNav 🔿 SysNav <u>O</u> E	Sca <u>n</u>
Comm: 🖲 USB O Ethernet O Lpti O Lp	2 C Lp <u>t</u> 3
Cable type: 🔿 EJTAG 🤏 ES2DI 🔿 Mjctor	
Default device (for 3rd party MDI apps):	
root	
🔽 Don't prompt for this again	
OK	Conner

Fig 4. System Navigator Console

Next go to the Configure window in the System Navigator Console. In the Configure window make sure the following settings are made in the window:

Tck Rate is set to 1000000

ResetDuration is set to 100

ResetAsserted is set to low

ResetNegated is set to off

The settings in the Configure window are shown in Fig 5.

CP	LICD compations	UCD comparison
SB connection:	Osb connection.	
Auto-detect	(• Auto-detect	• Auto-detect
Specific serial number: <none></none>	C Specific serial number: <none></none>	C Specific serial number: <pre></pre>
c <u>k</u> Rate: 1000000	Tck Rate: 1000000 TCK Throttle Ltag Chain: 8	Tck Rate: 1000000 TCK Throttle Jtag Chain: 8
e <u>v</u> ice:	Device:	De <u>v</u> ice:
ot 🖂	root	root
<u>N</u> ew	<u>N</u> ew	<u>N</u> ew
Remove	Bemove	. <u>B</u> emove
evice Inde <u>x</u> : 0 <u>I</u> ype: m8051ew	Device Index: 0 Iype: m8051ew	Device Inde <u>x</u> : 0 <u>I</u> ype: m8051ew
her configuration settings:	Other configuration settings:	Other configuration settings:
ash 🔼	Flash	Flash
esetDuration	ResetDuration	ResetDuration
neout k	Reset/Asserted	Timeout New.
esetNegated Delete	ResetNegated	ResetNegated
lue: 100	Value: Iow	Value: off
efault: 100	Default: low	Default: off
esgription: 1 to 1000. Length in milliseconds of the pulse on RST* from the probe.	Description: high/low. Signal state for debugger to drive when asserting reset.	Description: high/low/off. Signal state for debugger to drive when not asserting reset.
Close	Close	Close
a. ResetDuration	D. ResetAsserted	c. Resetinegated

3.1.2 Testing the set-up

After the System Navigator is set-up the console window will open. From this console a variety of commands can be used. All the different commands for the System Navigator are described in the FS2 Getting Started manual. For the purposes of this appnote we will just use 1 command to test the communication between the LPC952 microcontroller and the System Navigator.

In the System Navigator Console type the "Status" command. The Console will display the OCI version of the LPC952 device. The correct OCI version for the LPC952 microcontroller is 0xF2.

Fig 6 shows the System Navigator Console return the correct OCI version.

File Edit Window Prefs History Help	
configuration: m8051ew-single-core	
sysnav open server local	
sysnav open probe usb sysnav fs2di console	
sysnav tok usb 1000000	
sysnav tckthrottle usb off	
sysnav jtagchain usb 8	
stopstate usb idle	
jtaginit usb	
sysnav open device root m8051ew usb 0 shared	
openport jnet	
hotplug root	
bank off	
User reset.	
1FOOp C1O3 ajmp Ox1eO3	
add Window menu items	
start up complete	
1> status	
Chassis type= SysNav	
OCI version = 0xF2	
trace depth = 16 frames	
triggers = 4	
Host comm = usb-fs	
2>	
Deed. Ulabed Ulaerook	
Heady Halted User reset	

Fig 6. Console returning OCI version

If an error occurs check that the USB is powering the MCB950 board, the Power light should be on.

Check if the RUN / Reset jumper is set on the RUN position.

3.2 Using µVision3 to program the LPC952

The μ Vision3 IDE can interface to the FS2 System Navigator Console software. This way the LPC952 can be programmed through the same IDE as the code / firmware for the LPC952 is developed in.

3.2.1 Setting up µVision3 to use the FS2 System Navigator as Programmer

Open μ Vision 3, open the project that you would like to program / debug using the System Navigator. If you don't have a project the P5_Blinky example, that is include in the ZIP file with this appnote, can be used.

Go to options for Target select the Utilities tab. From the list of "Target Driver for Flash Programming" select the FS2/Keil M8051E Driver. The selection of the driver is shown in Fig 7.

	FS2/Keil M8051EW Driver	Settings	Update Targ	jet before Debugging
Init File:	ST-uPSD ULINK Driver		E	dit
C Use Extern	EPM900 LPC Emulator/Programl. En]		
Command:	FM.EXE			
Arguments:	'DEVICE(\$D,\$X) ERASE(DEVICE,PRO	TECTISP) HEXFIL	E("#H",NOCHEC	KSUMS,NOFILL,PROT
	Run Independent			

Fig 7. Options for Target - Utilities

Go to the Settings of the target driver and make sure the following are set-up

- Erase is selected
- Program is selected
- Verify is selected
- Protect ISP is selected
- Write configuration

rownload Function ▼ Erase ▼ Program ▼ Verify	Write ConfigurationProtect ISP Code
Vuse Configuration from START950.A5	1
evice Configuration & Security	
SECO OXO SEC4 OXO	BOOTVEC: 0x1F
SEC1 0x0 SEC5 0x0	BOOTSTAT: 0x00
SEC2 0x0 SEC6 0x0	
SEC3 0x0 SEC7 0x0	
*** Note. Setting security bits at this tim	e has been temporarily disabled ***
_UCFG1 0x0063	_UCFG2 0x00FC
Oscillator Internal BC oscillator	HLT_TO Halt Timer 0
WDSE Watchdog Safety Enable	HLT_RTC Halt Timer RTC
₩ BOE Brownout Detect Enable	HLT_T1 Halt Timer 1
🔽 RPE 🛛 Reset Pin Enable	DBG_B Debug Enable B
☐ WDTE Watchdog Timer Enable	TRIGEN Trigger Output Enable
	M DBG_A Debug Enable A
lash Erase	
Global Erase all progam space on F	Flash
Sector 0 (0x0000-0x03FF)	🔽 Sector 4 (0x1000-0x13FF)
Sector 1 (0x0400-0x07FF)	Sector 5 (0x1400-0x17FF)
Sector 2 (0x0800-0x0BFF)	Sector 6 (0x1800-0x1BFF)
Sector 3 (0x0C00-0x0FFF)	Sector 7 (0x1C00-0x1FFF)
	Cancel

3.2.2 Programming the LPC952

Once the $\mu Vision$ IDE is set up to use the System Navigator it is quite easy to program an LPC952 device.

The μ Vision IDE has a LOAD shortcut button that will program the selected target with the selected target driver settings. Once the LOAD button is pressed μ Vision 3 will open a System Navigator Console and send the correct commands through the console to the LPC952 microcontroller for erasing, programming and verifying.

Fig 9 shows the LOAD button, and the IDE status



4. Debugging the LPC952 using the FS2 System Navigator

This section describes how the System Navigator can be used to debug the LPC952 in an application target. How to interface the debugger to the μ Vision IDE will also be shown.

4.1 Setting up µVision3 to use the FS2 System Navigator as Programmer

Setting up the System Navigator as a debugger should be quite easy if you have followed the previous steps with programming the LPC952 with the System Navigator.

Open μ Vision 3, open the project that you would like to program / debug using the System Navigator. If you don't have a project the P5_Blinky example, that is include in the ZIP file with this appnote, can be used.

Go to options for Target; select the Debug tab. Select to use the FS2/Keil M8051E Driver. The selection of the driver is shown in Fig 10.

Use Simula	tor Settings I to Real-Time	Use: FS2	/Keil M8051EW Driver Settings Monitor-51 Driver					
 Load Applic nitialization File 	eation at Startup 🔽 Run to main()	Initializatic ST-u	Keii ISDS I In-System Debugger MON390: Dallas Contiguous Mode LPC900 EPM Emulator/Programmer Initializatii ST-uPSD ULINK Driver					
	Edit	FS2 EPM	/Keil M8051EW Driver 1900 LPC Emulator/Nogrammer					
Restore Debu	ug Session Settings	- Restore Deb	ug Session Settings					
Breakpo Watchp Memory	oints ✓ Toolbox ioints & PA Display	 ✓ Breakpo ✓ Watchp ✓ Memory 	oints I✓ Toolbox ooints ⊬Display					
CPU DLL:	Parameter:	Driver DLL:	Parameter:					
S8051.DLL		\$8051.DLL						
Dialog DLL:	Parameter:	Dialog DLL:	Parameter:					
DLPC.DLL	-pLPC952	TLPC.DLL	-pLPC952					

In the settings window the FS2 System Navigator Console will be opened and the same settings as described in section <u>3.1</u> can be used.

4.2 Entering debug mode

Once the debugger is set-up correctly the device has to be programmed with the code from the project you would like to debug. The steps that are shown in section 3 cover programming. Once the programming is set-up the LOAD button from the μ Vision 3 IDE can be used to program the code from the latest build of the open project.

Now clicking on the debug icon can enter debug-mode. Fig 11 shows some debug icons.



Once in debug-mode, double clicking on a code line can set breakpoints. Fig 12 shows a breakpoint in the C code. The yellow cursor indicates where the program counter is currently pointing in the code.

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The μ Vision IDE can also be used to display a memory window. Fig 13 shows a memory window of the on-chip data RAM.

×	Address: d:8	30							
	D:0x80:	00	07	ΒA	FF	FF	00	00	0(
	D:0x90:	23	FF	2C	FF	FC	00	25	0(
	D:OxAO:	CO	00	02	00	FF	CO	00	FI
	D:OxBO:	FC	FF	FC	38	04	00	00	8(
	D:OxCO:	00	FF	00	00	00	00	00	0(
	D:OxDO:	01	60	00	00	00	FΟ	FΟ	F(
	D:OxEO:	08	ЗF	04	00	70	63	00	0(
2	D:OxFO:	00	00	00	00	00	00	С1	2(
Memo] Με	emor	y #1	Μ	emory	/#2	χм	emo

AN10498

5. Conclusion

In this appnote the detailed steps have been described how to program and debug the LPC952 microcontroller with the FS2 System Navigator and the μ Vison 3 IDE. This knowledge can be used to program and debug LPC952 applications in the end target instead of on the MCB950 development board.

If the provisions for debugging have been made on the target board, this set-up can even be used out in the field if a problem occurs. The same debugger can be hooked up to a target board to debug the actual system and conditions that cause a failure.

If the firmware of the LPC952 needs to be updated in the field the LPC952 can be used to reprogram the LPC952 if needed.

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7. **Contents**

1.	Introduction
2.	Development tools for the LPC9523
2.1	MCB950 development board3
2.2	FS2 system navigator4
2.3	Keil's µVision 3 IDE4
3.	Programming the LPC952 using the FS2
system	navigator4
3.1	Hooking up the system navigator to the
MCB95	
3.1.1	System navigator set-up5
3.1.2	Testing the set-up7
3.2	Using μ Vision3 to program the LPC9528
3.2.1	Setting up µVIsion3 to use the FS2 System
Navigati	or as Programmer
3.2.2	Programming the LPC95210
4.	Debugging the LPC952 using the FS2 System
Navigat	or11
4.1	Setting up μ Vision3 to use the FS2 System
Navigat	or as Programmer11
4.2	Entering debug mode12
5.	Conclusion14
6.	Legal information15
6.1	Definitions15
6.2	Disclaimers15
6.3	Trademarks15
7.	Contents16

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