



8位微控制器，带双时钟**80C51**内核和带**256**字节数据**RAM**的**4 kB/8 kB 3 V**闪存

P89LPC930FDH

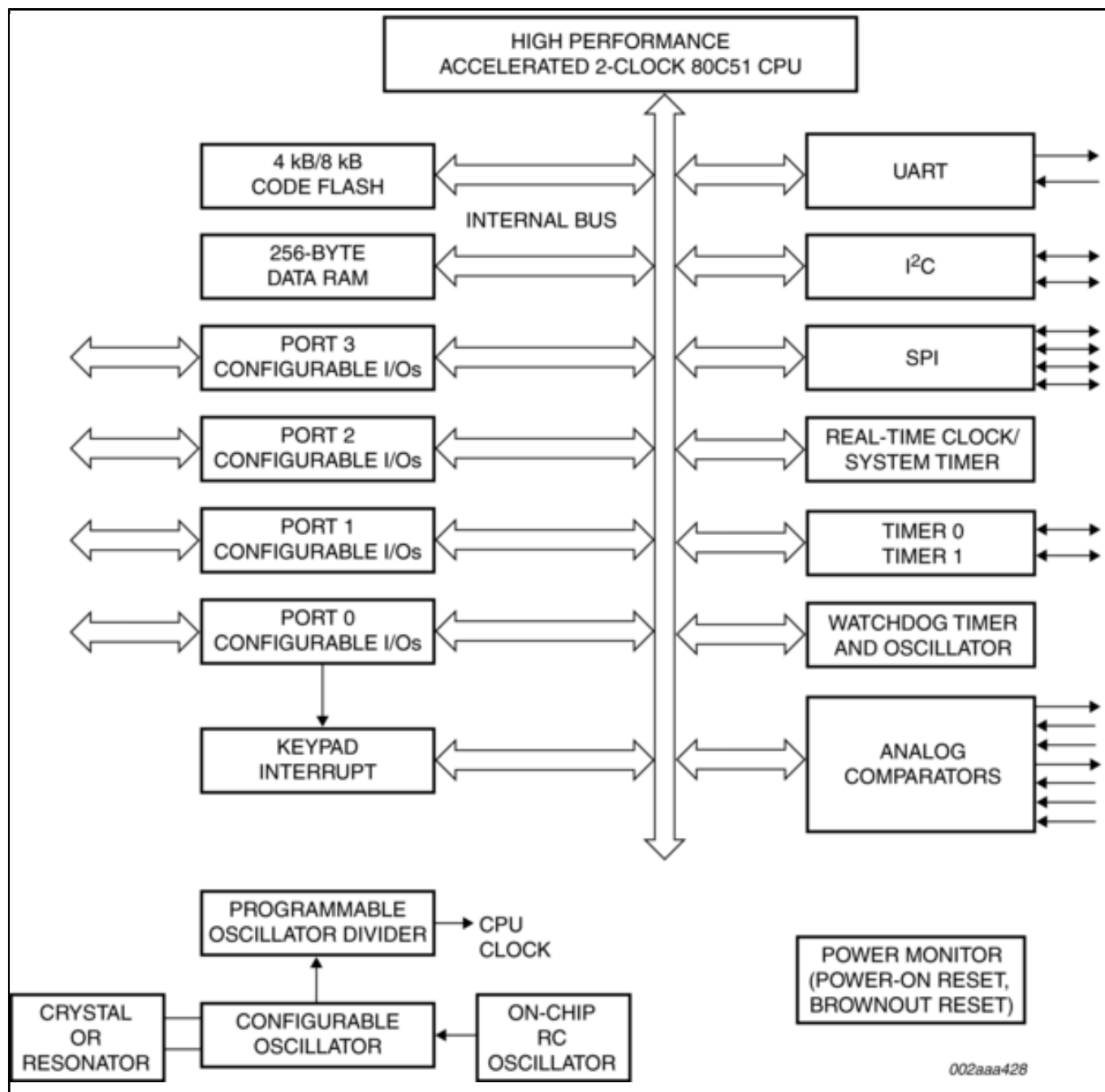
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P89LPC930/931是一款单芯片微控制器，专门针对各种性能需求中需要高集成、低成本解决方案的应用而设计。**P89LPC930/931**基于高性能处理器架构，在两到四个时钟周期内执行指令，速率是标准**80C51**设备的**6**倍。**P89LPC930/931**已纳入许多系统级功能，以减少零件数量、电路板空间和系统成本。

Block diagram: P89LPC930FDH, P89LPC931FDH Block Diagram



View additional information for [8位微控制器，带双时钟80C51内核和带256字节数据RAM的4 kB/8 kB 3 V闪存](#).

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