

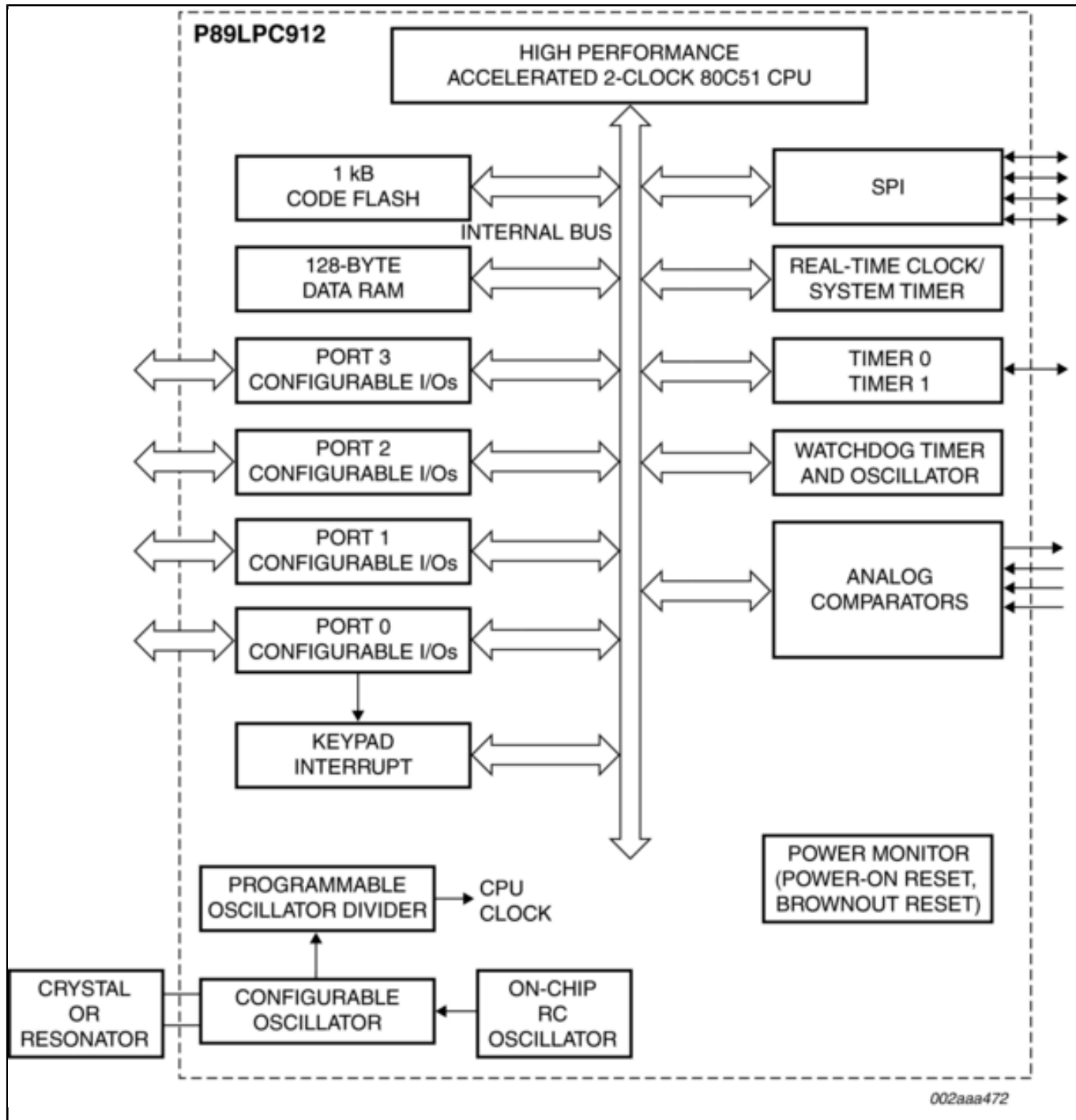


8位微控制器，带双时钟**80C51**内核和带**128**字节**RAM**的**1 kB 3 V**闪存

P89LPC913FDH

P89LPC912/913/914是单芯片微控制器，采用低成本**14**引脚封装，基于高性能处理器架构，在两到四个时钟周期内执行指令，速率是标准**80C51**设备的**6**倍。P89LPC912/913/914已纳入许多系统级功能，以减少组件数量、电路板空间和系统成本。

Block diagram: P89LPC912FDH, P89LPC912HDH, P89LPC913FDH Block Diagram



View additional information for [8位微控制器，带双时钟80C51内核和带128字节RAM的1 kB 3 V闪存](#).

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