



PowerQUICC® III处理器具有 TDM、DDR、PCI、1 **GB**以太网、安全性、 带有**UTOPIA**的**CPM**

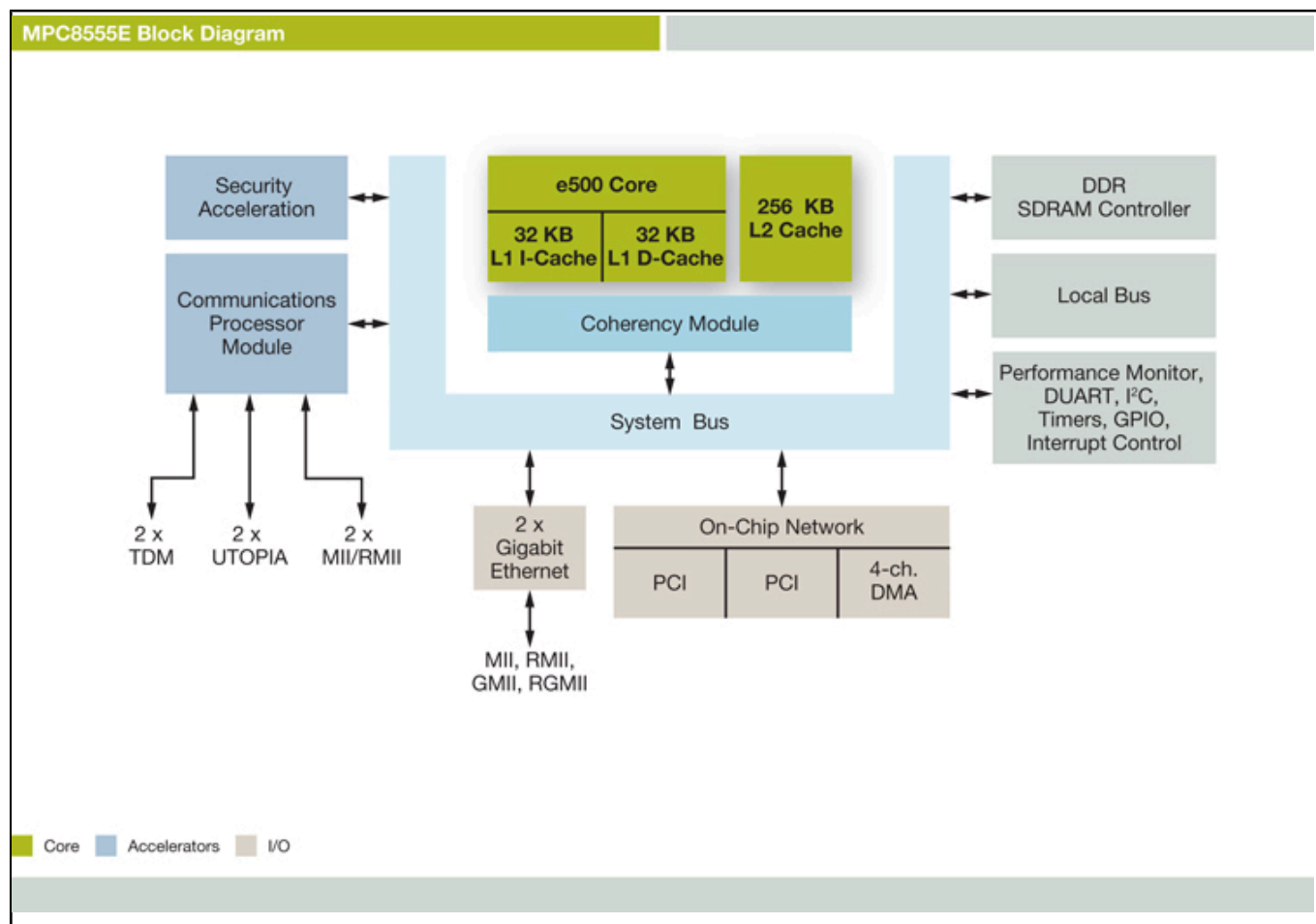
MPC8555E

Last Updated: Apr 9, 2022

恩智浦MPC8555E PowerQUICC®III主机处理器集成了多种先进的恩智浦技术、模块化内核以及外设。利用恩智浦的片上系统 (SoC) PowerQUICC® III平台架构，MPC8555E结合了功能强大的Book E e500内核和通信外设技术，以平衡处理器的性能和I/O系统吞吐量。该处理器的时钟速率在533 MHz至1 GHz范围。

访问该器件的勘误表文件需要一个NDA。请联系当地恩智浦销售机构或恩智浦授权分销商。

Freescale PowerQUICC MPC8555E Communications Processor Block Diagram Block Diagram



View additional information for [PowerQUICC® III](#) 处理器具有TDM、DDR、PCI、1 GB以太网、安全性、带有UTOPIA的CPM.

Note: The information on this document is subject to change without notice.

www.nxp.com

NXP and the NXP logo are trademarks of NXP B.V. All other product or service names are the property of their respective owners. The related technology may be protected by any or all of patents, copyrights, designs and trade secrets. All rights reserved. © 2025 NXP B.V.