



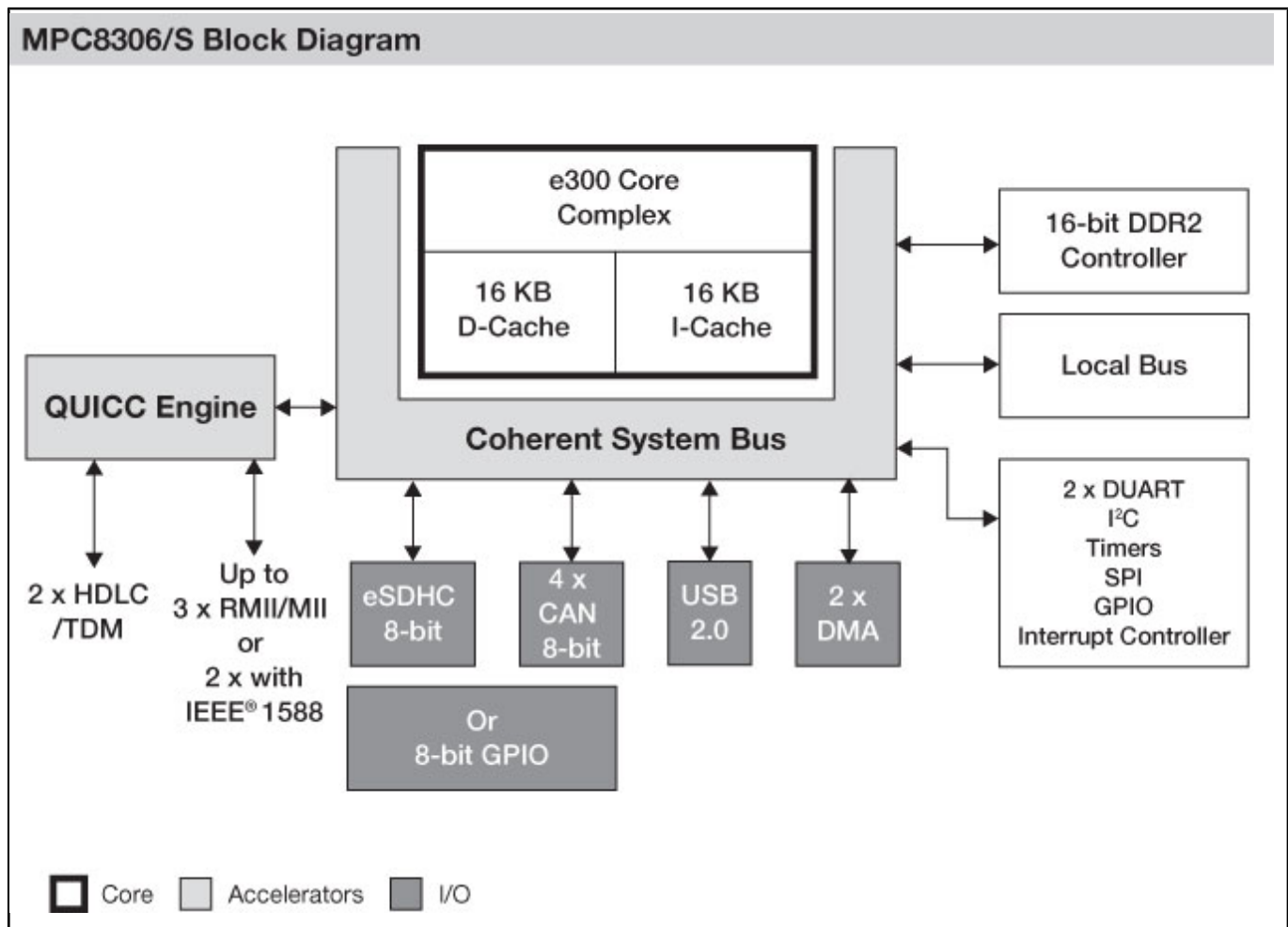
低功率**Low-Power PowerQUICC® II** 处理器，带 DDR2、eSDHC和**128**个通道。 **HDLC/TDM**，10/100以太网，USB，IEEE® **1588**

MPC8306

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MPC8306和MPC8306S是高性价比的MPC830x通信处理器系列产品的成员，基于e300内核，采用Power Architecture®技术构建。这两款器件集成了QUICC Engine®技术，能支持 HDLC、TDM 和 10/100 R/MII。MPC8306 还支持 IEEE® 1588 v2 时间戳功能。MPC8306/S 器件为用户提供高度集成且完全可编程的通信处理器，能用于多种网络、工业控制和低端嵌入式应用。这可确保快速开发出高性价比的系统解决方案，并可灵活适应新标准，并逐步演进系统要求。

Freescale PowerQUICC MPC8306 Communications Processor Block Diagram Block Diagram



View additional information for [低功率Low-Power PowerQUICC® II处理器，带 DDR2、eSDHC和128个通道。HDLC/TDM，10/100以太网，USB，IEEE® 1588。](#)

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