

32位微控制器

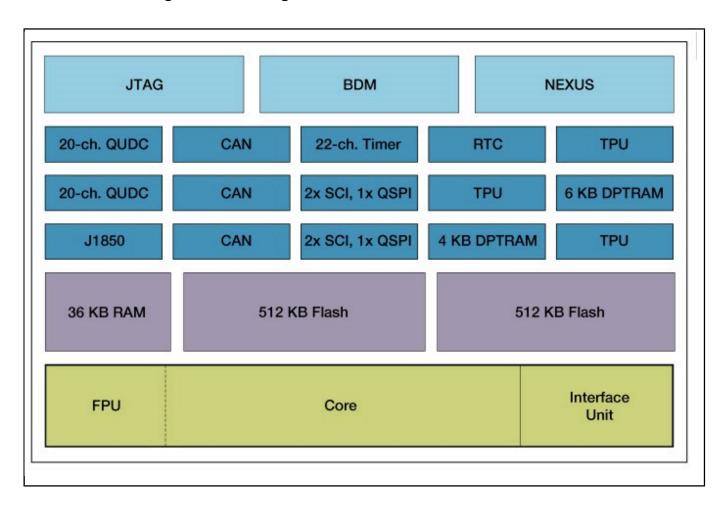
MPC566

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MPC566整合了一个Power Architecture®内核、一个双精度浮点运算单元和BCC、36 KB的RAM和 1 MB的闪存。

MPC566是MPC565的升级版, 提供代码压缩以便更有效地利用内部或外部闪存。代码压缩针对 汽车(非高速缓存)应用进行了优化,这个新方案将性能提高了40%至50%。

MPC566 Block Diagram Block Diagram



View additional information for 32位微控制器.

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