MCX N94, N54, N53, N52 and N24 with Highly Integrated Low-power Dual Core Arm® Cortex®-M33 MCUs, with on-chip Accelerators and Advanced Security

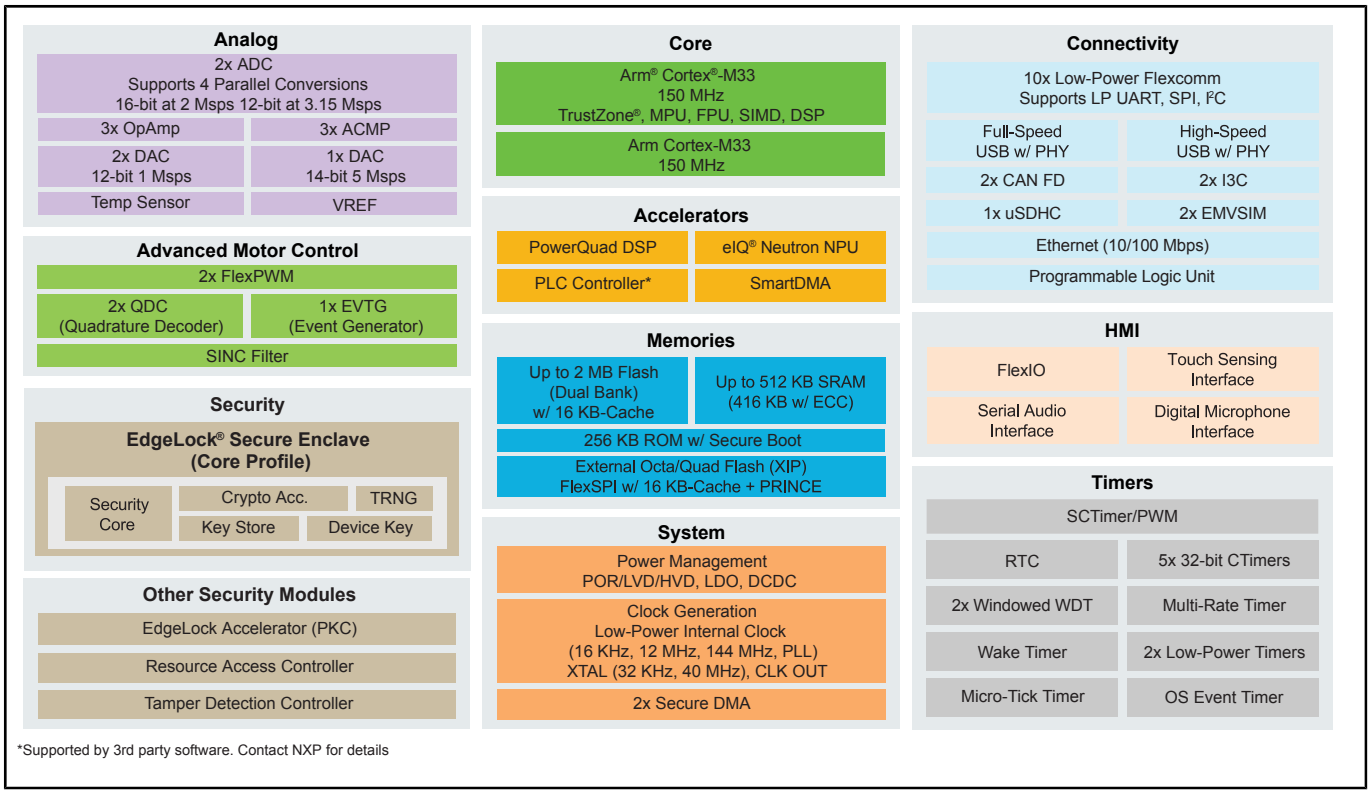
MCX-N94-N54-N53-N52-N24

Last Updated: Nov 28, 2025

MCX N94x、N54、N53、N52和N24采用两个高性能Arm®Cortex®-M33内核，运行频率高达150MHz，提供2MB闪存以及可配置的带完整ECC的RAM、DSP协处理器，并集成了eIQ Neutron NPU。与单独的CPU内核相比，NPU可提供高达42倍的机器学习（ML）吞吐量提升，从而能够减少系统唤醒的时间，并降低整体功耗。

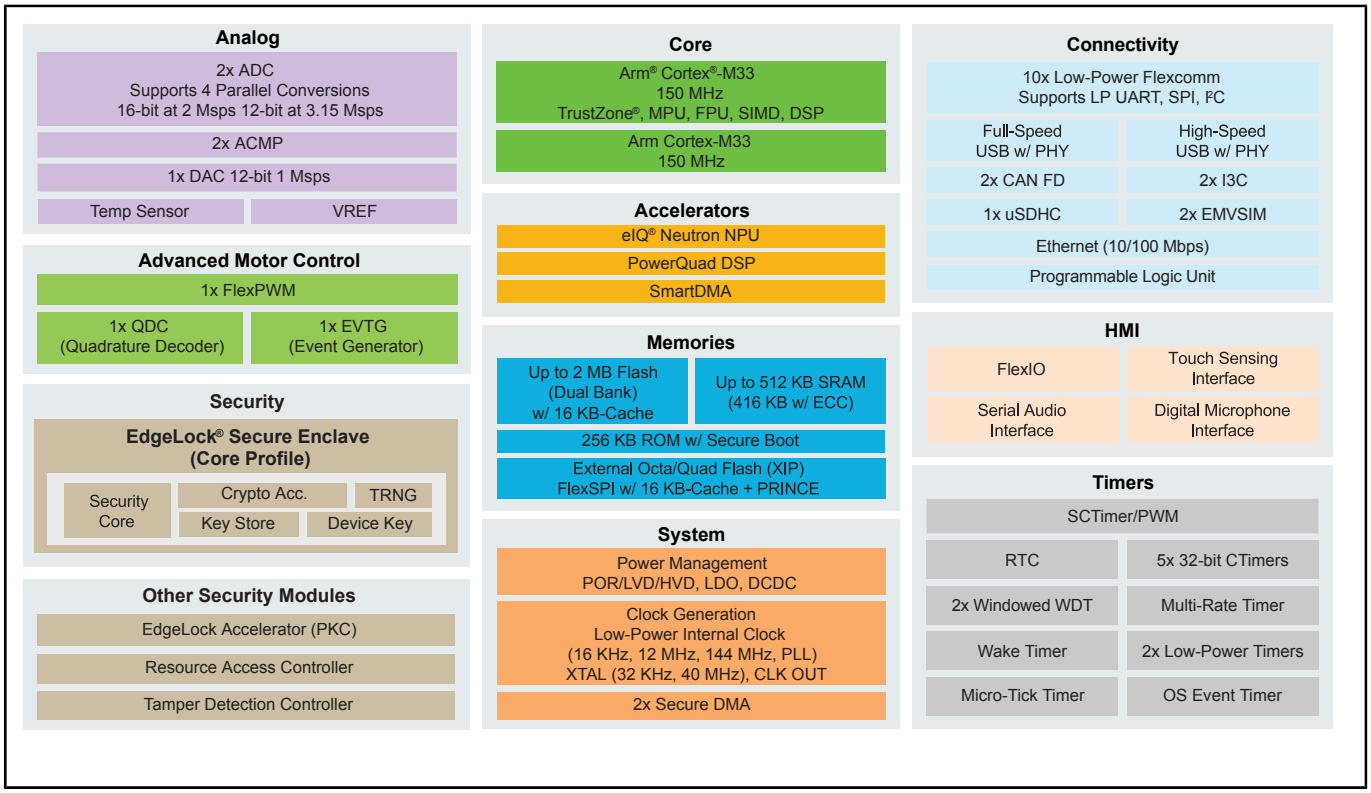
这种多核设计智能、高效地将工作负载分配给模拟与数字外设，提升了系统性能并降低了功耗。MCX N全系受到MCUXpresso Developer Experience支持，有助于优化、简化并加速嵌入式系统开发。

MCX N94x MCUs Block Diagram

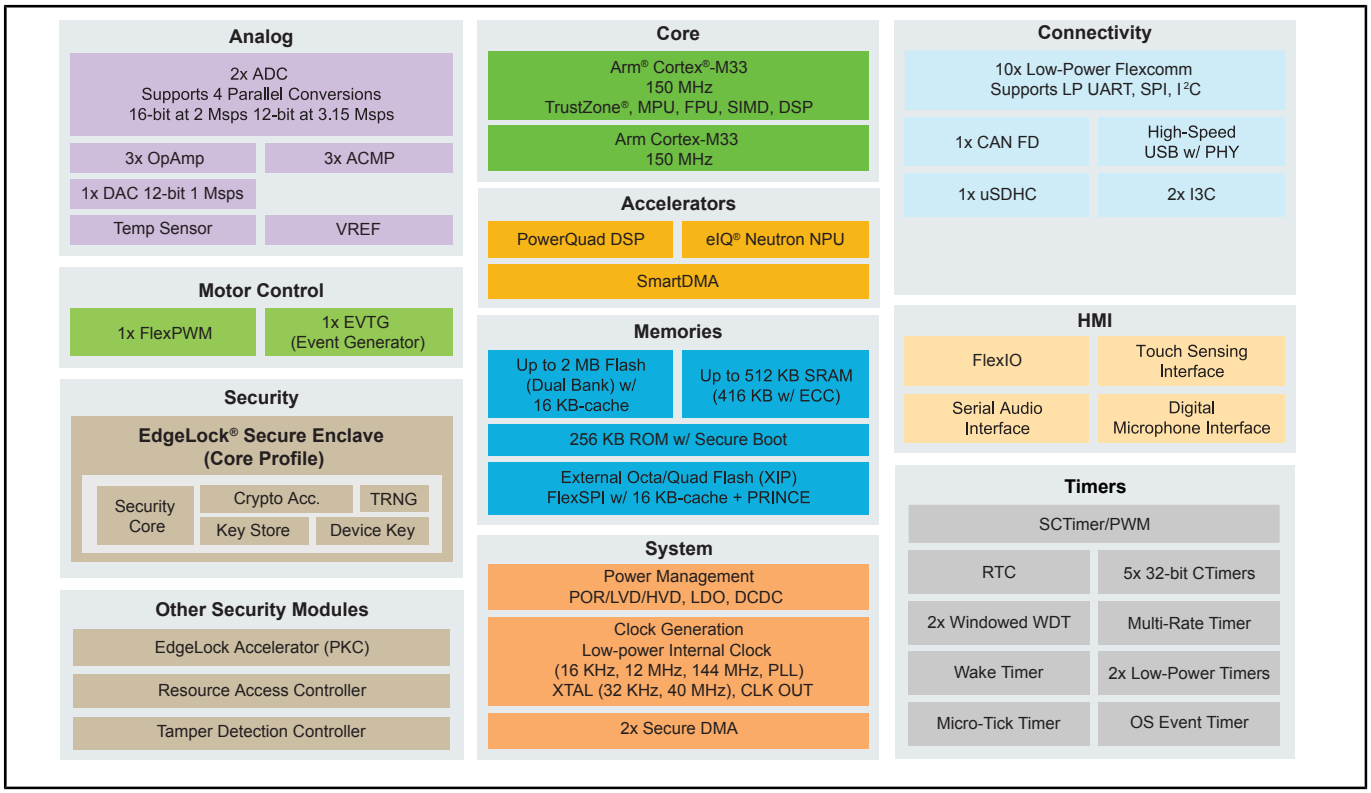


*Supported by 3rd party software. Contact NXP for details

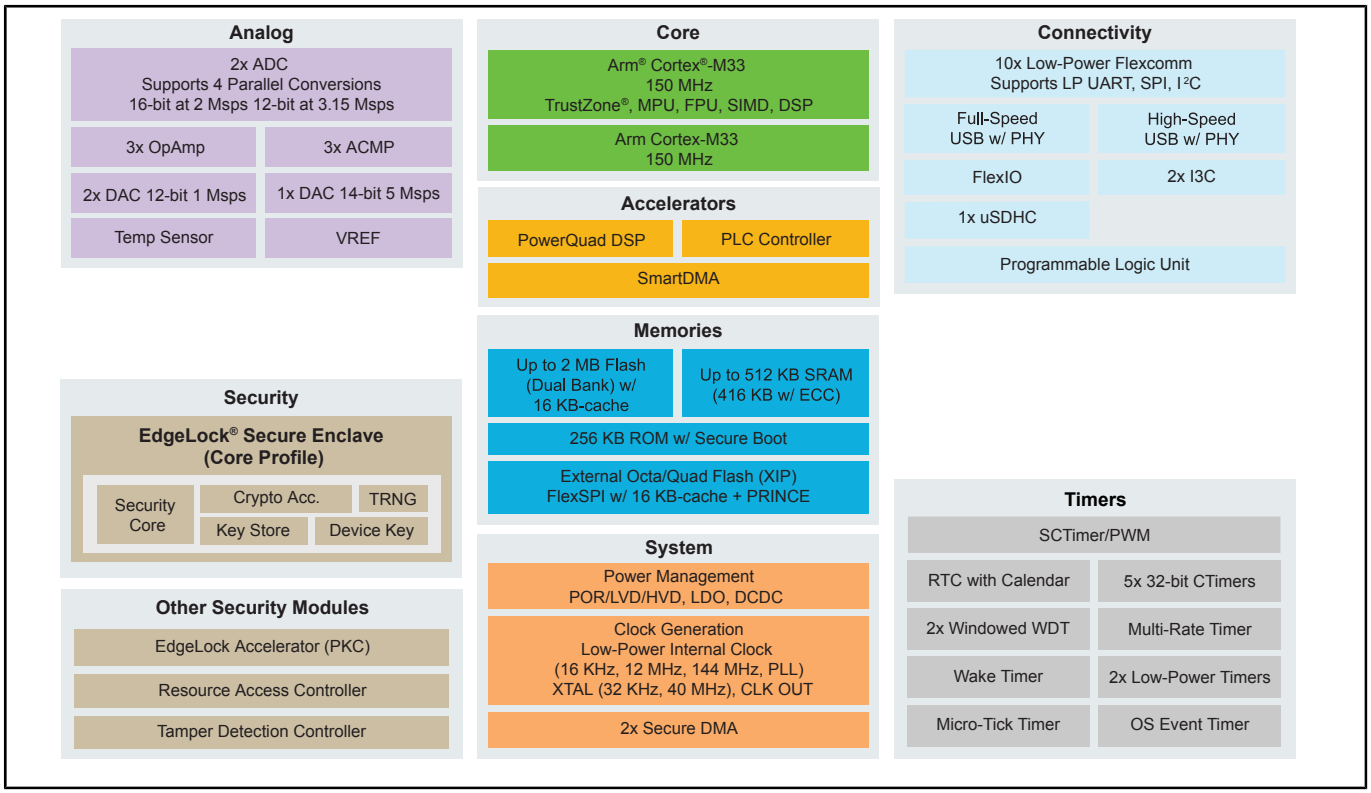
MCX N54x MCUs Block Diagram



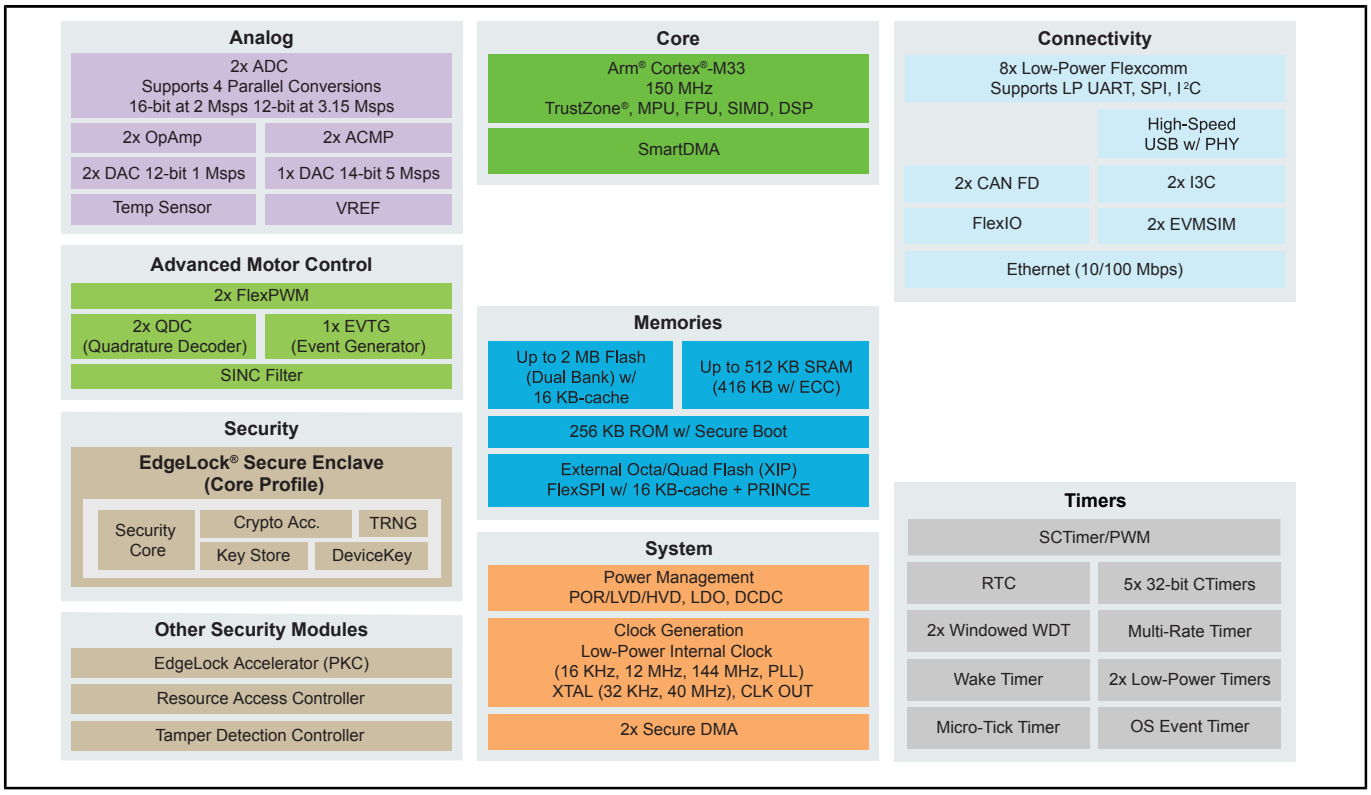
MCX N53x MCUs Block Diagram



MCX N52x MCUs Block Diagram



MCX N24x MCUs Block Diagram



View additional information for MCX N94, N54, N53, N52 and N24 with Highly Integrated Low-power Dual Core Arm® Cortex®-M33 MCUs, with on-chip Accelerators and Advanced Security.

Note: The information on this document is subject to change without notice.