



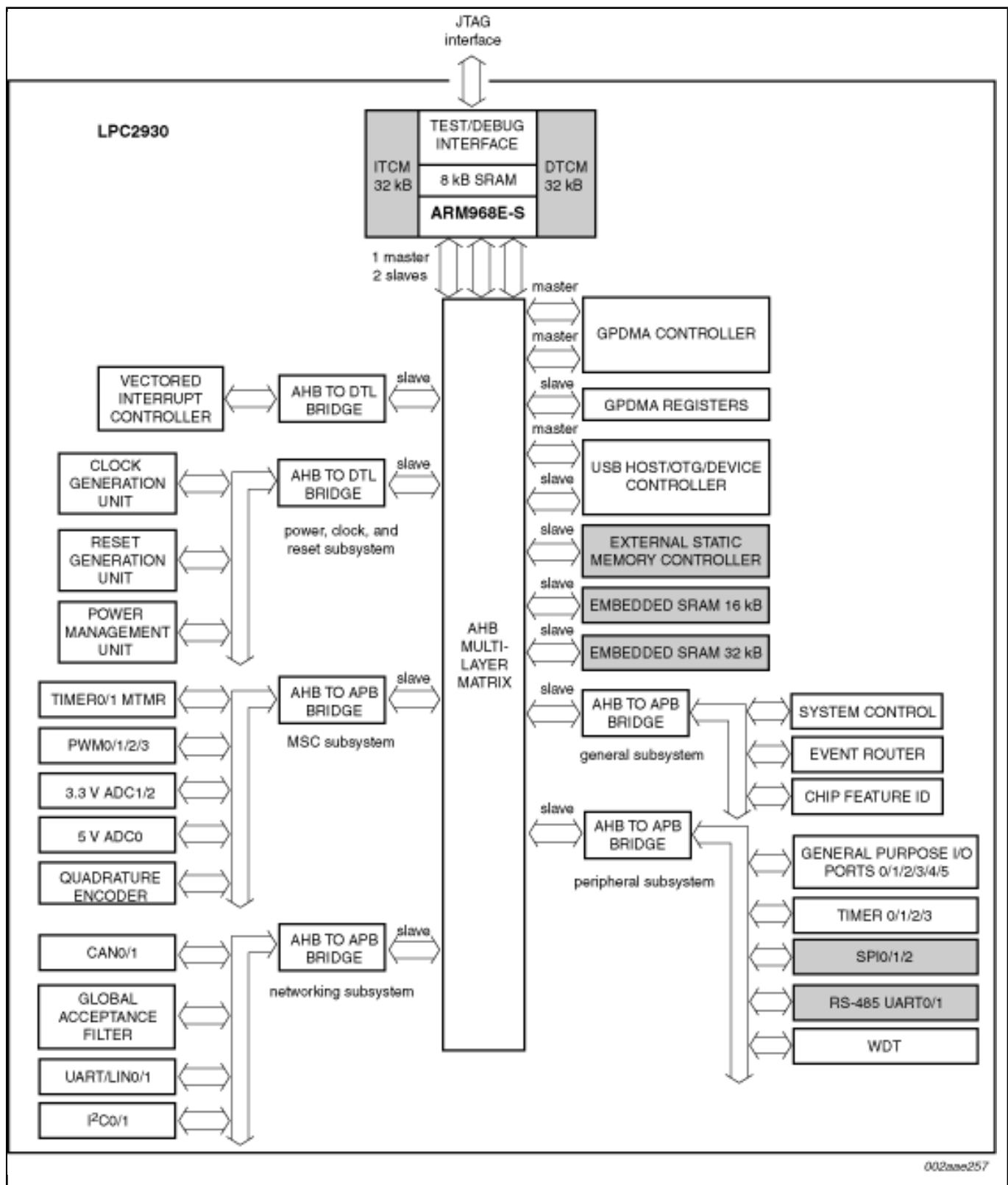
不带**flash**的**Arm9™**微控制器，带 CAN、LIN和**USB**

LPC2930FBD208

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LPC2930在单个芯片上结合了ARM968E-S CPU内核，2个运行频率高达125 MHz的集成TCM模块，全速**USB 2.0**主机/OTG/设备控制器，CAN和LIN，56 kB SRAM，外部存储器接口，3个10位ADC以及多个串行和并行接口，针对消费品、工业和通信市场。为优化系统功耗，LPC2930拥有非常灵活的时钟产生单元（CGU），它提供动态的时钟选通及校准。

Block diagram: LPC2930FBD208 Block Diagram



View additional information for [不带flash的Arm9™微控制器](#)，带CAN、LIN和USB.

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