



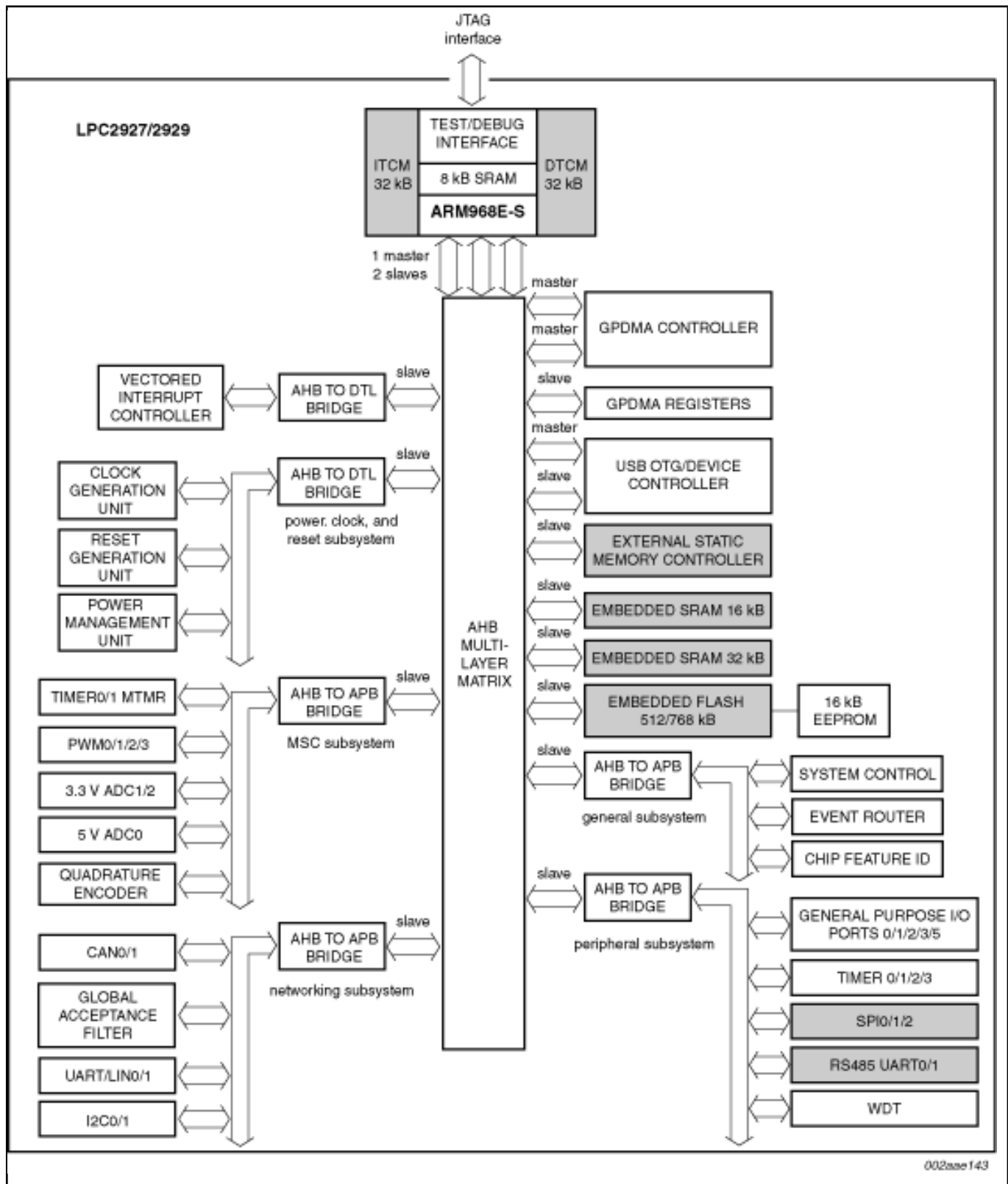
Arm9™微控制器，带CAN、LIN和USB

LPC2929FBD144

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LPC2926/2927/2929在单个芯片内结合了ARM968E-S CPU核，2个运行频率高达125 MHz的集成TCM模块，全速USB 2.0OTG和设备控制器，CAN和LIN，56 kB SRAM，高达768 kB Flash存储器，外部存储接口，3个10位ADC以及多个串行和并行接口，针对消费品、工业和通信市场。为优化系统功耗，LPC2926/2927/2929拥有非常灵活的时钟产生单元（CGU），它提供动态的时钟门控及校准。

Block diagram: LPC2927FBD144, LPC2929FBD144 Block Diagram



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