



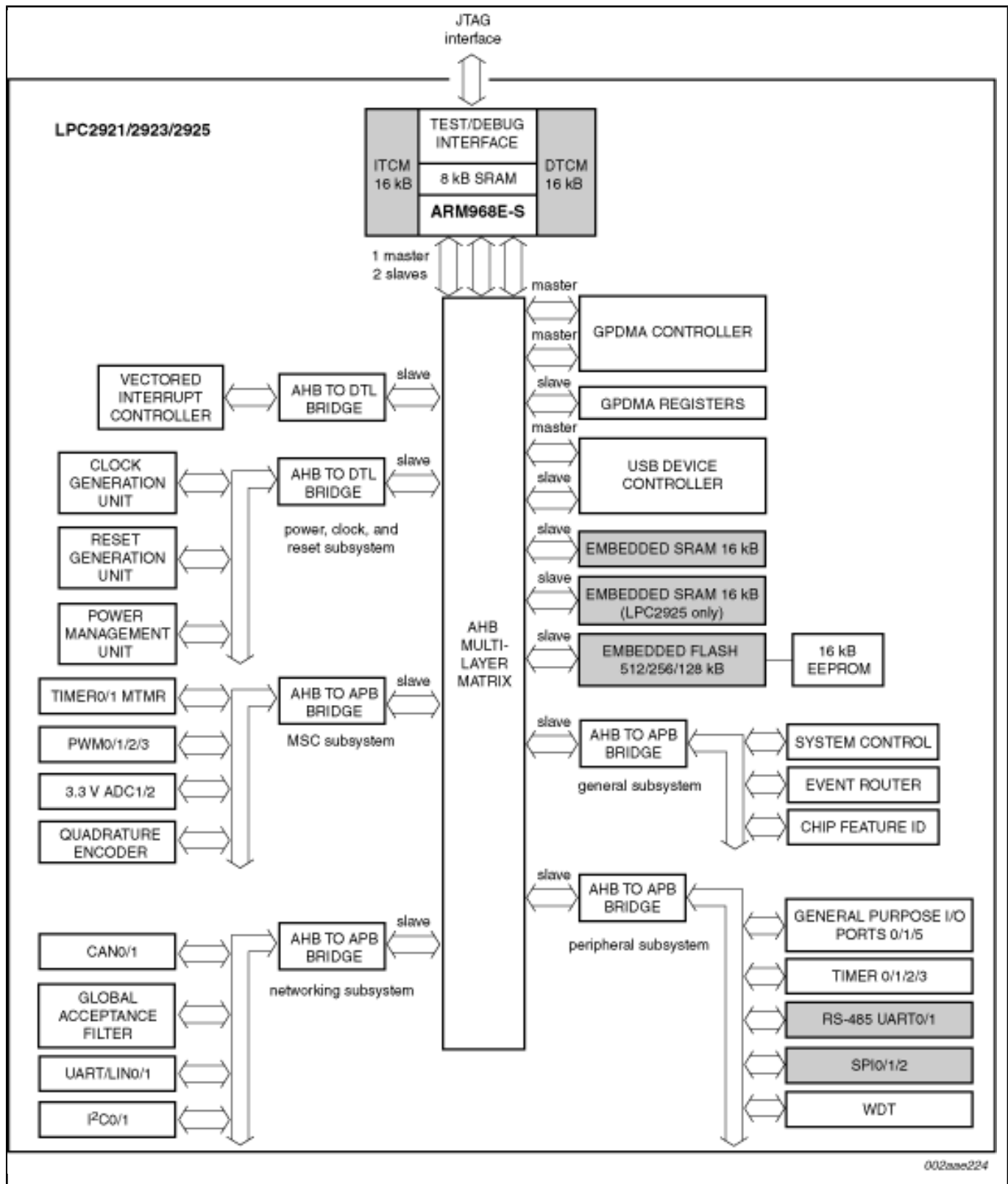
Arm9™微控制器，带CAN、LIN和USB

LPC2925FBD100

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LPC2921/2923/2925在单个芯片上结合了ARM968E-S CPU核，2个运行频率高达125 MHz的集成TCM块，全速USB 2.0设备控制器，CAN和LIN，高达40 kB的SRAM，高达512 kB的Flash存储器，2个10位ADC以及多个串行和并行接口，针对消费品、工业和通信市场。为优化系统功耗，LPC2921/2923/2925拥有非常灵活的时钟产生单元（CGU），它提供动态的时钟选通及校准。

Block diagram: LPC2921FBD100, LPC2923FBD100, LPC2925FBD100 Block Diagram



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