



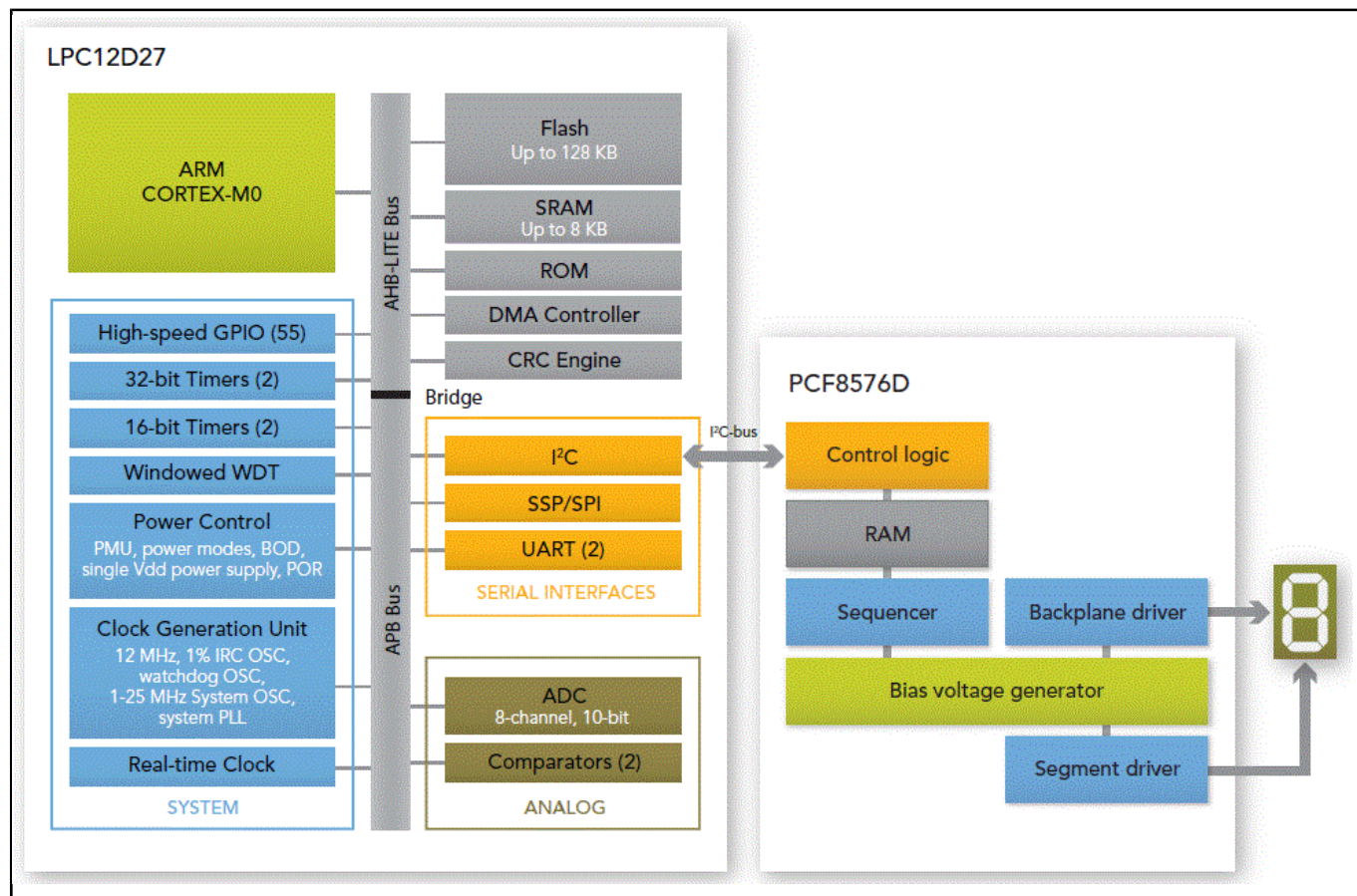
基于**ARM® Cortex®-M0**内核的耐用、可靠的**32**位微控制器(MCU)

LPC12D27FBD100

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LPC12D27FBD100是一种适合嵌入式应用的ARM Cortex-M0微控制器，具有较高的整合度和低功耗。ARM Cortex-M0是下一代的内核，它提供系统增强型特性，例如增强调试特性和提供更高级别的块集成支持。LPC12D27FBD100是双芯片模块，由1个LPC1227单芯片微控制器以及采用低成本100引脚封装的PCF8576D通用LCD驱动器组成。LCD驱动器提供40个段并支持1到4个背板。通过采用供显示专用的片上RAM使得显示开销最小化，片上RAM可自动递增寻址。

LPC12D27 Block Diagram Block Diagram



View additional information for [基于ARM® Cortex®-M0内核的耐用、可靠的32位微控制器\(MCU\)](#).

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