



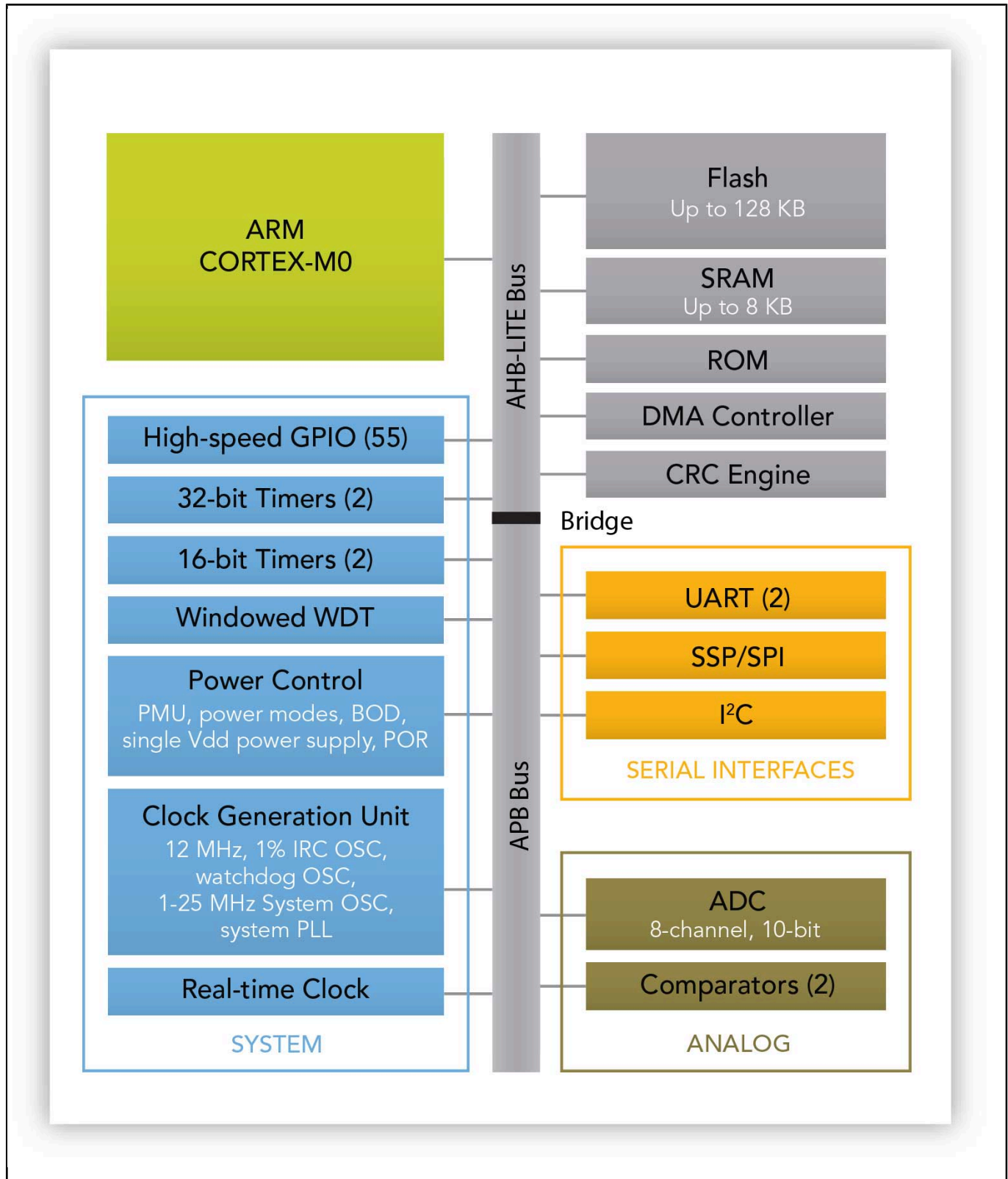
Cortex-M0，带有高达80 kB闪存、8 kB SRAM、RTC、比较器、10位ADC、CRC、DMA 等等

LPC1225FBD64

Last Updated: Dec 16, 2024

LPC1225FBD64是一款适合嵌入式应用的ARM Cortex-M0微控制器，具有较高的整合度和低功耗。LPC1225FBD64可在高达30 MHz的CPU频率下运行，包含高达80 kB的闪存和8 kB的数据存储器。LPC1225FBD64的补充外设包括DMA控制器、CRC引擎、一个超快速模式I2C接口、一个RTC、一个SSP/SPI接口、两个UART、四个通用定时器、一个10位ADC、两个比较器和多达55个通用I/O (GPIO)引脚。LPC1225FBD64以LQFP64封装形式提供。

LPC122x Block Diagram Block Diagram



View additional information for [Cortex-M0](#), 带有高达80 kB闪存、8 kB SRAM、RTC、比较器、10位ADC、CRC、DMA等等.

Note: The information on this document is subject to change without notice.

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