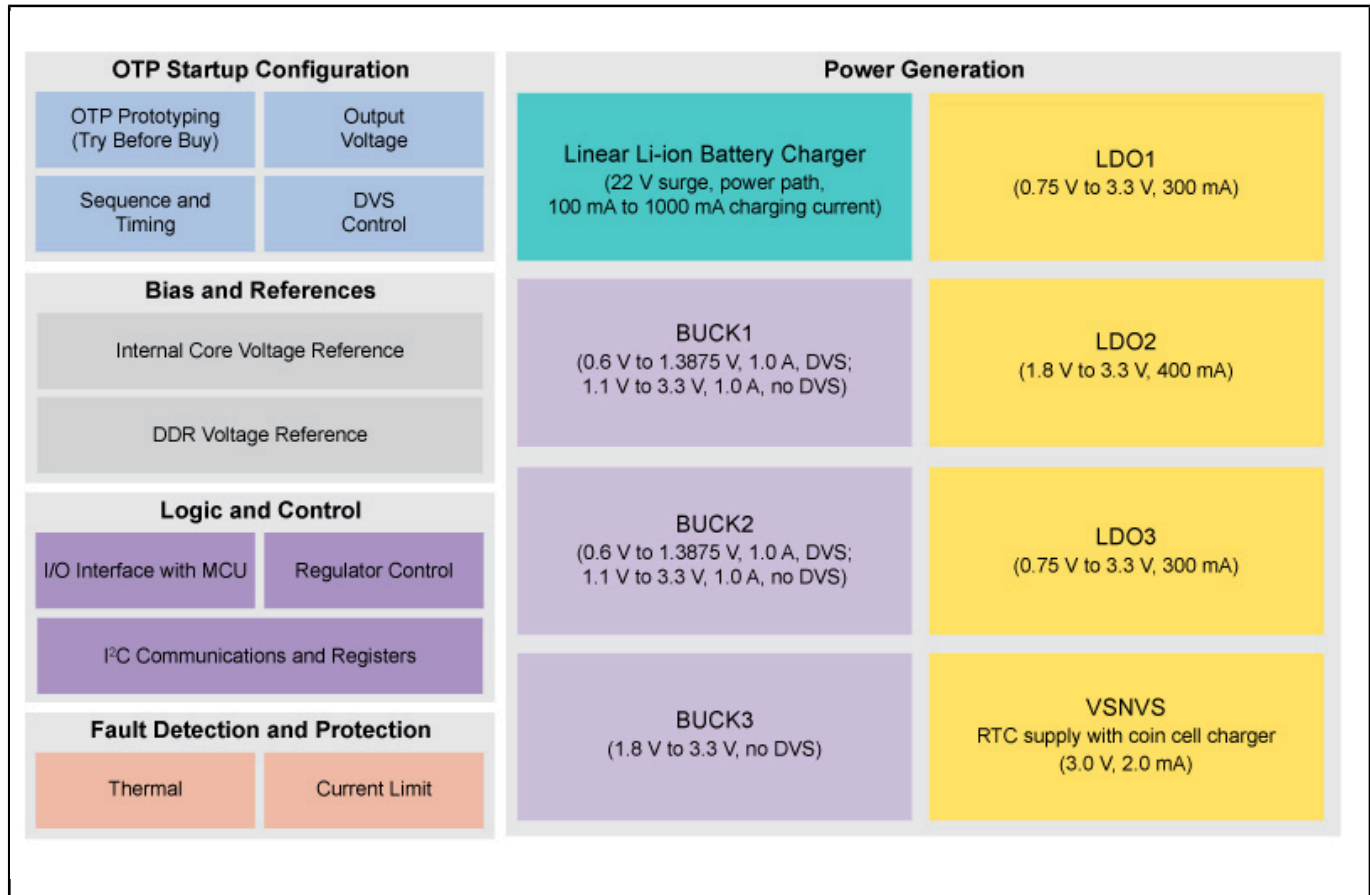# PF1550编程插座板

KITPF1550FRDMPGM

Last Updated: Sep 7, 2022

KITPF1550FRDMPGM编程插座板支持采用一次性可编程(OTP)来编程PF1550电源管理集成电路
(PMIC)。

该套件配有已安装的FRDM-KL25Z，并装有兼容的微码。FRDM–KL25Z的主要功能是控制插座板和
PC之间的通信。

- Block Diagram

OTP Startup Configuration

| OTP Prototyping (Try Before Buy) | Output Voltage |
| Sequence and Timing | DVS Control |

Bias and References

Internal Core Voltage Reference

DDR Voltage Reference

Logic and Control

| I/O Interface with MCU | Regulator Control |

I²C Communications and Registers

Fault Detection and Protection

| Thermal | Current Limit |

Power Generation

Linear Li-ion Battery Charger
(22 V surge, power path, 100 mA to 1000 mA charging current)

LDO1
(0.75 V to 3.3 V, 300 mA)

BUCK1
(0.6 V to 1.3875 V, 1.0 A, DVS; 1.1 V to 3.3 V, 1.0 A, no DVS)

LDO2
(1.8 V to 3.3 V, 400 mA)

BUCK2
(0.6 V to 1.3875 V, 1.0 A, DVS; 1.1 V to 3.3 V, 1.0 A, no DVS)

LDO3
(0.75 V to 3.3 V, 300 mA)

BUCK3
(1.8 V to 3.3 V, no DVS)

VSNVS
RTC supply with coin cell charger
(3.0 V, 2.0 mA)

View additional information for PF1550编程插座板.

Note: The information on this document is subject to change without notice.